

APT8011JFLL

800V 51A 0.110Ω

POWER MOS 7™

FREDFET

Power MOS 7^{TM} is a new generation of low loss, high voltage, N-Channel enhancement mode power MOSFETS. Both conduction and switching losses are addressed with Power MOS 7^{TM} by significantly lowering $R_{\text{DS(ON)}}$ and Q_g . Power MOS 7^{TM} combines lower conduction and switching losses along with exceptionally fast switching speeds inherent with APT's patented metal gate structure.



• Lower Miller Capacitance

• Lower Gate Charge, Qg

Increased Power Dissipation

Easier To Drive

Popular SOT-227 Package

• FAST RECOVERY BODY DIODE





MAXIMUM RATINGS

All Ratings: $T_C = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	APT8011JFLL	UNIT	
V _{DSS}	Drain-Source Voltage	800	Volts	
I _D	Continuous Drain Current @ T _C = 25°C	51	Amne	
I _{DM}	Pulsed Drain Current ①	204	Amps	
V _{GS}	Gate-Source Voltage Continuous	±30	Valta	
V _{GSM}	Gate-Source Voltage Transient	±40	Volts	
P _D	Total Power Dissipation @ T _C = 25°C	690	Watts	
, D	Linear Derating Factor	5.52	W/°C	
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C	
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300] [
I _{AR}	Avalanche Current (Repetitive and Non-Repetitive)	51	Amps	
E _{AR}	Repetitive Avalanche Energy ^①	50	m	
E _{AS}	Single Pulse Avalanche Energy ⁽⁴⁾	3600	mJ	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage $(V_{GS} = 0V, I_D = 250\mu\text{A})$	800			Volts
I _{D(on)}	On State Drain Current ② $(V_{DS} > I_{D(on)} \times R_{DS(on)} Max, V_{GS} = 10V)$	51			Amps
R _{DS(on)}	Drain-Source On-State Resistance ② (V _{GS} = 10V, 0.5 I _{D[Cont.]})			0.110	Ohms
I _{DSS}	Zero Gate Voltage Drain Current $(V_{DS} = V_{DSS}, V_{GS} = 0V)$			250	μA
	Zero Gate Voltage Drain Current $(V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_{C} = 125^{\circ}C)$			1000	
I _{GSS}	Gate-Source Leakage Current (V _{GS} = ±30V, V _{DS} = 0V)			±100	nA
V _{GS(th)}	Gate Threshold Voltage $(V_{DS} = V_{GS}, I_{D} = 2.5 \text{mA})$	3		5	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

APT Website - http://www.advancedpower.com

USA 405 S.W. Columbia Street EUROPE Chemin de Magret Bend, Oregon 97702-1035 F-33700 Merignac - France Phone: (541) 382-8028 Phone: (33) 5 57 92 15 15 FAX: (541) 388-0364

FAX: (33) 5 56 47 97 61

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C _{iss}	Input Capacitance	$V_{GS} = 0V$		10020		
C _{oss}	Output Capacitance	$V_{DS} = 25V$		1930		pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		317		
Q_g	Total Gate Charge ^③	V _{GS} = 10V		369		
Q_gs	Gate-Source Charge	$V_{DD} = 0.5 V_{DSS}$		48		nC
Q_{gd}	Gate-Drain ("Miller") Charge	I _D = I _D [Cont.] @ 25°C		209		
t _d (on)	Turn-on Delay Time	V _{GS} = 15V		23		
t _r	Rise Time	$V_{DD} = 0.5 V_{DSS}$		23		ns
t _d (off)	Turn-off Delay Time	I _D = I _D [Cont.] @ 25°C		83		115
t _f	Fall Time	$R_G = 0.6\Omega$		19		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions		MIN	TYP	MAX	UNIT
Is	Continuous Source Current (Body Diode)				51	
I _{SM}	Pulsed Source Current (Body Diode)	ed Source Current ^① (Body Diode)		204	Amps	
V_{SD}	Diode Forward Voltage \bigcirc (V _{GS} = 0V, I _S = -I _D [Cont.])				1.3	Volts
dv/ _{dt}	Peak Diode Recovery dv/dt 5				18	V/ns
4	Reverse Recovery Time	T _j = 25°C			560	
t _{rr}	$(I_S = -I_D [Cont.], \frac{di}{dt} = 100A/\mu s)$	T _j = 125°C			1600	ns
	Reverse Recovery Charge	T _j = 25°C		2.5		
Q _{rr}	$(I_S = -I_D [Cont.], di/_{dt} = 100A/\mu s)$	T _j = 125°C		17.7		μC
	Peak Recovery Current	T _j = 25°C		16.1		A
I _{RRM}	$(I_S = -I_D [Cont.], \frac{di}{dt} = 100A/\mu s)$	T _j = 125°C		36.2		Amps

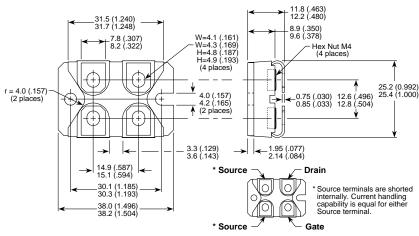
THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{ hetaJC}$	Junction to Case			0.18	
$R_{\theta JA}$	Junction to Ambient			40	°C/W

¹ Repetitive Rating: Pulse width limited by maximum junction temperature.

APT Reserves the right to change, without notice, the specifications and information contained herein.

SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters and (Inches)

² Pulse Test: Pulse width < 380 µs, Duty Cycle < 2%

³ See MIL-STD-750 Method 3471

 $[\]textcircled{4}$ Starting T_j = +25°C, L = 2.77mH, R_G = 25 Ω , Peak I_L = 51A 5 dv/_{dt} numbers reflect the limitations of the test circuit rather than the device itself. $I_S \le \neg I_{D[Cont.]}$ $dI/_{dt} \le 700 A/\mu s$ $V_R \le V_{DSS}$ $T_J \le 150^{\circ} C$