

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62783APA

8CH HIGH-VOLTAGE SOURCE DRIVER

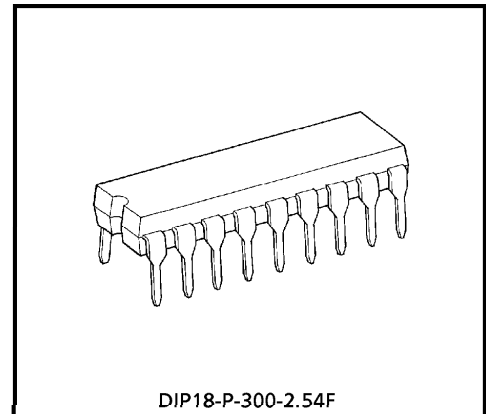
The TD62783APA is comprised of eight source current transistor array.

These drivers are specifically designed for fluorescent display applications.

Applications include relay, hammer and lamp drivers.

FEATURES

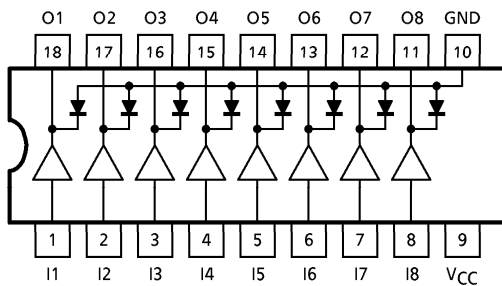
- High output voltage type-APA : $V_{CE(SUS)} = 50V$ (Min.)
- Output current (single output) : $I_{OUT} = -500mA / ch$ (Max.)
- Output clamp diodes
- Single supply voltage
- Input compatible with TTL, 5V CMOS
- Package type-APA : DIP-18 pin



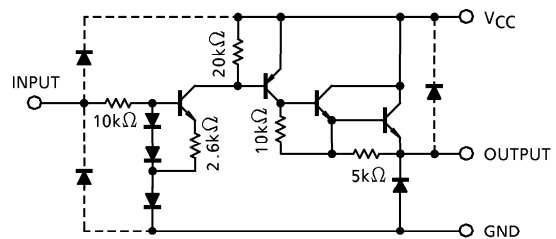
Weight : 1.478g (Typ.)

TYPE	DESIGNATION
TD62783APA	TTL, 5V CMOS

PIN CONNECTION (TOP VIEW)



SCHEMATICS (EACH DRIVER)



(Note) The input and output parasitic diodes cannot be used as clamp diodes.

961001EBA2

- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.
- The products described in this document are subject to foreign exchange and foreign trade control laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{CC}	50	V
Output Current	I _{OUT}	- 500	mA / ch
Input Voltage	V _{IN}	15	V
Clamp Diode Reverse Voltage	V _R	50	V
Clamp Diode Forward Current	I _F	500	mA
Power Dissipation	P _D (Note)	1.47	W
Operating Temperature	T _{opr}	- 40~85	°C
Storage Temperature	T _{stg}	- 55~150	°C

(Note) Delated above 25°C in the proportion of 11.7mW/°C.

RECOMMENDED OPERATING CONDITIONS (Ta = - 40~85°C)

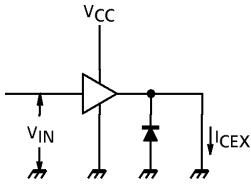
CHARACTERISTIC	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Supply Voltage	V _{CC}	—	—	—	50	V	
Output Current	I _{OUT}	T _{pw} = 25ms, Duty = 8% 8 Circuits	—	—	- 400	mA / ch	
		T _{pw} = 25ms, Duty = 25% 8 Circuits	—	—	- 200		
Input Voltage	V _{IN}	—	—	—	12	V	
Input Voltage	Output On	V _{IN} (ON)	—	2.0	5.0	15	V
	Output Off	V _{IN} (OFF)	—	0	—	0.8	V
Clamp Diode Reverse Voltage	V _R	—	—	—	50	V	
Clamp Diode Forward Current	I _F	—	—	—	400	mA	
Power Dissipation	P _D	—	—	—	0.52	W	

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

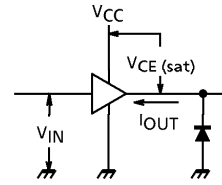
CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Leakage Current	I _{CEX}	1	V _{CC} = V _{CC} MAX., V _{IN} = 0.4V Ta = 25°C	—	—	100	μA
Output Saturation Voltage	V _{CE} (sat)	2	V _{IN} = V _{IN} (ON), I _{OUT} = - 350mA	—	—	2.0	V
			V _{IN} = V _{IN} (ON), I _{OUT} = - 225mA	—	—	1.9	
			V _{IN} = V _{IN} (ON), I _{OUT} = - 100mA	—	—	1.8	
Input Current	I _{IN} (ON)	3	V _{IN} = 2.4V	—	36	52	μA
			V _{IN} = 3.85V	—	180	260	
Input Voltage	V _{IN} (ON)	4	V _{CE} = 2.0V, I _{OUT} = - 350mA	—	—	2.0	V
	V _{IN} (OFF)		I _{OUT} = - 500μA	0.8	—	—	
Supply Current	I _{CC} (ON)	3	V _{IN} = V _{IN} (ON), V _{CC} = 50V	—	—	2.5	mA / ch
Clamp Diode Leakage Current	I _R	5	V _R = 50V	—	—	50	μA
Clamp Diode Forward Voltage	V _F	6	I _F = 350mA	—	—	2.0	V
Turn-On Delay	t _{ON}	7	V _{CC} = V _{CC} MAX., R _L = 125Ω C _L = 15pF	—	0.15	—	μs
Turn-Off Delay	t _{OFF}			—	1.8	—	

TEST CIRCUIT

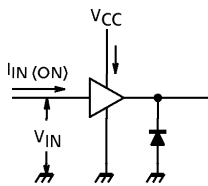
1. I_{CEX}



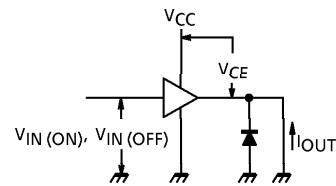
2. $V_{CE(sat)}$



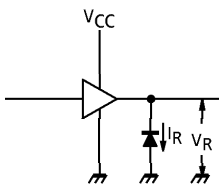
3. $I_{IN(ON)}$, I_{CC}



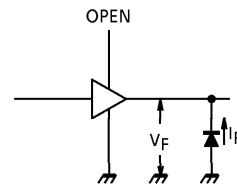
4. $V_{IN(ON)}$, $V_{IN(OFF)}$



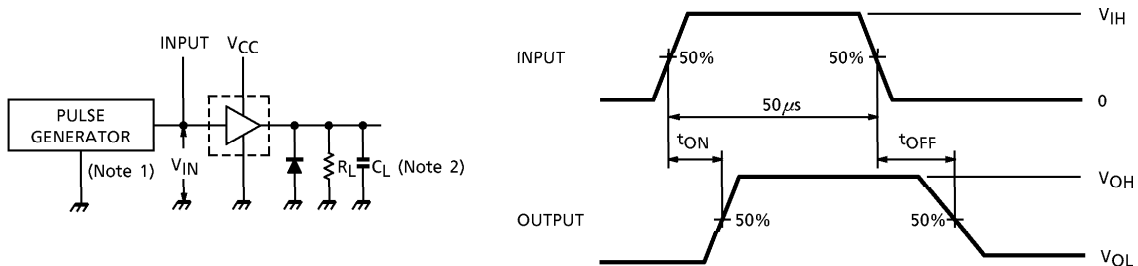
5. I_R



6. V_F



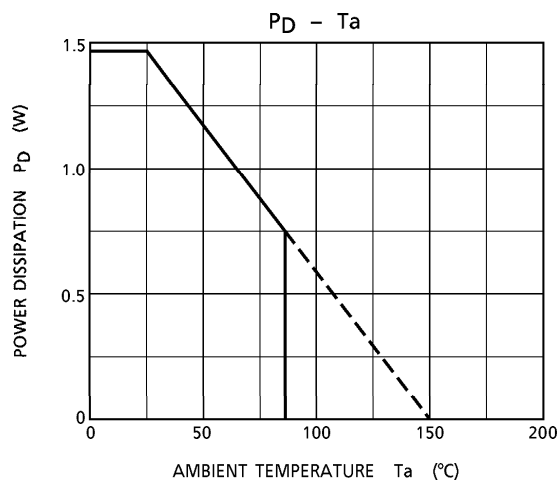
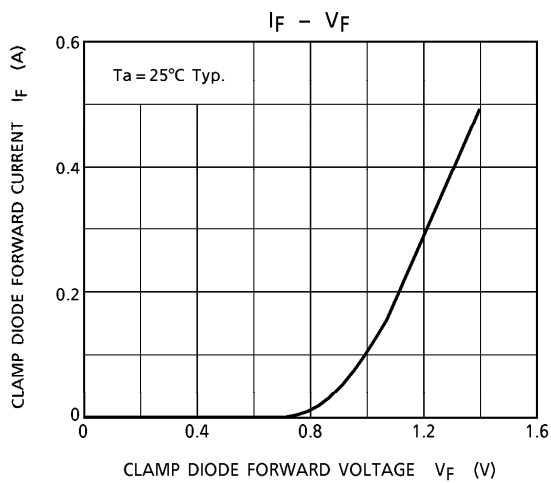
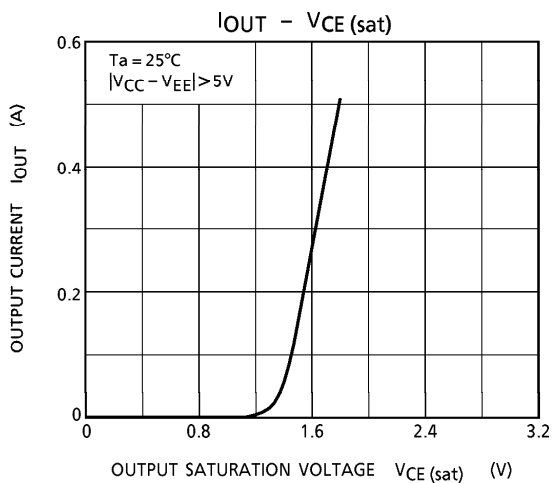
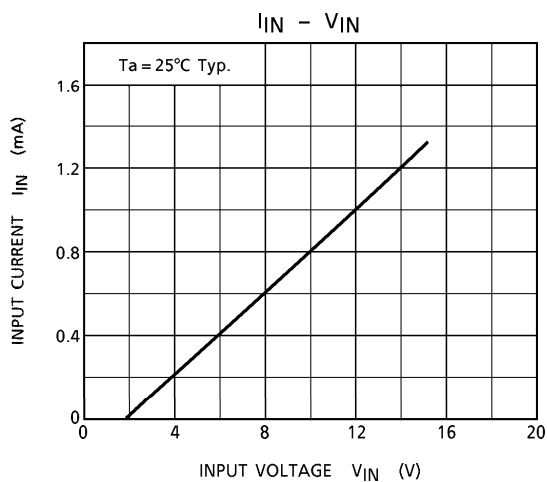
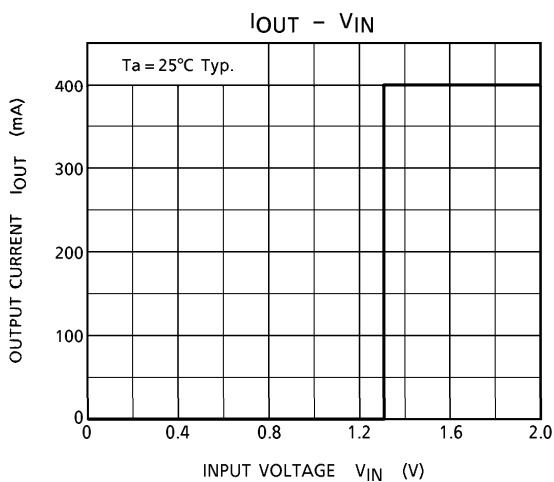
7. t_{ON} , t_{OFF}



(Note 1) Pulse Width $50\mu s$, Duty Cycle 10%
 Output Impedance 50Ω , $t_r \leq 5ns$, $t_f \leq 10ns$
 (Note 2) C_L includes probe and jig capacitance.

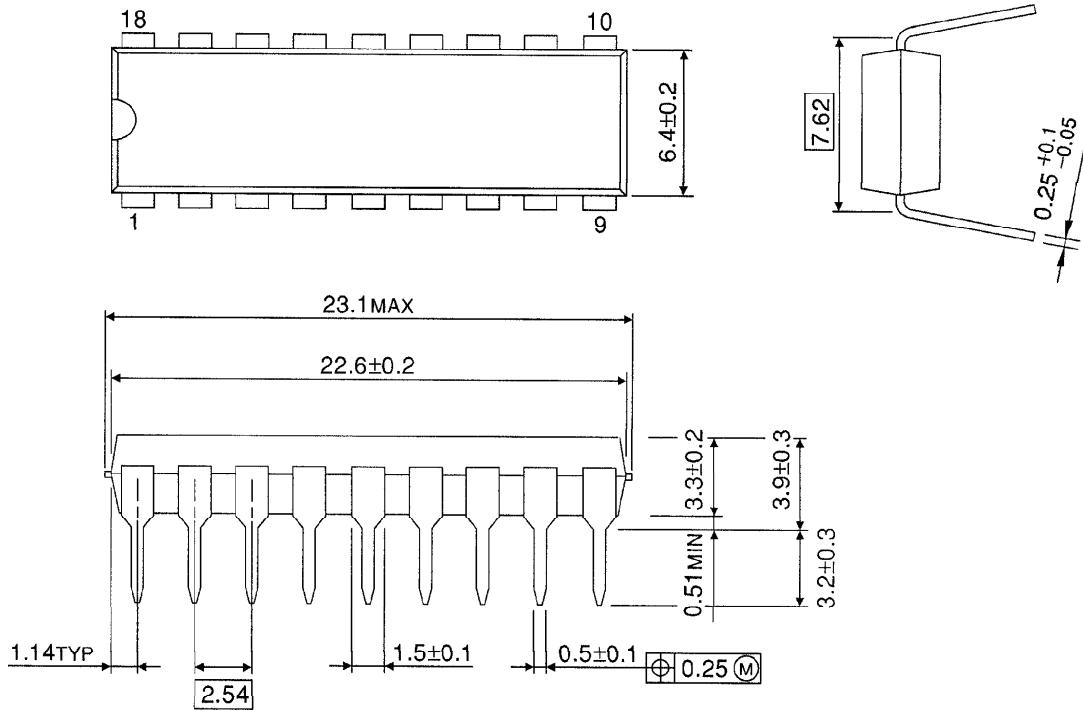
PRECAUTIONS for USING

Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



OUTLINE DRAWING
DIP18-P-300-2.54F

Unit : mm



Weight : 1.478g (Typ.)