

Adapting HC908GZ16 Stationery for HC908GZ8

GRGZ Service Pack Software Addendum

In the GRGZ Service Pack for CodeWarrior Development Studio for HC08 Microcontrollers, Special Edition Version 2.1.1, there are no GZ8 stationeries. To create a new project for the GZ8, follow the steps outlined below.

Launch the CodeWarrior IDE

Create a new project for GZ8 starting from GZ16 stationery

From main menu bar, select File > New **– New window appears**

New	د	×
Project File Object		
Empty Project HC08 Stationery Makefile Importer Wizard	Project name: GZ8_Project Location: C:\Projects\GZ8_Project Set Add to Project: Project: V	
	OK Cancel	

New Window



- □ Select HC08 Stationery
- **Enter a name for your project in** Project Name text box
- Click the OK button New project Window appears

New Project	×
Select project stationery:	
Project Stationery	
AB	•
. ⊞ Board Support	
GZ	
JK_JL	_
. Egacy	Ŧ
OK Cancel	

New Project Dialog Window

- Click + to display the GZ Stationery
- Click + to display the GZ16 stationery
- □ If you want to create a project using Absolute Assembly, proceed to page 5
- □ Select Assembly, C, C and Assembly, or C++ listed below GZ16

New Project	<				
Select project stationery:					
Project Stationery]				
I ⊞ GP_GT					
⊟ GZ	1				
Ė- GZ16	L				
- Absolute Assembly	L				
- Assembly	L				
- C and Assembly					
C++					
I ⊞… JK_JL	1				
	1				
OK Cancel					
	4				
GZ16 stationery					

Click the OK button





Project (GZ8_Project.mcp) Window

- **u** You have successfully created a project for the GZ16
- □ You must now change the memory mapping in the parameter file to use the GZ8 flash area
- **Click + to display the files in the Prm folder**

GZ8_Project.mcp				_	
🙌 P&E PEDebug FCS-ICS-I 💌 🔢 😽 <	🖉 🖕				
Files Link Order Targets					
🖌 File	Code	Data	9	*	<u>à</u>
👔 readme.txt	n/a	n/a	٠		
🛭 🖝 🛅 Sources	0	0	•	٠	
🕀 🧰 Debugger Cmd Files	0	0	+		
🖋 🕀 🧰 Startup Code	0	0	٠	٠	
🖌 🖂 🗁 Prm	0	0			
💉 📲 burner.bbl	n/a	n/a	٠		
💉 🚽 📲 default.prm	n/a	n/a	٠		
mon08_ram.prm	n/a	n/a			
🖌 🛩 🕀 🧰 Libs	0	0	٠	٠	
🕀 🧰 Debugger Project File	0	0			
					-
24 files	0	0			_//

The .prm File Selected

Double-click the .prm file for the target you're using (default.prm in this case)

Semiconductor, Inc.



D Edit the ROM memory section for 8k flash instead of 16k flash



default.prm Before Editing

□ Change 0xC000 to 0xE000, which is the lower address of the flash area in the GZ8

📾 default.prm	- D ×
NAMES END	므
SECTIONS Z_RAM = READ_WRITE 0x0040 TO 0x00FF; RAM = READ_WRITE 0x0100 TO 0x043F; ROM = READ_ONLY DEBOOD TO 0xFDFF; END	<u> </u>
PLACEMENT DEFAULT_ROM, ROM_VAR, STRINGS INTO ROM; DEFAULT_RAM INTO RAM; ZEROPAGE, MY_ZEROPAGE INTO Z_RAM; END	
STACKSIZE 0x50	
VECTOR 0 _Startup	
	-
Line 6 Col 31	• //

default.prm After Editing

- □ Save this file
- □ You're now ready to develop code for the GZ8

Freescale Semiconductor, Inc.



Technical Note

Create a new absolute assembly project for GZ8 starting from GZ16 Stationery

- □ Follow the steps on pages 1 and 2
- Select Absolute Assembly listed below GZ16



GZ16 Absolute Assembly stationery

Click the OK button

GZ8_Project.mcp			_ 0	×
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Files Link Order Targets				
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📓 readme.txt	n/a	n/a	•]	-
🛛 🖋 🕀 🧰 Sources	0	0	• • 1	-
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🛩 🕀 🧰 Prm	0	0	• 1	∎ 🗐
💷 💭 Debugger Project File	0	0	<u></u>	-1
20 files	0	0		

Project (GZ8_Project.mcp) Window

- □ You have successfully created an absolute assembly project for GZ16
- **u** You must now change the assembly source file ROM definition to use the GZ8 flash area



Click + to display the files in the Sources folder



The .asm File Selected

- Double-click the .asm file for the target you're using (main.asm in this case)
- Edit the definition of RomStart to reflect the lower address for the GZ8 flash

В	main.asm	
đ	▶ • {} • M. • = • □ • Path: C:\Projects\GZ8_Project\sources\main.asm	\diamond
	XDEF main, Entry, AtoD_ISR, T_ISR ABSENTRY Entry Include 'gz_registers.inc'	-
	RamStartEQU \$0040RomStartEQU \$C000ADC_ChannelEQU \$05ADC_ENABLE_INTEQU \$40; in the ADC status/control register	
	;DEFAULT_RAM: SECTION SHORT ORG RamStart temp_long ds 4 temp_word ds 2 temp_byte ds 1 Timeout1 ds 1 ; Allows three timeout routines to be called Timeout2 ds 1 ; can run for up to ~ 1/2 second. Timeout3 ds 1	l ea
	;DEFAULT_ROM: SECTION ORG RomStart	
	<pre>* Init_SCI - Turns on the asyncronous communications port * * for "transmitting only" at 9600 baud 8N1. *</pre>	-
Li	ne 1 Col 1 🗍 📢	

main.asm Before Editing



Change "\$C000" to "\$E000", which is the lower address of the flash area in the GZ8

🛯 main.asm _ 🗆 🗙 , 📲 🔸 🕅 🔹 🖬 🔹 🖬 🔹 Path: C:\Projects\GZ8_Project\sources\main.asm 8 XDEF main, Entry, AtoD_ISR, T_ISR
ABSENTRY Entry
Include 'gz_registers.inc' ٠ RamStart EQU \$0040 EQU SECCO EQU \$05 RomStart ADC_Channel EQU \$05 ADC_ENABLE_INT EQU \$40 ; Bit mask for interrupt enable bit ; in the ADC status/control register ; DEFAULT_RAM : SECTION SHORT ORG RamStart temp_long ds 4 temp_word ds 2 temp_byte ds 1 Timeout1 ds 1 Timeout2 ds 1 Timeout3 ds 1 ; Allows three timeout routines to be called est; can run for up to \simeq 1/2 second. ; DEFAULT_ROM : SECTION ORG RomStart * Init_SCI - Turns on the asyncronous communications port * for "transmitting only" at 9600 baud 8N1. × × Line 6 Col 25 🔰 🖣 ۲

main.asm After Editing

- □ Save this file
- You're now ready to develop absolute assembly code for the GZ8



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