

REPETITIVE AVALANCHE AND dv/dt RATED HEXFET® TRANSISTOR

IRHN7450SE

N-CHANNEL SINGLE EVENT EFFECT (SEE) RAD HARD

500 Volt, 0.51Ω, (SEE) RAD HARD HEXFET

International Rectifier's (SEE) RAD HARD technology HEXFETs demonstrate virtual immunity to SEE failure. Additionally, under **identical** pre- and post-radiation test conditions, International Rectifier's RAD HARD HEXFETs retain **identical** electrical specifications up to 1×10^5 Rads (Si) total dose. No compensation in gate drive circuitry is required. These devices are also capable of surviving transient ionization pulses as high as 1×10^{12} Rads (Si)/Sec, and return to normal operation within a few microseconds. Since the SEE process utilizes International Rectifier's patented HEXFET technology, the user can expect the highest quality and reliability in the industry.

RAD HARD HEXFET transistors also feature all of the well-established advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and temperature stability of the electrical parameters.

They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers and high-energy pulse circuits in space and weapons environments.

Product Summary

| Part Number | BV _{DSS} | R _{DS(on)} | I _D |
|-------------|-------------------|---------------------|----------------|
| IRHN7450SE | 500V | 0.51Ω | 12A |

Features:

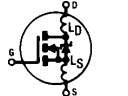
- Radiation Hardened up to 1×10^5 Rads (Si)
- Single Event Burnout (SEB) Hardened
- Single Event Gate Rupture (SEGR) Hardened
- Gamma Dot (Flash X-Ray) Hardened
- Neutron Tolerant
- Identical Pre- and Post-Electrical Test Conditions
- Repetitive Avalanche Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Surface Mount
- Lightweight

Absolute Maximum Ratings

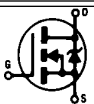
Pre-Radiation

| | Parameter | IRHN7450SE | Units |
|----------------------------------------------------------------|--------------------------------------------|---------------------|------------------|
| I _D @ V _{GS} = 12V, T _C = 25°C | Continuous Drain Current | 12 | A |
| I _D @ V _{GS} = 12V, T _C = 100°C | Continuous Drain Current | 7.0 | |
| I _{DM} | Pulsed Drain Current ^① | 48 | |
| P _D @ T _C = 25°C | Max. Power Dissipation | 150 | W |
| | Linear Derating Factor | 1.2 | W/K ^⑤ |
| V _{GS} | Gate-to-Source Voltage | ±20 | V |
| EAS | Single Pulse Avalanche Energy ^② | 500 | mJ |
| I _{AR} | Avalanche Current ^① | 12 | A |
| EAR | Repetitive Avalanche Energy ^① | 15 | mJ |
| dv/dt | Peak Diode Recovery dv/dt ^③ | 3.5 | V/ns |
| T _J | Operating Junction | -55 to 150 | °C |
| T _{STG} | Storage Temperature Range | | |
| | Package Mounting Surface Temperature | 300 (for 5 seconds) | |
| | Weight | 2.6 (typical) | g |

Electrical Characteristics @ T_j = 25°C (Unless Otherwise Specified)

| | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|-------------------------------------|----------------------------------------------|------|------|------|-------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| BV _{DSS} | Drain-to-Source Breakdown Voltage | 500 | — | — | V | V _{GS} = 0V, I _D = 1.0 mA |
| ΔBV _{DSS} /ΔT _J | Temperature Coefficient of Breakdown Voltage | — | 0.6 | — | V/°C | Reference to 25°C, I _D = 1.0 mA |
| RDS(on) | Static Drain-to-Source | — | — | 0.51 | Ω | V _{GS} = 12V, I _D = 7A V _{GS} = 12V, I _D = 12A ④ |
| | On-State Resistance | — | — | 0.57 | | |
| VGS(th) | Gate Threshold Voltage | 2.5 | — | 4.5 | V | V _{DS} = V _{GS} , I _D = 1.0 mA |
| g _{fs} | Forward Transconductance | 3 | — | — | S (②) | V _{DS} > 15V, I _{DS} = 7A ④ |
| IDSS | Zero Gate Voltage Drain Current | — | — | 50 | μA | V _{DS} = 0.8 x Max Rating, V _{GS} = 0V V _{DS} = 0.8 x Max Rating V _{GS} = 0V, T _J = 125°C |
| | | — | — | 250 | | |
| IGSS | Gate-to-Source Leakage Forward | — | — | 100 | nA | V _{GS} = 20V |
| IGSS | Gate-to-Source Leakage Reverse | — | — | -100 | nA | V _{GS} = -20V |
| Q _g | Total Gate Charge | — | — | 140 | nC | V _{GS} = 12V, I _D = 12A V _{DS} = Max. Rating x 0.5 |
| Q _{gs} | Gate-to-Source Charge | — | — | 50 | | |
| Q _{gd} | Gate-to-Drain ("Miller") Charge | — | — | 60 | | |
| t _{d(on)} | Turn-On Delay Time | — | — | 35 | ns | V _{DD} = 250V, I _D = 12A, R _G = 2.35Ω |
| t _r | Rise Time | — | — | 50 | | |
| t _{d(off)} | Turn-Off Delay Time | — | — | 100 | | |
| t _f | Fall Time | — | — | 60 | | |
| LD | Internal Drain Inductance | — | 2.0 | — | nH | <p>Measured from the drain lead, 6mm (0.25 in.) from package to center of die.</p> <p>Modified MOSFET symbol showing the internal inductances.</p>  |
| LS | Internal Source Inductance | — | 6.5 | — | | |
| C _{iss} | Input Capacitance | — | 4000 | — | pF | V _{GS} = 0V, V _{DS} = 25V f = 1.0 MHz |
| C _{oss} | Output Capacitance | — | 330 | — | | |
| C _{rss} | Reverse Transfer Capacitance | — | 52 | — | | |

Source-Drain Diode Ratings and Characteristics

| | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|-----------------|----------------------------------------|----------------------------------------------------------------------------------------------------------------------|------|------|-------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| I _S | Continuous Source Current (Body Diode) | — | — | 12 | A | Modified MOSFET symbol showing the integral reverse p-n junction rectifier.  |
| I _{SM} | Pulse Source Current (Body Diode) ① | — | — | 48 | | |
| V _{SD} | Diode Forward Voltage | — | — | 1.6 | V | T _j = 25°C, I _S = 12A, V _{GS} = 0V ④ |
| t _{rr} | Reverse Recovery Time | — | — | 500 | ns | T _j = 25°C, I _F = 12A, di/dt ≤ 100A/μs V _{DD} ≤ 50V ④ |
| Q _{RR} | Reverse Recovery Charge | — | — | 16 | μC | |
| t _{on} | Forward Turn-On Time | Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L _S + L _D . | | | | |

Thermal Resistance

| | Parameter | Min. | Typ. | Max. | Units | Test Conditions |
|----------------------|----------------------|------|------|------|------------------|------------------------------------|
| R _{thJC} | Junction-to-Case | — | — | 0.83 | K/W ^⑤ | soldered to a copper-clad PC board |
| R _{thJ-PCB} | Junction-to-PC board | — | TBD | — | | |

Radiation Performance of Rad Hard HEXFETs

International Rectifier Radiation Hardened HEXFETs are tested to verify their hardness capability. The hardness assurance program at International Rectifier uses two radiation environments.

Every manufacturing lot is tested in a low dose rate (total dose) environment per MIL-STD-750, test method 1019. International Rectifier has imposed a standard gate voltage of 12 volts per note 6 and a V_{DSS} bias condition equal to 80% of the device rated voltage per note 7. Pre- and post-radiation limits of the devices irradiated to 1×10^5 Rads (Si) are identical and are presented in Table 1. The values in Table 1 will be met for either of the two low dose rate test circuits that are used.

Both pre- and post-radiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison. It should be noted that at a radiation level of 1×10^5 Rads (Si), no change in limits are specified in DC parameters.

High dose rate testing may be done on a special request basis, using a dose rate up to 1×10^{12} Rads (Si)/Sec.

International Rectifier radiation hardened HEXFETs have been characterized in neutron and heavy ion Single Event Effects (SEE) environments. Single Event Effects characterization is shown in Table 3.

Table 1. Low Dose Rate ⑥ ⑦

| Parameter | | IRHN7450SE | | Units | Test Conditions ⑩ |
|---------------|-----------------------------------------------------|----------------|------|----------|------------------------------------------------------|
| | | 100K Rads (Si) | | | |
| | | min. | max. | | |
| BV_{DSS} | Drain-to-Source Breakdown Voltage | 500 | — | V | $V_{GS} = 0V, I_D = 1.0 \text{ mA}$ |
| $V_{GS(th)}$ | Gate Threshold Voltage ④ | 2.0 | 4.5 | | $V_{GS} = V_{DS}, I_D = 1.0 \text{ mA}$ |
| I_{GSS} | Gate-to-Source Leakage Forward | — | 100 | nA | $V_{GS} = 20V$ |
| I_{GSS} | Gate-to-Source Leakage Reverse | — | -100 | | $V_{GS} = -20V$ |
| I_{DSS} | Zero Gate Voltage Drain Current | — | 50 | μA | $V_{DS} = 0.8 \times \text{Max Rating}, V_{GS} = 0V$ |
| $R_{DS(on)1}$ | Static Drain-to-Source ④ On-State Resistance One | — | 0.51 | Ω | $V_{GS} = 12V, I_D = 7A$ |
| V_{SD} | Diode Forward Voltage ④ | — | 1.6 | V | $TC = 25^\circ C, I_S = 12A, V_{GS} = 0V$ |

Table 2. High Dose Rate ⑧

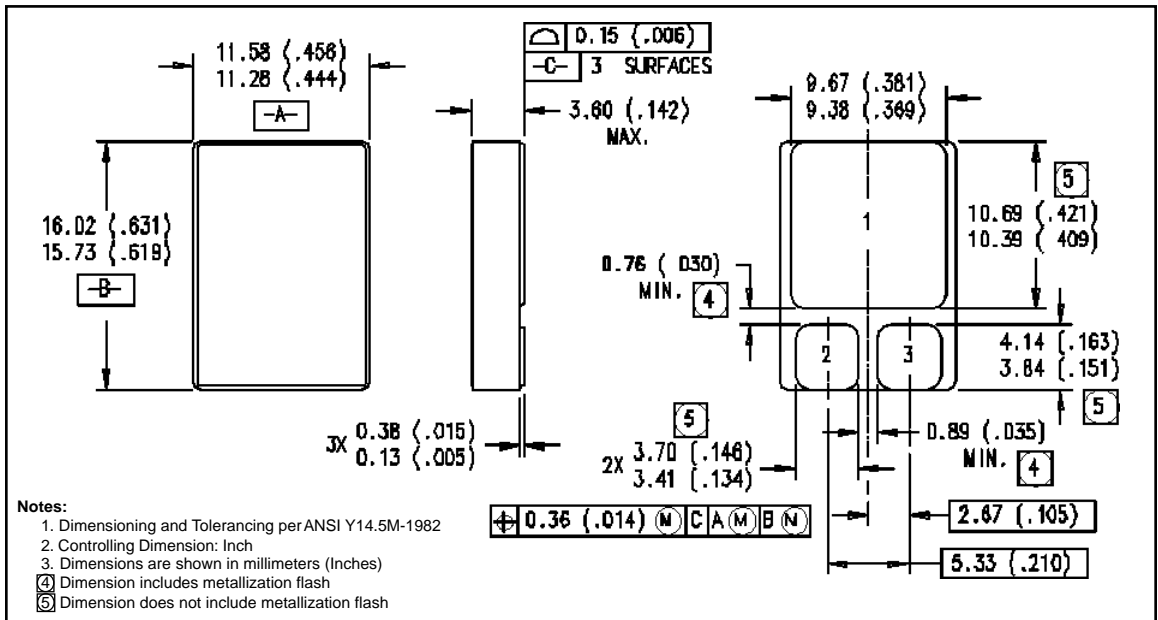
| Parameter | | 10 ¹¹ Rads (Si)/sec | | | 10 ¹² Rads (Si)/sec | | | Units | Test Conditions |
|-----------|-------------------------|--------------------------------|-----|------|--------------------------------|------|------|--------------|--------------------------------------------------|
| | | Min. | Typ | Max. | Min. | Typ. | Max. | | |
| V_{DSS} | Drain-to-Source Voltage | — | — | 400 | — | — | 400 | V | Applied drain-to-source voltage during gamma-dot |
| I_{PP} | | — | 8 | — | — | 8 | — | A | Peak radiation induced photo-current |
| di/dt | | — | 15 | — | — | 3 | — | A/ μ sec | Rate of rise of photo-current |
| L_1 | | — | 27 | — | — | 133 | — | μH | Circuit inductance required to limit di/dt |

Table 3. Single Event Effects ⑨

| Parameter | Typ. | Units | Ion | LET (Si) (MeV/mg/cm ²) | Fluence (ions/cm ²) | Range (μm) | V_{DS} Bias (V) | V_{GS} Bias (V) |
|------------|------|-------|-----|---------------------------------------|------------------------------------|----------------------|----------------------|----------------------|
| BV_{DSS} | 500 | V | Ni | 28 | 1×10^5 | ~35 | 400 | -5 |

- ① Repetitive Rating; Pulse width limited by maximum junction temperature. Refer to current HEXFET reliability report.
- ② @ $V_{DD} = 50V$, Starting $T_J = 25^\circ C$,
 $E_{AS} = [0.5 * L * (I_L^2) * [BV_{DSS}/(BV_{DSS}-V_{DD})]]$
 Peak $I_L = 12A$, $V_{GS} = 12V$, $25 \leq R_G \leq 200\Omega$
- ③ $I_{SD} \leq 12A$, $di/dt \leq 130 A/\mu s$,
 $V_{DD} \leq BV_{DSS}$, $T_J \leq 150^\circ C$
 Suggested $R_G = 2.35\Omega$
- ④ Pulse width $\leq 300 \mu s$; Duty Cycle $\leq 2\%$
- ⑤ $K/W = ^\circ C/W$
 $W/K = W/^\circ C$
- ⑥ **Total Dose Irradiation with V_{GS} Bias.**
 12 volt V_{GS} applied and $V_{DS} = 0$ during irradiation per MIL-STD-750, method 1019.
- ⑦ **Total Dose Irradiation with V_{DS} Bias.**
 $V_{DS} = 0.8$ rated BV_{DSS} (pre-radiation) applied and $V_{GS} = 0$ during irradiation per MIL-STD-750, method 1019.
- ⑧ This test is performed using a flash x-ray source operated in the e-beam mode (energy ~ 2.5 MeV), 30 nsec pulse.
- ⑨ Process characterized by independent laboratory.
- ⑩ All Pre-Radiation and Post-Radiation test conditions are **identical** to facilitate direct comparison for circuit applications.

Case Outline and Dimensions — SMD-1



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