

7-UNIT 150mA SOURCE TYPE DARLINGTON TRANSISTOR ARRAY

6249826 MITSUBISHI ELEK (LINEAR)

80C 09319 D T-43-25

DESCRIPTION

The M54580P, 7-channel source driver, consists of 7 PNP and 7 NPN transistors connected to form high current gain driver with PNP action.

FEATURES

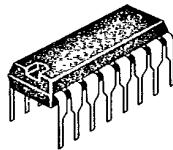
- High output sustaining voltage to 50V
- High output source current to 150mA
- Wide operating temperature range ($T_a = -20 \sim +75^\circ\text{C}$)

APPLICATION

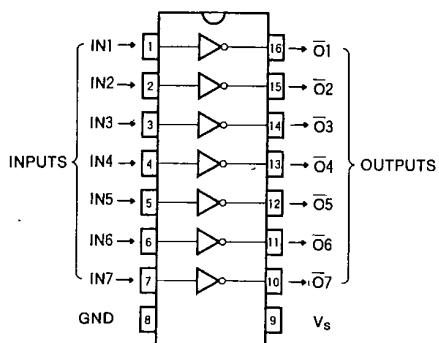
Relay and printer driver, LED, incandescent or fluorescent display driver, Interfacing for standard MOS/BIPOLAR logics

FUNCTION

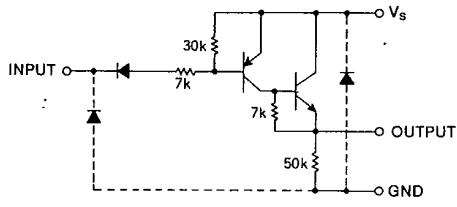
The M54580P is comprised of seven PNP-NPN darlington source driver pairs with a diode and 7 k Ω resistor in series to the input. The output is turned ON by switching the input low. Each output has 50k Ω pull-down resistor suitable for driving fluorescent displays. The outputs are capable of driving 100mA and are rated for operation with output voltage up to 50V.



16-pin molded plastic DIP

PIN CONFIGURATION (TOP VIEW)

Outline 16P4

CIRCUIT SCHEMATICUnit: Ω **ABSOLUTE MAXIMUM RATINGS** ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

| Symbol | Parameter | Conditions | Ratings | Unit |
|-----------|-------------------------------------|--------------------------|------------|------------------|
| V_s | Supply voltage | | 50 | V |
| V_{CEO} | Output sustaining voltage | Transistor OFF | -0.5 ~ +50 | V |
| V_i | Input voltage | | 0 ~ V_s | V |
| I_o | Output current | Transistor OFF | -150 | mA |
| P_d | Power dissipation | $T_a = 25^\circ\text{C}$ | 1.47 | W |
| T_{opr} | Operating ambient temperature range | | -20 ~ +75 | $^\circ\text{C}$ |
| T_{stg} | Storage temperature range | | -55 ~ +125 | $^\circ\text{C}$ |

MITSUBISHI BIPOLAR DIGITAL ICs

MITSUBISHI ELEK (LINEAR) 80 DE 6249826 0009320 2 M54580P

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RECOMMENDED OPERATIONAL CONDITIONS ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

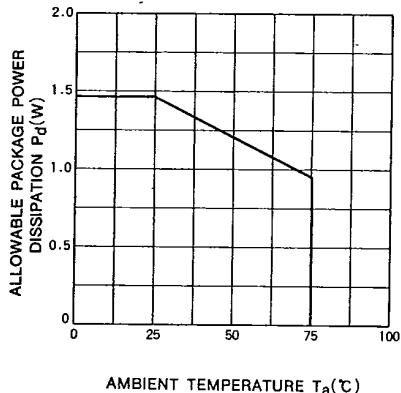
| Symbol | Parameter | Limits | | | Unit |
|----------|---------------------------------------|----------------------------------|-----------|-----------|------|
| | | Min | Typ | Max | |
| V_s | Supply voltage | 4 | | 50 | V |
| I_o | All outputs conducting simultaneously | | | | mA |
| | Percent duty cycle less than 85% | 0 | | -100 | |
| V_{IH} | All outputs conducting simultaneously | | | | V |
| | Percent duty cycle less than 100% | 0 | | -50 | |
| V_{IH} | "H" Input voltage | $I_o(\text{leak})=50\mu\text{A}$ | $V_s=0.4$ | V_s | |
| V_{IL} | "L" Input voltage | $I_o=-100\text{mA}$ | 0 | $V_s=3.2$ | V |

ELECTRICAL CHARACTERISTICS ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

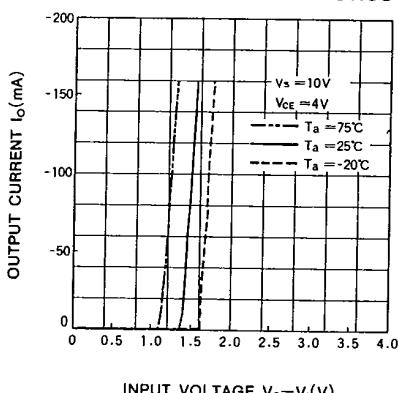
| Symbol | Parameter | Test conditions | | Limits | | | Unit |
|---------------|---------------------------|--|---|---------------|---------------|--|---------------|
| | | $I_{CE0} = 100\mu\text{A}$ | | 50 | | | |
| $V_{(BR)CEO}$ | Output sustaining voltage | | | | | | V |
| $V_{ce(sat)}$ | Output saturation voltage | $V_t = V_s - 3.2\text{V}$ | $I_o = -100\text{mA}$ $I_o = -50\text{mA}$ | 0.9 0.8 | 1.5 1.2 | | V |
| I_i | Input current | $V_t = V_s - 3.5\text{V}$ $V_t = V_s - 6\text{V}$ | | -0.3 -0.65 | -0.6 -0.95 | | mA |
| I_R | Input leakage current | $V_t = 40\text{V}$ | | | 100 | | μA |
| h_{FE} | DC forward current gain, | $V_{CE} = 4\text{V}$, $V_s = 10\text{V}$, $I_C = -100\text{mA}$, $T_a = 25^\circ\text{C}$ | | 800 | 3000 | | - |

TYPICAL CHARACTERISTICS

ALLOWABLE AVERAGE
POWER DISSIPATION



OUTPUT CURRENT
CHARACTERISTICS



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