

Structure : Silicon Monolithic Integrated Circuit

Product : 6ch Volume with 8ch input selector

Type : **BD3811K1**

Function :
 •6ch Master volume(0 to -103dB、MUTE、1dB/STEP)
 •Tone(-14dB to +14dB、2dB/STEP)、Bass boost(0 to 12dB、4dB/STEP)

○Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Rating	Unit
Power Supply voltage	VCC-VEE	15	V
Input voltage	VIN	VCC+0.3 to VEE-0.3	V
Power dissipation	Pd	1200※	mW
Operating temperature	Topr	-20 to +75	°C
Storage temperature	Tastg	-55 to +125	°C

※This value decreases 12mW/°C for Ta=25°C or more.

A standard board, 70 × 70 × 1.6mm, shall be mounted.

○Operating Voltage Range (Basic operation shall be available upon Ta=25°C.)

	Symbol	Range	Unit
Power supply (Positive)	VCC-GND	5 to 7.3	V
Power supply (Negative)	VEE-GND	-5 to -7.3	V

Status of this document

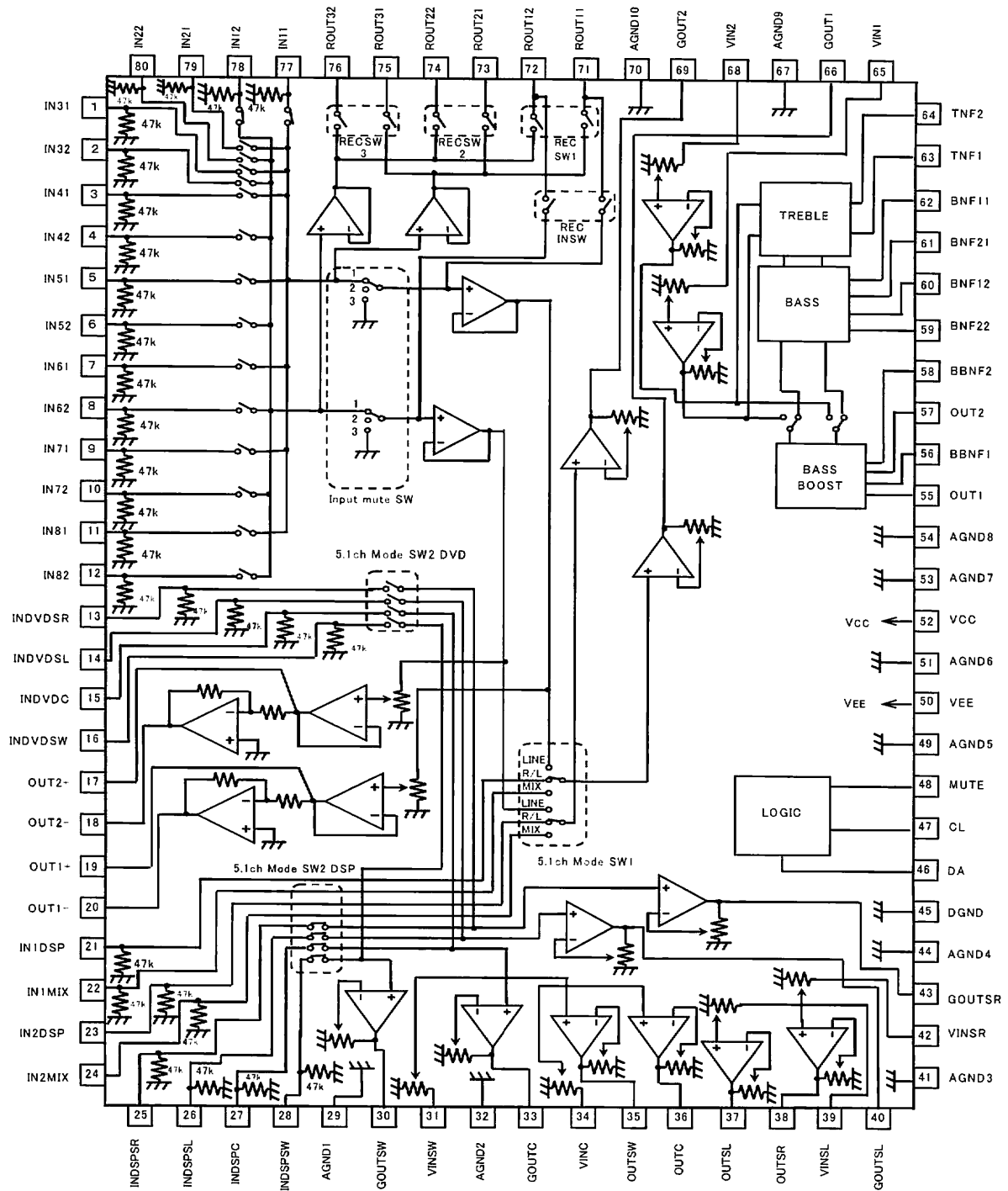
The Japanese version of this document is the formal specification.

A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document, formal version takes priority.

Rev.B

OBLOCK Diagram



○Terminal Number/ Terminal Name

Terminal Number	Terminal Name	Terminal Number	Terminal Name	Terminal Number	Terminal Name	Terminal Number	Terminal Name
1	IN31	21	IN1DSP	41	AGND3	61	BNF21
2	IN32	22	IN1MIX	42	VINSR	62	BNF11
3	IN41	23	IN2DSP	43	GOUTSR	63	TNF1
4	IN42	24	IN2MIX	44	AGND4	64	TNF2
5	IN51	25	INDSPSR	45	DGND	65	VIN1
6	IN52	26	INDSPSL	46	DA	66	GOUT1
7	IN61	27	INDSPC	47	CL	67	AGND9
8	IN62	28	INDSPSW	48	MUTE	68	VIN2
9	IN71	29	AGND1	49	AGND5	69	GOUT2
10	IN72	30	GOUTSW	50	VEE	70	AGND10
11	IN81	31	VINSW	51	AGND6	71	ROUT11
12	IN82	32	AGND2	52	VCC	72	ROUT12
13	INDVDSR	33	GOUTC	53	AGND7	73	ROUT21
14	INDVDSL	34	VINC	54	AGND8	74	ROUT22
15	INDVDC	35	OUTSW	55	OUT1	75	ROUT31
16	INDVDSW	36	OUTC	56	BBNF1	76	ROUT32
17	OUT2(+)	37	OUTSL	57	OUT2	77	IN11
18	OUT2(-)	38	OUTSR	58	BBNF2	78	IN12
19	OUT1(+)	39	VINSL	59	BNF22	79	IN21
20	OUT1(-)	40	GOUTSL	60	BNF12	80	IN22

○Cautions of use

(1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

(2) VEE potential

Make the VEE pin voltage such that it is the lowest voltage even when operating below it.

(3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation (Pd) in actual states of use.

(4) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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