## **DATA SHEET**



# MOS FIELD EFFECT TRANSISTOR

2SK2487

## SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

#### **DESCRIPTION**

The 2SK2487 is N-Channel MOS Field Effect Transistor designed for high voltage switching applications.

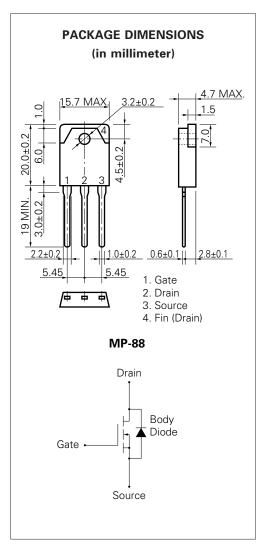
#### **FEATURES**

- Low On-Resistance RDS (on) = 1.6  $\Omega$  (VGS = 10 V, ID = 4.0 A)
- Low Ciss Ciss = 2 100 pF TYP.
- High Avalanche Capability Ratings

#### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage	VDSS	900	V
Gate to Source Voltage	Vgss	±30	V
Drain Current (DC)	ID (DC)	±8.0	Α
Drain Current (pulse)*	D (pulse)	±20	Α
Total Power Dissipation ( $T_c = 25$ °C)	P <sub>T1</sub>	140	W
Total Power Dissipation (T <sub>A</sub> = 25 °C)	P <sub>T2</sub>	3.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	T <sub>stg</sub> -5	5 to +150	°C
Single Avalanche Current**	las	8.0	Α
Single Avalanche Energy**	Eas	264	mJ

- \* PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %
- \*\* Starting T<sub>ch</sub> = 25 °C, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20 V  $\rightarrow$  0

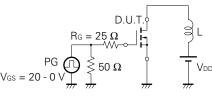


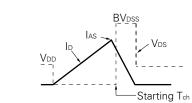


## **ELECTRICAL CHARACTERISTICS (TA = 25 °C)**

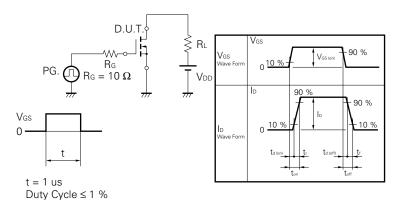
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	RDS (on)		1.1	1.6	Ω	Vgs = 10 V, ID = 4.0 A
Gate to Source Cutoff Voltage	VGS (off)	2.5		3.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance	yfs	3.0			S	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 4.0 A
Drain Leakage Current	IDSS			100	μΑ	V <sub>DS</sub> = V <sub>DSS</sub> , V <sub>GS</sub> = 0
Gate to Source Leakage Current	Igss			±100	nA	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Input Capacitance	Ciss		2 100		pF	V <sub>DS</sub> = 10 V
Output Capacitance	Coss		310		pF	V <sub>GS</sub> = 0
Reverse Transfer Capacitance	Crss		60		pF	f = 1 MHz
Turn-On Delay Time	td (on)		30		ns	ID = 4.0 A
Rise Time	tr		20		ns	V <sub>GS</sub> = 10 V
Turn-Off Delay Time	td (off)		130		ns	V <sub>DD</sub> = 150 V
Fall Time	tf		23		ns	$R_G = 10 \Omega$
Total Gate Charge	<b>Q</b> G		65		nC	ID = 8.0 A
Gate to Source Charge	Qgs		11		nC	V <sub>DD</sub> = 450 V
Gate to Drain Charge	Q <sub>GD</sub>		29		nC	V <sub>GS</sub> = 10 V
Body Diode Forward Voltage	V <sub>F</sub> (S-D)		1.0		V	IF = 8.0 A, VGS = 0
Reverse Recovery Time	trr		770		ns	IF = 8.0 A, VGS = 0
Reverse Recovery Charge	Qrr		5.0		μC	di/dt = 50 A/μs

### **Test Circuit 1 Avalanche Capability**

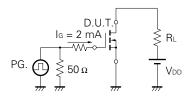




### **Test Circuit 2 Switching Time**



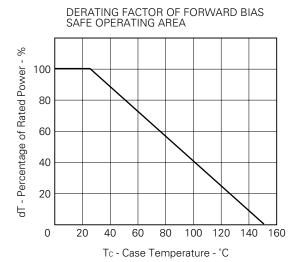
## **Test Circuit 3 Gate Charge**

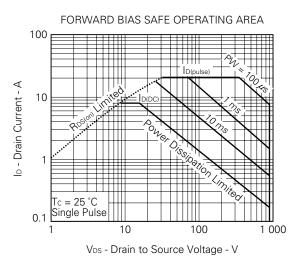


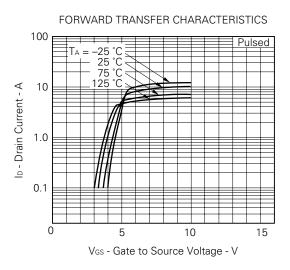
The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

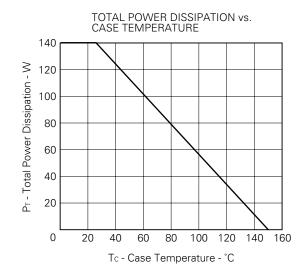
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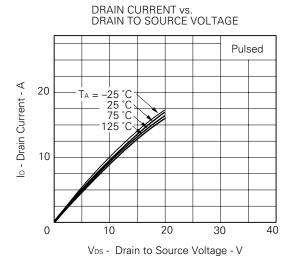
### TYPICAL CHARACTERISTICS (TA = 25 °C)





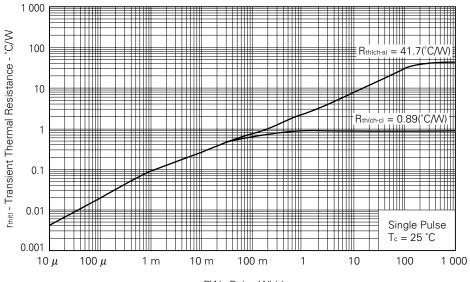






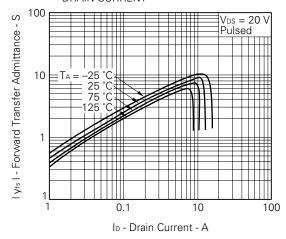


#### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

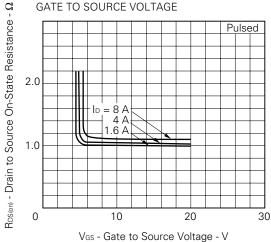


PW - Pulse Width - s

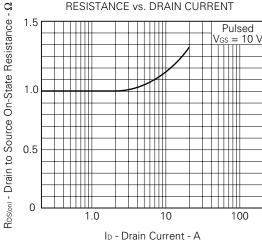
# FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



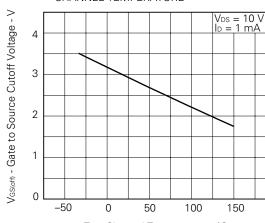
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



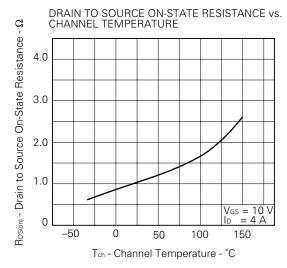
# DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

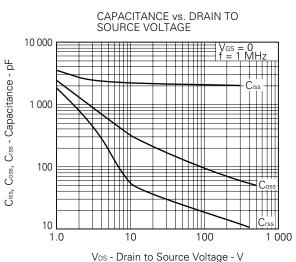


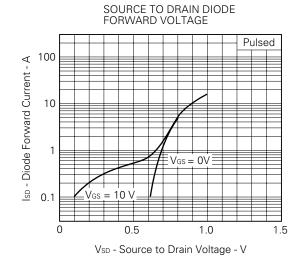
# GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

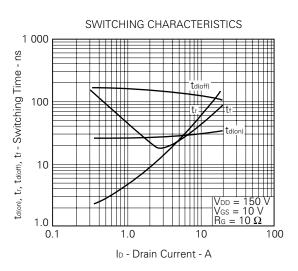


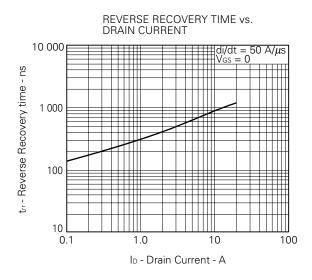
Tch - Channel Temperature - °C

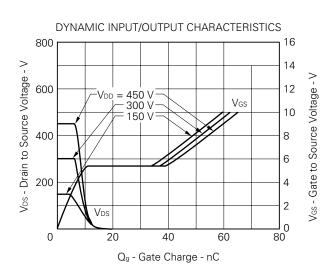




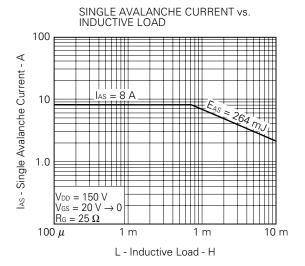


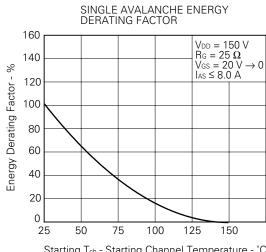














## REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

7

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