TOSHIBA TD62M3600F

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

TD62M3600F

3CH LOW SATURATION VOLTAGE SOURCE DRIVER

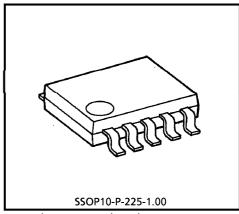
TD62M3600F is multi chip driver IC incorporates 3 low saturation voltage discrete PNP transistors which equipped bias resistor and fly-wheeling diode.

FEATURES

- Built-in fly-wheeling diode
- Built-in bias resistor : $R = 10k\Omega$ (Typ.)
- SSOP10 1mm pitch small package sealed
- Low saturation voltage

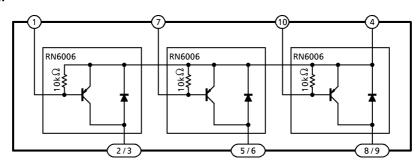
 $V_{CE (sat)} = 0.16V (Typ.)$ at $I_{O} = 1A$

 $V_{CE (sat)} = 0.28V (Typ.)$ at $I_{O} = 2A$

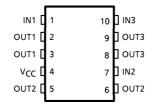


Weight: 0.10g (Typ.)

BLOCK DIAGRAM



PIN CONNECTION



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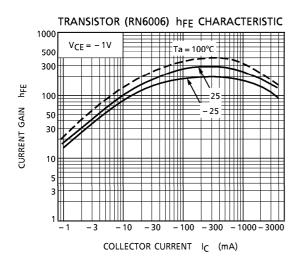
MAXIMUM RATINGS (Ta = 25°C)

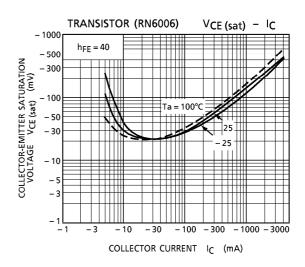
CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	Vcc	– 10	V	
Collector-Base Voltage	VCBO	– 10	V	
Collector-Emitter Voltage	VCEO	– 10	٧	
Emitter-Base Voltage	V _{EBO}	– 6	V	
Output Transistor Current	lo	- 2	A / ch	
	IO (PEAK)	-4 (Note 1)	A/CII	
Base Current	IB	- 0.4	Α	
base current	IB (PEAK)	-0.8 (Note 1)	^	
Diode Forward Current	l _F	– 2 (Note 2)	Α	
Power Dissipation	P _D	590	mW	
Junction Temperature	Tj	150	°C	
Operating Temperature	T _{opr}	- 40~85	°C	
Storage Temperature	T _{stg}	- 55∼150	°C	

(Note 1) T = 10ms Max. and maximum duty is less than 30% (Note 2) T = 10ms single pulse

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

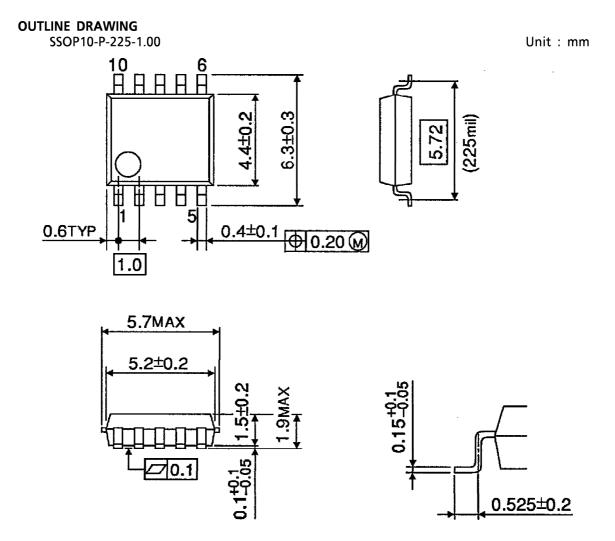
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Current Gain	h _{FE} (1)	_	$V_{CE} = 1V, I_{C} = 0.5A$	160	_	600	_
	h _{FE} (2)	_	$V_{CE} = 1V, I_{C} = 2.0A$	60	130	_	
Saturation Voltage	V ()	_	$I_C = 1A$, $I_B = 25mA$	_	0.13	0.25	V
	V _{CE} (sat)		I _C = 2A, I _B = 50mA	_	0.25	0.50	
Transition Frequency	f _T	-	$V_{CE} = 2V, I_{C} = 0.5A$	_	150	_	MHz
Leakage Current	lOL	_	V _{CC} = 10V	_	0	10	μΑ
Diode Forward Voltage V	\/-	_	I _F = 300mA	_	0.89	1.2	V
	V F		I _F = 450mA, 10ms	_	1.60	_	
Base-Emitter Resistance	R _{BE}	_	_	7	10	13	kΩ
Base-Emitter Forward Voltage	V _{BE}	_	V _{CE} = 1V, I _C = 2.0A	_	0.84	1.5	V





PRECAUTIONS for USING

Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



Weight: 0.10g (Typ.)