

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC
TA78L05S, TA78L07S, TA78L08S, TA78L09S
TA78L10S, TA78L12S, TA78L15S

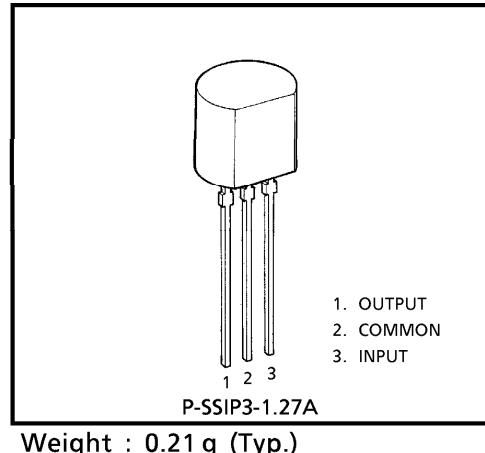
THREE TERMINAL POSITIVE VOLTAGE REGULATORS

5 V, 7 V, 8 V, 9 V, 10 V, 12 V, 15 V

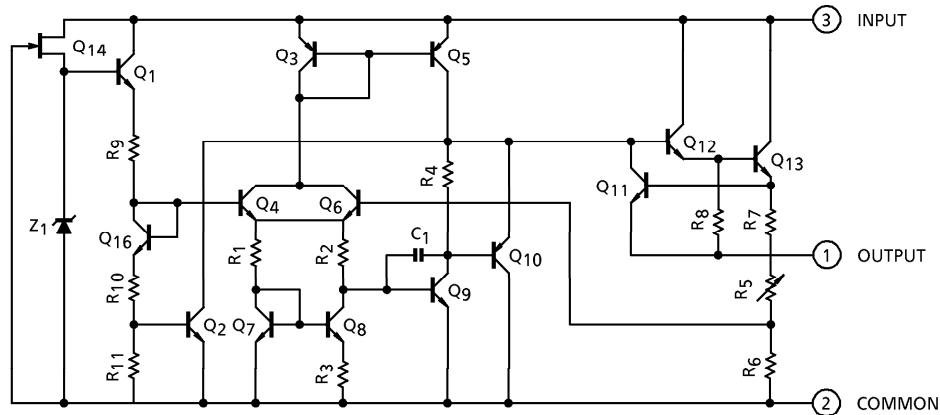
The TA78L \times S series of fixed voltage monolithic integrated circuit voltage regulators is designed for a wide range of applications.

FEATURES

- Suitable for TTL, C²MOS Power Supply
- Internal Short-Circuit Current Limiting
- Internal Thermal Overload Protection
- Maximum Output Current of 100 mA ($T_j = 25^\circ\text{C}$)
- TO-92 Package

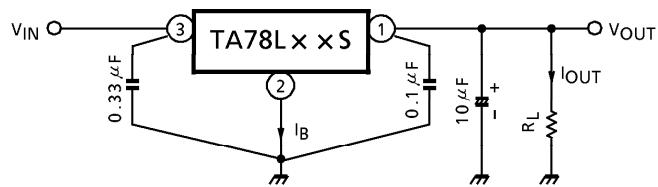
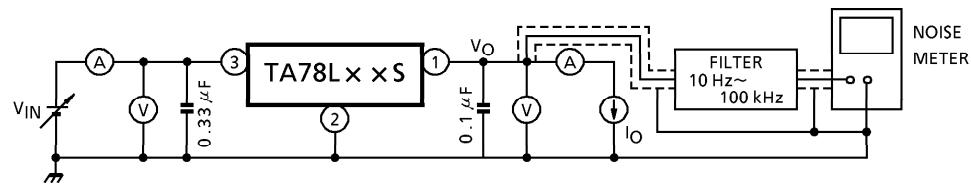
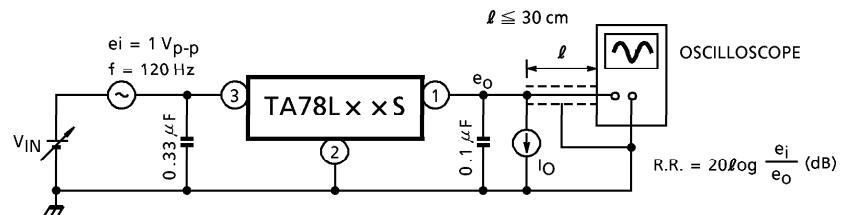


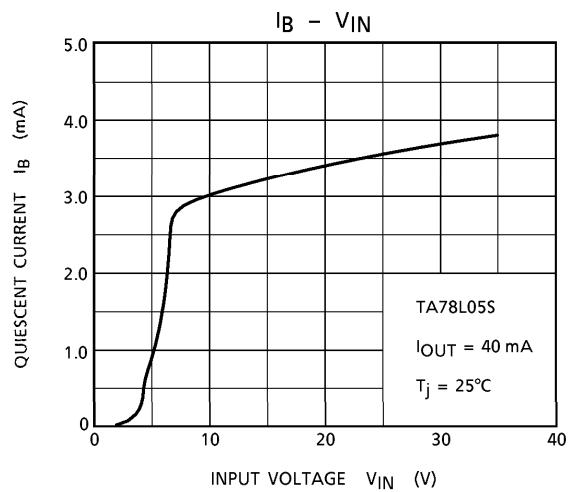
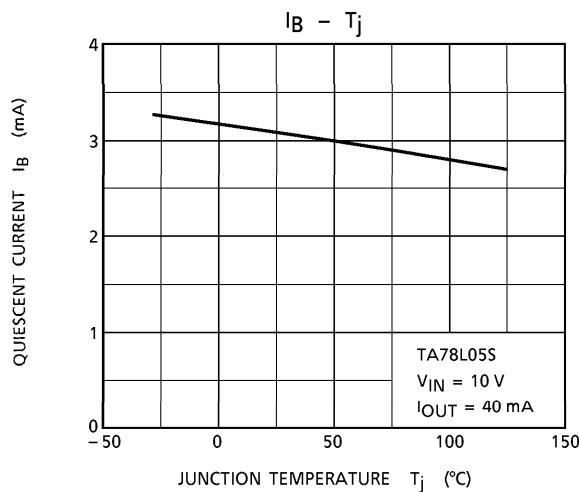
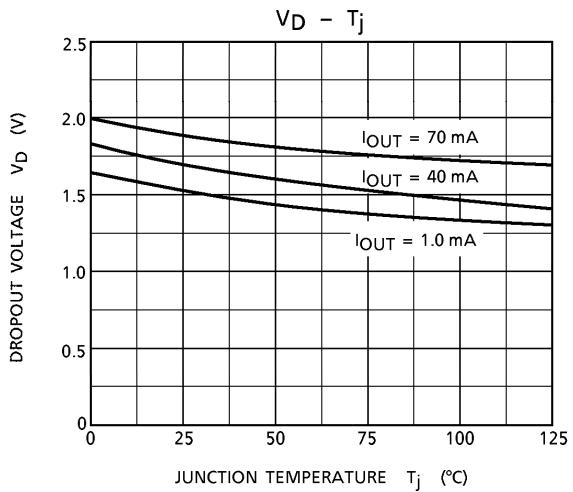
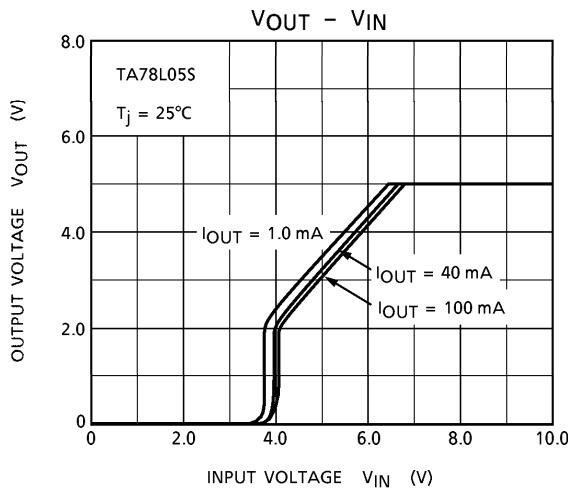
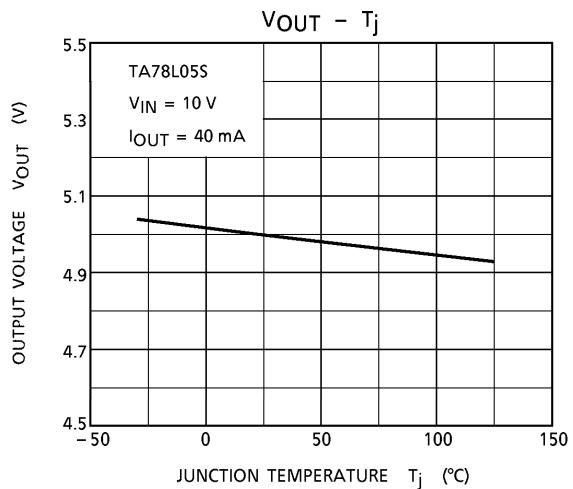
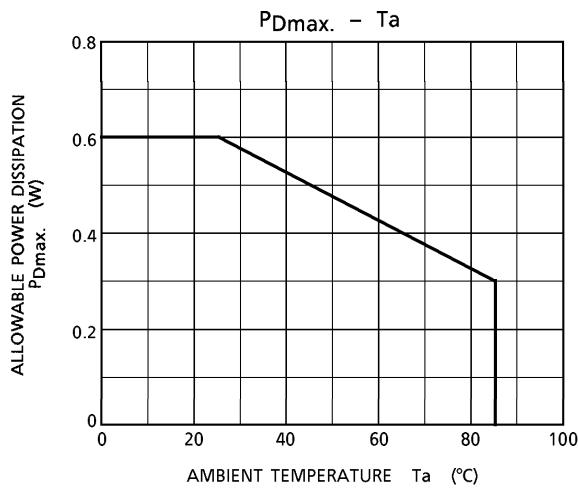
EQUIVALENT CIRCUIT

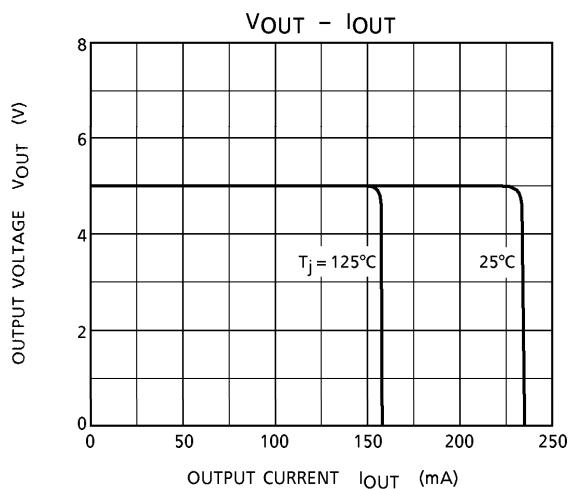
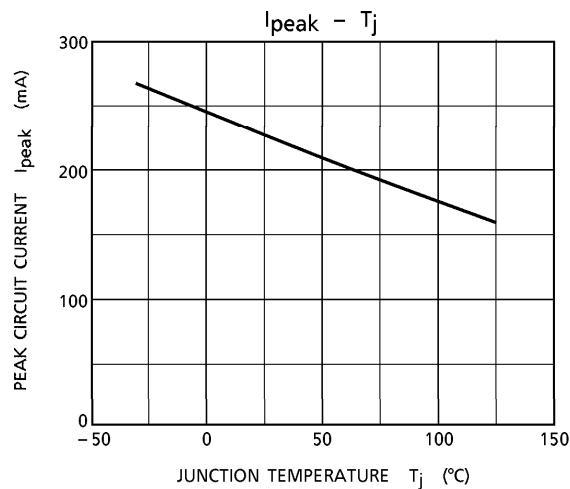


980910EBA1

- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

TEST CIRCUIT 1 / STANDARD APPLICATION**TEST CIRCUIT 2 V_{NO}****TEST CIRCUIT 3 R.R.**



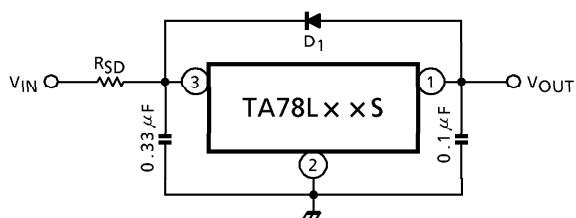


Precautions for Use

If high voltage in excess of output voltage (TYP. value) of IC is applied to its output terminal, IC may be destroyed. In this case, connect a Zener diode between the output terminal and GND to prevent application of excessive voltage. In particular, in such a current boosting circuit as shown in Application Circuit Example (2), if input voltage is suddenly applied by stages and furthermore, load is light, excessive voltage may be applied transiently to the output terminal of IC. In such a case as this, it may become necessary to increase capacity of output capacitor as appropriate, use a smaller R₁ (a resistor for bypassing IC bias current) or gradually rise input voltage in addition to use of a Zener diode as mentioned above.

APPLICATION CIRCUIT

(1) STANDARD APPLICATION



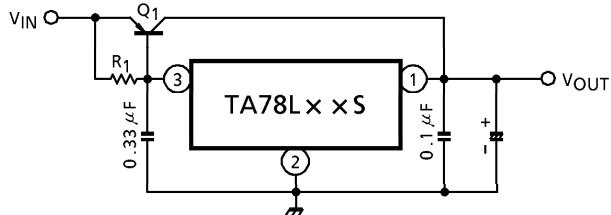
D₁ : IC protective diode

When surge voltage is applied to IC output terminal or V_{IN} < V_{OUT} at the time of power ON/OFF, always connect the high speed switching diode D₁.

R_{SD} : Power limiting resistor

If V_{IN} is too high, always connect R_{SD} in order to reduce power consumption of IC.

(2) A. CURRENT BOOST VOLTAGE REGULATOR

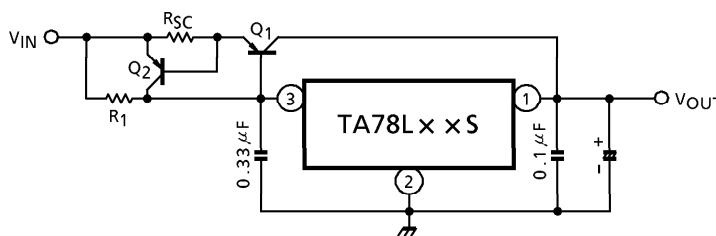


Use a required radiation plate for Q₁.

$$R_1 \leq \frac{V_{BE1}}{I_B \text{ MAX}}$$

where, V_{BE1} : V_{BE} of external transistor Q₁.
I_B MAX : Max. bias current of IC.

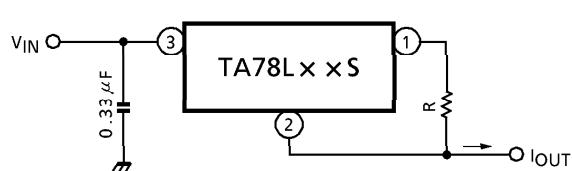
B. SHORT-CIRCUIT PROTECTION



$$R_{SC} = \frac{V_{BE2}}{I_{SC}}$$

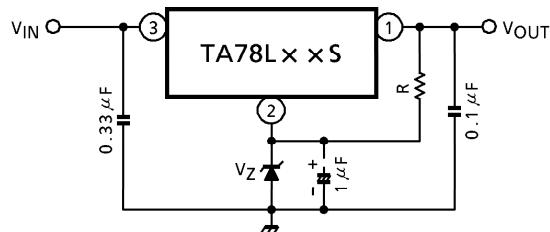
where, I_{SC} : Short-Circuit current

(3) CURRENT REGULATOR

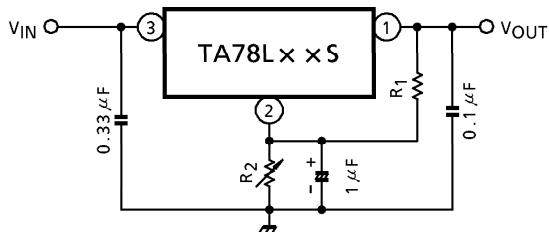


$$I_{OUT} = \frac{V_{OUT}}{R} + I_B$$

(4) VOLTAGE BOOST REGULATOR

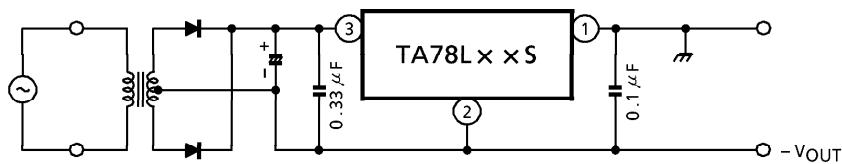


$V_{OUT} = V_Z + V_{OUT}$ (of IC)
Apply current of several mA to R.

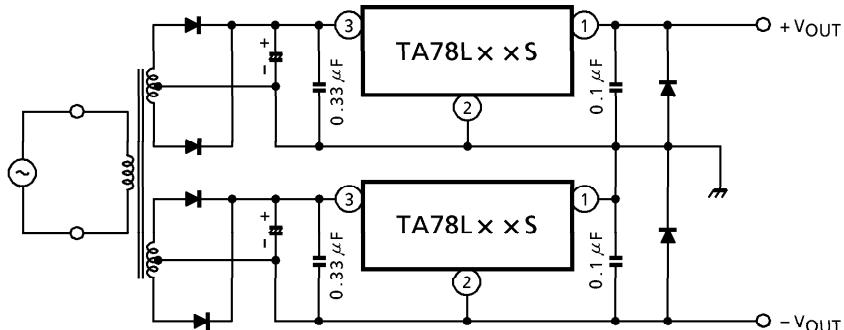


$$V_{OUT} = R_2(I_B + \frac{V_{OUT}(\text{of IC})}{R_1}) + V_{OUT}(\text{of IC})$$

(5) NEGATIVE REGULATOR



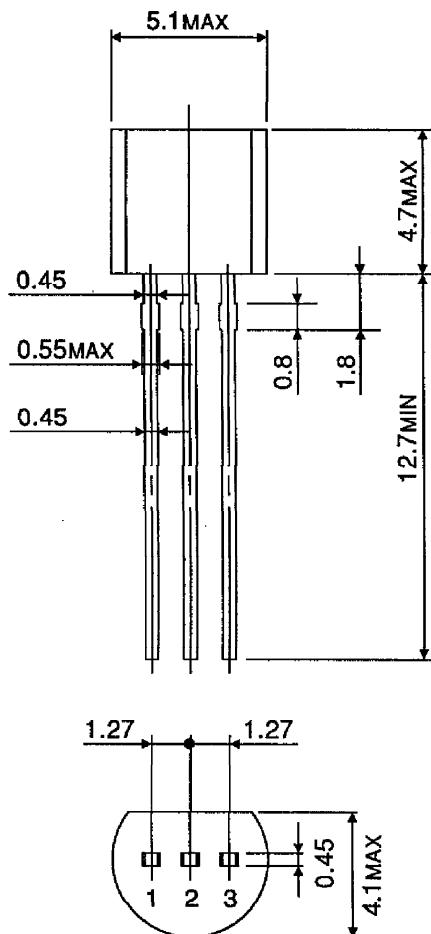
(6) POSITIVE AND NEGATIVE REGULATOR



PACKAGE DIMENSIONS

P-SSIP3-1.27A

Unit : mm



Weight : 0.21 g (Typ.)