



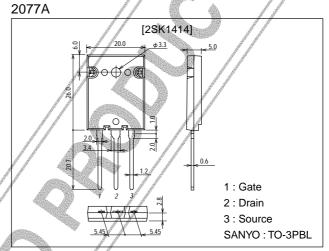
Ultrahigh-Speed Switching Applications

Features

- · Low ON resistance, low input capacitance, Ultrahigh-speed switching.
- · High reliability (Adoption of HVP process).

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter		Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage		VDSS		1500	V
Gate-to-Source Voltage		Vgss		±20	V
Drain Current (DC)		//b		6	Α
Drain Current (Pulse)	B	/ I _{DP} 《	PW≤10μs, duty cycle≤1%	12	Α
Allowable Power Dissipation		PD		3.5	W
		' ט	Tc=25°C	200	W
Channel Temperature		Tch		150	°C
Storage Temperature		Tstg	* //	-55 to +150	°C

Electrical Characteristics at Ta = 25°C

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Parameter //	Symbol	Conditions	Ratings			Unit
T didincter			min	typ	max	Onne
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =1mA, V _{GS} =0	1500			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =1200V, V _{GS} =0			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±20V, V _{DS} =0			±100	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.5		3.5	V
Forward Transfer Admittance	/ yfs	V _{DS} =20V, I _D =3A	1.0	3.0		S
Static Drain-to-Source ON-State Resistance	RDS(on)	I _D =3A, V _{GS} =10V		2.5	3.5	Ω

(Note) Be careful in handling the 2SK1414 because it has no protection diode between gate and source.

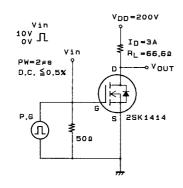
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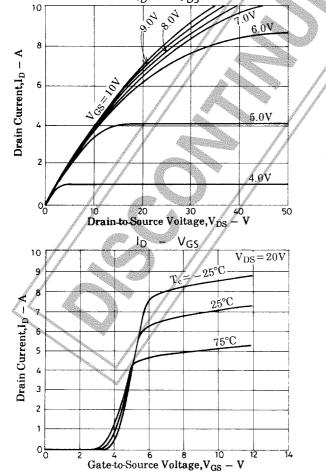
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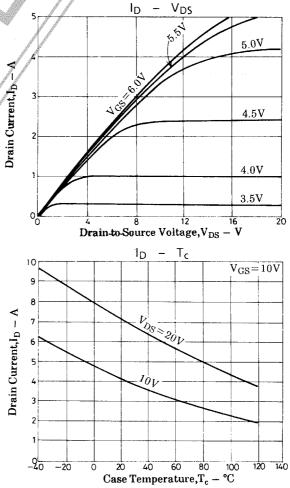
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		1100		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		350		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz	1	150		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit	1//~	25		ns
Rise Time	t _r	See specified Test Circuit	1/	85	San	ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit	/ /	155	The state of the s	ns
Fall Time	t _f	See specified Test Circuit		95	The state of the s	ns
Diode Forward Voltage	V _{SD}	I _S =6A, V _{GS} =0		1.0	1.5	V

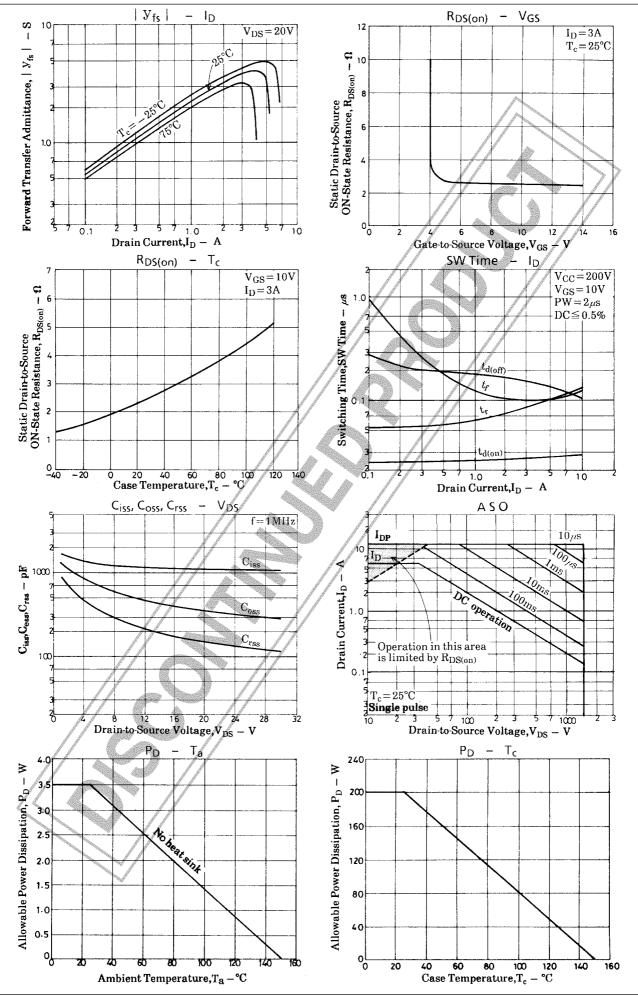
Switching Time Test Circuit

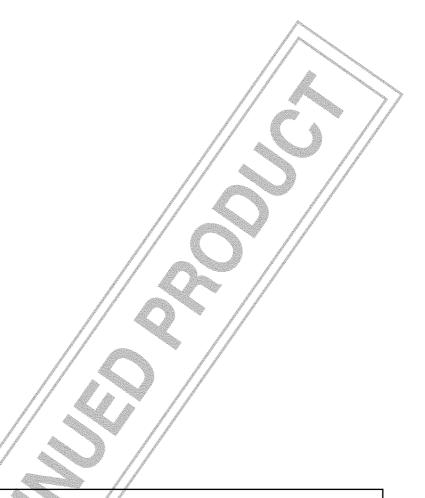




 V_{DS}







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