

MAXIM

MAX2402 Evaluation Kit

Evaluates: MAX2402

General Description

The MAX2402 evaluation kit (EV kit) simplifies the evaluation of the MAX2402 transmitter. The EV kit will enable the testing of all functions of the MAX2402 with no additional support circuitry and with minimal equipment.

Features

- ◆ Low-Cost, Flexible Transmitter
- ◆ More than 100mW of Output Power
- ◆ Operates from 800MHz to 1000MHz
- ◆ Single +5V Supply
- ◆ Easy Testing of All MAX2402 Features

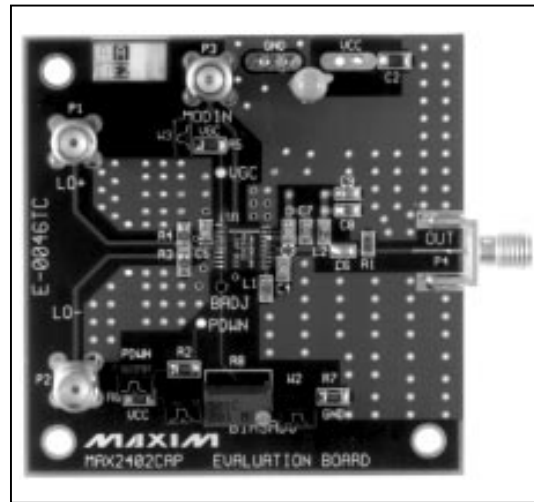
Component List

DESIGNATION	QTY	DESCRIPTION
C1	1	10 μ F, 25V, 10%, tantalum capacitor
C2	1	0.1 μ F, 50V, 10% ceramic capacitor
C3, C4, C5	3	39pF, 50V, 5% ceramic capacitors
C6	1	150pF, 50V, 5% ceramic capacitor
C7, C9	2	0.01 μ F, 50V, 10% ceramic capacitors
C8	1	100pF, 50V, 5% ceramic capacitor
L1	1	27nH, 20% inductor CoilCraft 0805CS-270XMBC
L2	1	47nH, 20% inductor CoilCraft 0805CS-470XMBC
LO+, LO-MOD, OUT	4	Female SMA connectors
R1	1	121 Ω , 1% resistor
R2, R5	2	100 Ω , 5% resistors
R3, R4	2	49.9 Ω , 1% resistors
R6, R7	2	51 Ω , 5% resistors
R8	1	1k Ω , multi-turn potentiometer
U1	1	MAX2402EAP
W1-W4	4	2-pin headers
None	4	2-pin shunts

Ordering Information

PART	TEMP. RANGE	BOARD TYPE
MAX2402EVKIT-SO	-40°C to +85°C	Surface Mount

EV Kit



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Quick Start

The MAX2402 EV kit is fully assembled and factory tested. **Do not turn on the power until all connections are made.**

Test Equipment Required

- Signal Source, sine-wave generator with range up to 1000MHz (example: HP8656B)
- Signal Source, function generator with range up to 10MHz
- Spectrum Analyzer with range up to 4GHz (example: TEK2755AP)
- Power Supply capable of 5V, 300mA output with current limit

Connections and Signal Conditions

- 1) Verify that all shunts are across jumpers W1–W4.
- 2) Connect an SMA cable from the 1000MHz signal source to the LO+ SMA input (or LO- SMA input) on the EV kit. Place a shorting termination on LO-. If differential drive is desired, connect the SMA cables from the signal source to a balun with sufficient bandwidth, and connect cables from the balun 0° and 180° ports to the LO+ and LO- inputs.
- 3) Connect an SMA cable from the spectrum analyzer to the OUT SMA on the EV kit.
- 4) Connect the power supply to the appropriate V_{CC} and GND terminals on the EV kit.
- 5) Place a shorting termination on the MOD SMA connector to put the mixer in a fully on position.
- 6) Set LO power to 0dBm and frequency to 900MHz on the signal source. Do not apply a signal to the DUT yet, if you have control of this function.
- 7) Set the spectrum analyzer's dynamic range for a top limit of 30dBm, and set the frequency range for an appropriate setting to view the output.
- 8) Set the power supply to 5.0V and set the current limit to 300mA. Apply power.

Analysis

- 1) There is a 121Ω surface-mount resistor on the EV kit which is parallel with the 50Ω termination of the spectrum analyzer. This sets the load of the power amplifier at 35Ω, which is a close match to the power-amplifier's output impedance. As a result, the spectrum analyzer will display an output power level which is 1.5dB below the actual transmitted power. As long as this resistor is on the EV kit, 1.5dB must be added to any displayed power levels to get accurate information.

The 121Ω can be removed if so desired with about a 0.7dB reduction in transmitted power due to the load mismatch. The output power (with 1.5dB added to the displayed power) should be at least 20dBm. (Note: Before signal is applied to the LO port there may be a parasitic oscillation on the EV board. This is caused by parasitic feedback from the power-amplifier output to the LO port and cable. When signal is applied to the LO port, this oscillation will abate.)

- 2) The output power can be observed for the 800MHz to 1000MHz LO input range and over the prescribed input power levels. (Near 800MHz, it may be necessary to adjust BADI to higher than 2V to maintain stability.)
- 3) To observe the effects of the VGC voltage on output power, connect an adjustable supply to the VGC test point on the EV kit and remove the VGC jumper (W3). This supply can now be adjusted and the output power can be observed as a function of VGC voltage. The VGC range is 0V to V_{CC}. The output power should be at a minimum when the VGC is adjusted below 0.8V. The output power should be at a maximum when the VGC is adjusted above (V_{CC} - 0.5V).
- 4) The BADI pin is used to control the bias level of the final stages of the PA. The adjustment range on the BADI is 0V to V_{CC}, with 0V representing the most bias current and 5V the least. More bias current will result in more output power, less efficiency, and less distortion. The intended configuration for this pin is a single resistor pull-up or pull-down to V_{CC} or GND, respectively. The value of this resistor will determine the bias voltage at the BADI pin. See Table 1 in the MAX2402 data sheet for a guide to resistor use at the BADI input.

The MAX2402 BADI input is self biasing to about V_{CC}/2 and can be left open. At low BADI voltage settings and lower frequencies within the 800MHz to 1000MHz range, the power-supply current may increase unacceptably or the circuit may oscillate. At these lower frequencies, more than 20dBm of power can easily be obtained with BADI set at 3V or above (see the *BADI Input* section of the MAX2402 data sheet).

Removing the V_{CC} jumper (W1) on the EV kit will connect BADI to GND, while removing the GND jumper (W2) will connect BADI to V_{CC}. Removing both will cause BADI to rely on its internal bias.

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- 5) The MAX2402 $\overline{\text{SHDN}}$ pin connects to V_{CC} through a jumper and 100Ω . To test the shutdown function, ensure that either W1 or W2 is removed, which will prevent current draw through R6, R7, and R8. When the $\overline{\text{SHDN}}$ jumper is removed and the $\overline{\text{SHDN}}$ test point is grounded, the supply current should drop below $1\mu\text{A}$.
- 6) The modulated spectrum can be examined on the spectrum analyzer by removing the shorting termination and supplying a modulation signal to the SMA MOD input. The MOD input is linear from approximately 1.5V to 3.5V, and has a bandwidth of DC to 25MHz. The MOD input is self-biasing to approximately $V_{CC}/2$. Any offset at the MOD input from a symmetric signal around the self-bias voltage will act as an offset and cause less than optimal carrier rejection. Capacitive coupling into the MOD input will eliminate this situation and result in optimum carrier rejection. The MOD input will act as an attenuator if it is left open.

Adjustments and Control

VGC

The VGC jumper (W3) shorts the VGC input of the MAX2402 to V_{CC} . The VGC test point can be used to manipulate the gain control voltage. The VGC jumper (W3) should be removed before trying to control this VGC voltage. It connects to V_{CC} when in place.

$\overline{\text{SHDN}}$

The $\overline{\text{SHDN}}$ jumper (W4) shorts the $\overline{\text{SHDN}}$ input to V_{CC} to keep the part in normal operating condition. The $\overline{\text{SHDN}}$ test point can be used with a controlling voltage to power down the MAX2402. The $\overline{\text{SHDN}}$ jumper (W4) should be removed when adjusting the voltage on the test pin. It connects to V_{CC} when in place.

BADJ

The two bias adjust jumpers connect either end of the $1\text{k}\Omega$ potentiometer to V_{CC} and GND through 50Ω resistors. The wiper on the potentiometer has been factory adjusted to provide 2V to the BADJ input on the MAX2402. The BADJ test point is for monitoring the BADJ voltage. The BADJ jumpers (W1, W2) connect R8 to V_{CC} and GND, respectively. BADJ voltage is altered by adjusting the R8 potentiometer.

Layout Considerations

The evaluation board can serve as a guide for layout of your board. The following considerations were taken into account on the evaluation board. C3, C4, and C5 should be small surface-mount capacitors, placed directly from each V_{CC} pin to the adjacent ground. Place them as close to the MAX2402 as possible, and make connections directly to the pins (not through vias or long traces). C6, C7, and C8 should also be surface mount. C7 should be next to C3. C8 and C9 should be located at the V_{CC} terminal of choke L2. If the LO is driven single-ended, ground the unused LO port. If a single resistor is used to bias BADJ, it may be necessary to AC couple BADJ to ground with a low-value capacitor, since the high-impedance at the BADJ node may be sensitive to circuit noise. Although the evaluation board uses four layers, it is certainly possible to use two.

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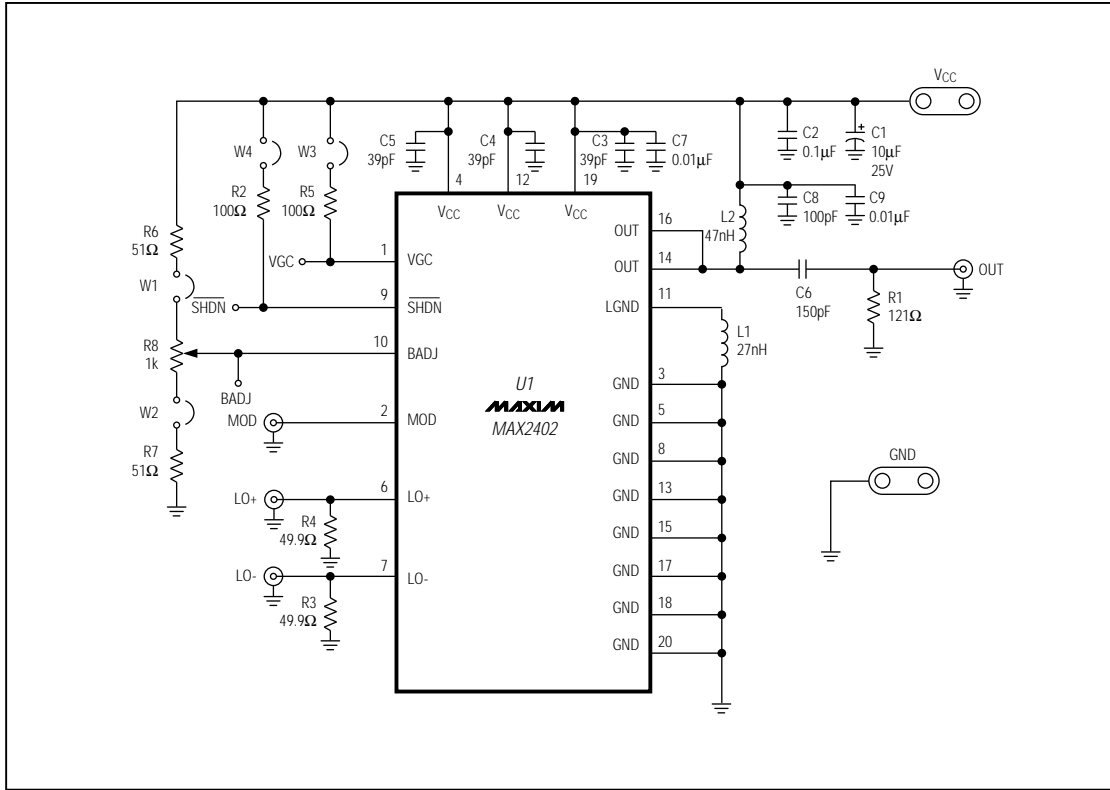


Figure 1. MAX2402 EV Kit Schematic

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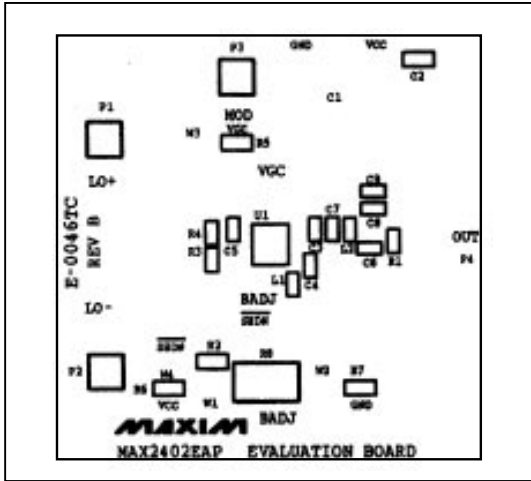


Figure 2. MAX2402 EV Kit Component Placement Guide—Component Side

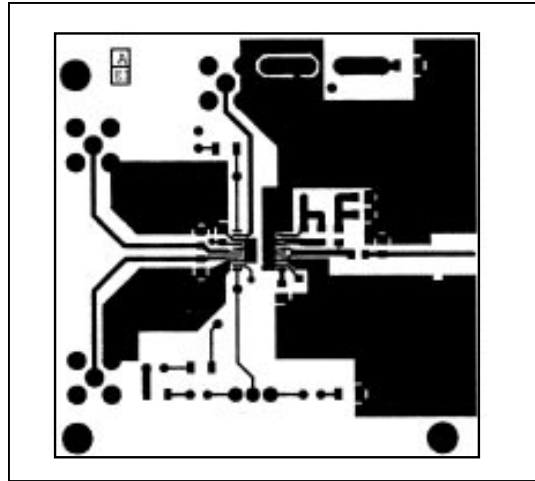


Figure 3. MAX2402 EV Kit PC Board Layout—Component Side

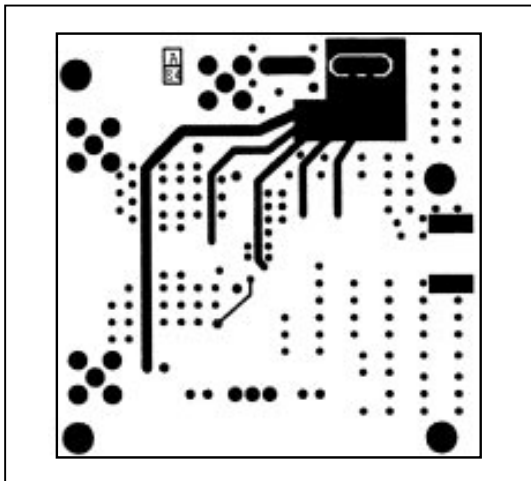


Figure 4. MAX2402 EV Kit PC Board Layout—Solder Side

Note: Ground layers 2 and 3 not shown.

