

APQO 7050

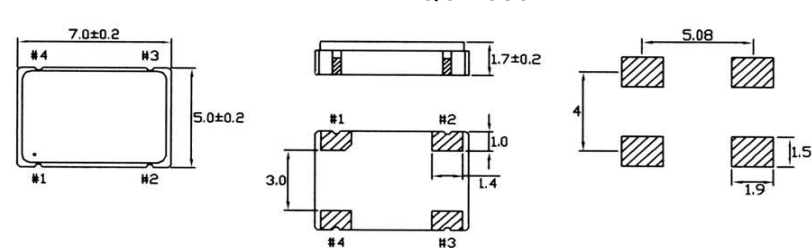
Features:

- Standard 7x5mm
- Low cost to performane
- 3.0 ~ 5.5 volt available
- Tolerance and stability to ± 25 ppm



Specifications		
	APQO 7050	Remarks
Frequency range	1 ~ 133MHz	Please specify
Frequency stability	± 25 ppm ~ ± 100 ppm	Please specify
Operating temperature	-0°C ~ +70°C - -40°C ~ +85°C	Please specify
Storage temperature	-55°C ~ +125°C	
Programmable voltage	1 ~ 133 MHz	4.0V ~ 5.5V
Programmable voltage	1 ~ 100 MHz	3.0V ~ 3.6V
Aging (ppm / Year), Ta = 25C, Vdd = 5 / 3.3 V		± 5 ppm
Programmable output level		CMOS / TTL

Operating conditions				
		Min	Max	Unit
Vdd	Supply voltage	3.0	5.5	V
CTTL	Max capacitive load on outputs for TTL levels			25
	4.5 V ~ 5.5 V Vdd 40 MHz		50	pF
	4.5 V ~ 5.5 V Vdd > 40 ~ 133 MHz		25	pF
CCMOS	Max capacitive load on outputs for CMOS levels			
	4.5 V ~ 5.5 V Vdd 66 MHz		50	pF
	4.5 V ~ 5.5 V Vdd > 66 ~ 133 MHz		25	pF
	3.0 V ~ 3.6 V Vdd 40 MHz		30	pF
	3.0 V ~ 3.6 V Vdd > 40 ~ 100 MHz		15	pF

Drawing	
APQO 7050  PIN FUNCTION 1 CONTROL 2 GND 3 OUTPUT 4 Vdd Dimensions in mm	

Order key						
O	- 10.000000M	- APQO 7050	- 50	- 5.0	- D	- T
Part	Frequency	Type/Package	Tolerance	Voltage	Temperature	Opion
O=Oscillator	M=MHz	APQO= programmable QO 7050=SMD 7x5	+/-ppm	5,0=5,0Volt 3,3=3,3Volt	A= 0°C - 70°C B=-10°C - 60°C C=-10°C - 70°C D=-20°C - 70°C E=-40°C - 85°C	T= Tristate X=Special options

Remarks: All specifications subject to change without notice!