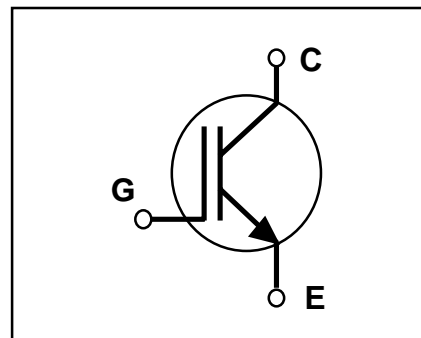
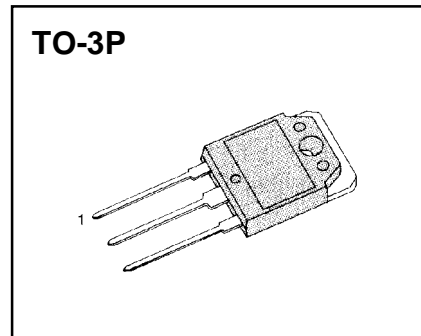


FEATURES

- * High Speed Switching
- * Low Saturation Voltage
: $V_{CE(sat)} = 1.95\text{ V}$ (@ $I_c=12\text{A}$)
- * High Input Impedance

APPLICATIONS

- * AC & DC Motor controls
- * General Purpose Inverters
- * Robotics , Servo Controls
- * Power Supply
- * Lamp Ballast



ABSOLUTE MAXIMUM RATINGS

Symbol	Characteristics	Rating	Units
V_{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	± 20	V
I_c	Collector Current @ $T_c = 25^\circ\text{C}$	23	A
	Collector Current @ $T_c = 100^\circ\text{C}$	12	A
$I_{CM(1)}$	Pulsed Collector Current	92	A
P_c	Maximum Power Dissipation @ $T_c = 25^\circ\text{C}$	100	W
	Maximum Power Dissipation @ $T_c = 100^\circ\text{C}$	40	W
T_j	Operating Junction Temperature	-55 ~ 150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55 ~ 150	$^\circ\text{C}$
T_L	Maximum Lead Temp. For Soldering Purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

Notes:(1) Repetitive rating : Pulse width limited by max. junction temperature

ELECTRICAL CHARACTERISTICS

(T_c=25°C, Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions	Min	Typ	Max	Units	
BV _{CES}	C - E Breakdown Voltage	V _{GE} = 0V , I _C = 250uA	600	-	-	V	
ΔV _{CES} / ΔT _J	Temperature Coeff. of Breakdown Voltage	V _{GE} = 0V , I _C = 1mA	-	0.6	-	V/°C	
V _{GE(th)}	G - E threshold voltage	I _C = 12mA , V _{CE} = V _{GE}	4.0	5.5	7.5	V	
I _{CES}	Collector cutoff Current	V _{CE} = V _{CES} , V _{GE} = 0V	-	-	250	uA	
I _{GES}	G - E leakage Current	V _{GE} = V _{GES} , V _{CE} = 0V	-	-	100	nA	
V _{CE(sat)}	Collector to Emitter saturation voltage	I _C =12A, V _{GE} = 15V	-	1.95	2.6	V	
		I _C =23A, V _{GE} = 15V	-	2.6	-	V	
Cies	Input capacitance	V _{GE} = 0V , f = 1MHz V _{CE} = 30V	-	720	-	pF	
Co _{es}	Output capacitance		-	65	-	pF	
Cr _{es}	Reverse transfer capacitance		-	26	-	pF	
td(on)	Turn on delay time	V _{CC} = 300V , I _C = 12A V _{GE} = 15V R _G = 23Ω Inductive Load	-	12	-	ns	
tr	Turn on rise time		-	20	-	ns	
td(off)	Turn off delay time		-	55	85	ns	
tf	Turn off fall time		-	100	220	ns	
E _{on}	Turn on Switching Loss		-	0.11	-	mJ	
E _{off}	Turn off Switching Loss		-	0.19	-	mJ	
E _{ts}	Total Switching Loss		-	0.3	0.5	mJ	
Q _g	Total Gate Charge		V _{CC} = 300V	-	48	72	nC
Q _{ge}	Gate-Emitter Charge		V _{GE} = 15V	-	11	16	nC
Q _{gc}	Gate-Collector Charge	I _C = 12A	-	14	21	nC	
Le	Internal Emitter Inductance	Measured 5mm from PKG	-	7.5	-	nH	

THERMAL RESISTANCE

Symbol	Characteristics	Min	Typ	Max	Units
R _θ JC	Junction-to-Case	-	-	1.2	°C/W
R _θ JA	Junction-to-Case	-	-	40	°C/W
R _θ CS	Case-to-Sink	-	0.24	-	°C/W

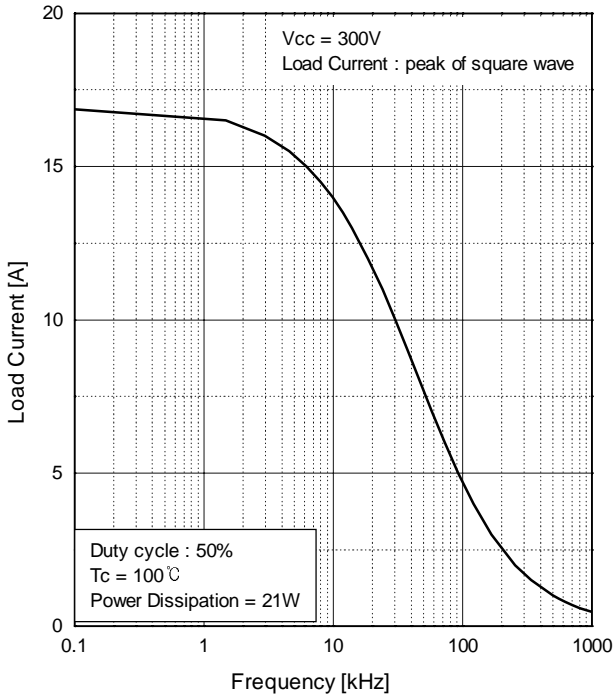


Fig.1 Typical Load Current vs. Frequency

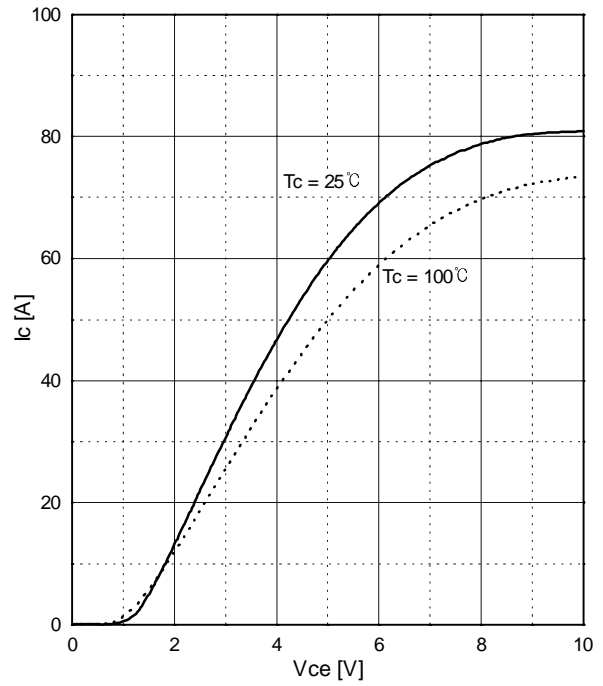


Fig.2 Typical Output Characteristics

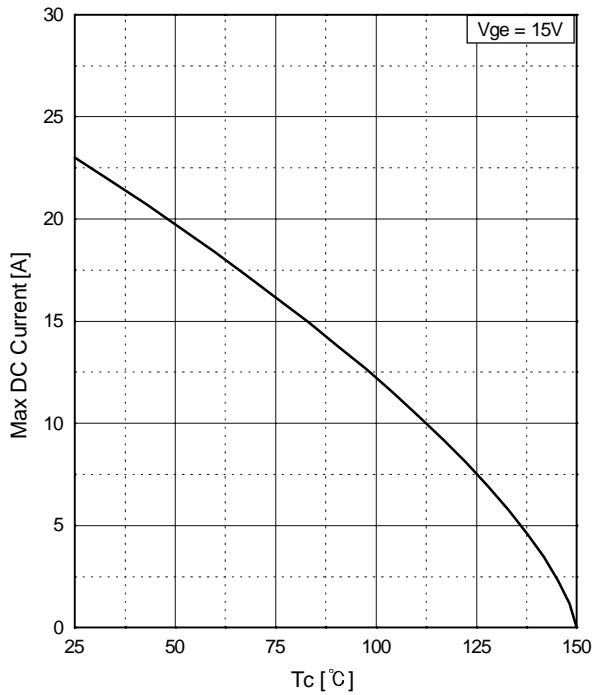


Fig.3 Maximum Collector Current vs. Case Temperature

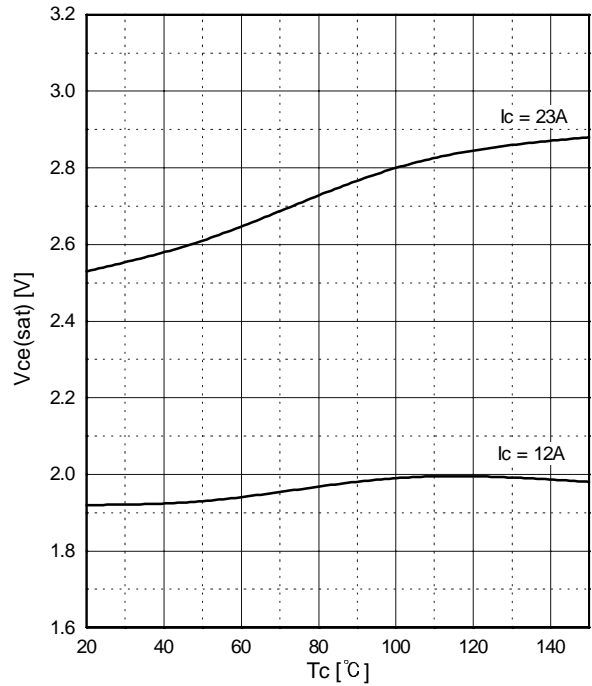


Fig.4 Collector to Emitter Voltage vs. Case Temperature

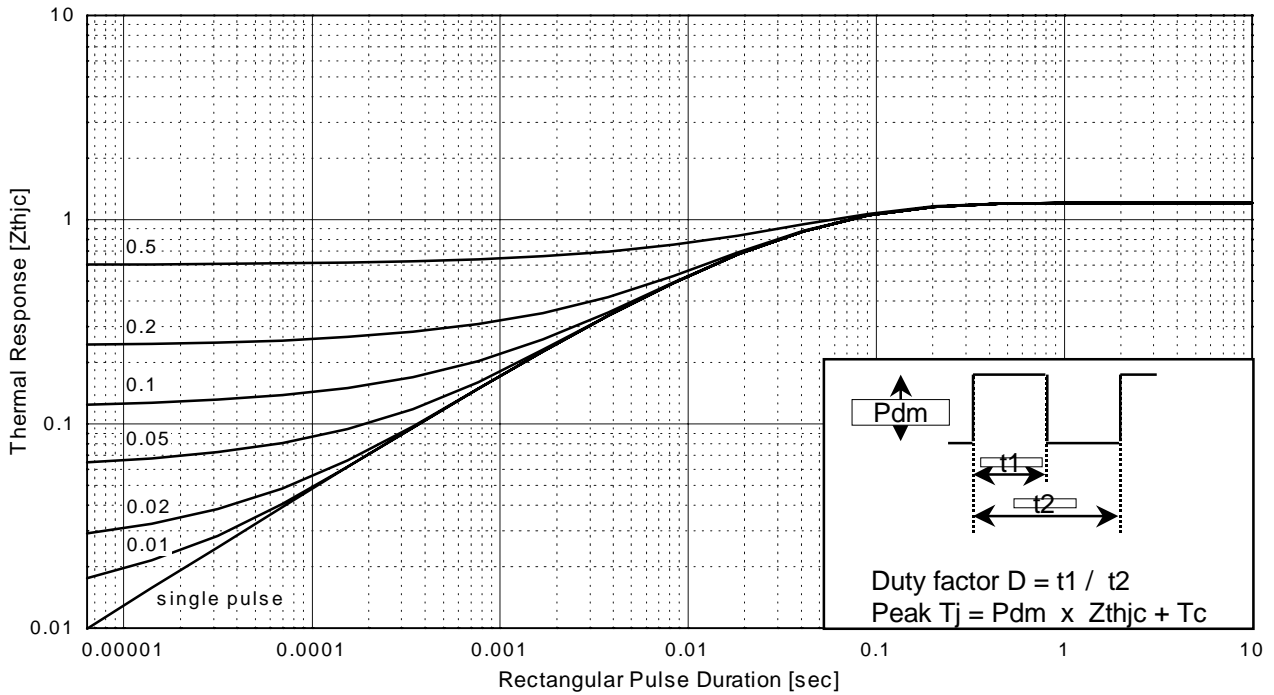


Fig.5 Maximum Effective Transient Thermal Impedance, Junction to Case

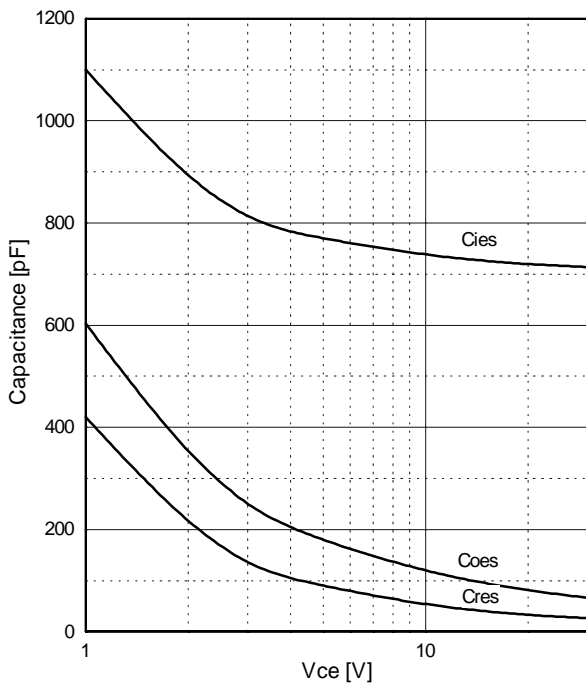


Fig.6 Typical Capacitance vs. Collector to Emitter Voltage

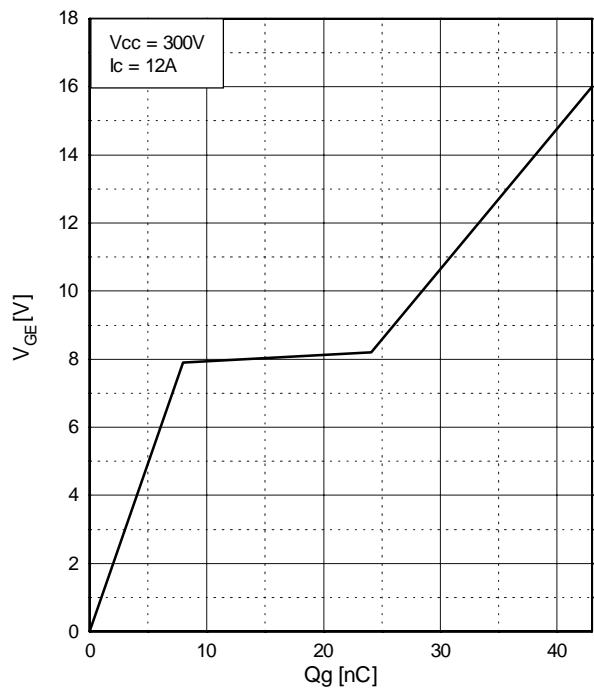


Fig.7 Typical Gate Charge vs. Gate to Emitter Voltage

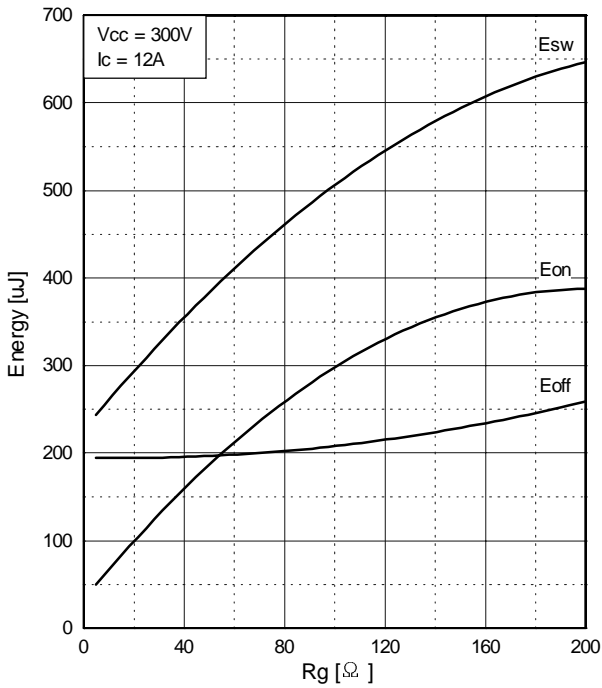


Fig.8 Typical Switching Loss vs. Gate Resistance

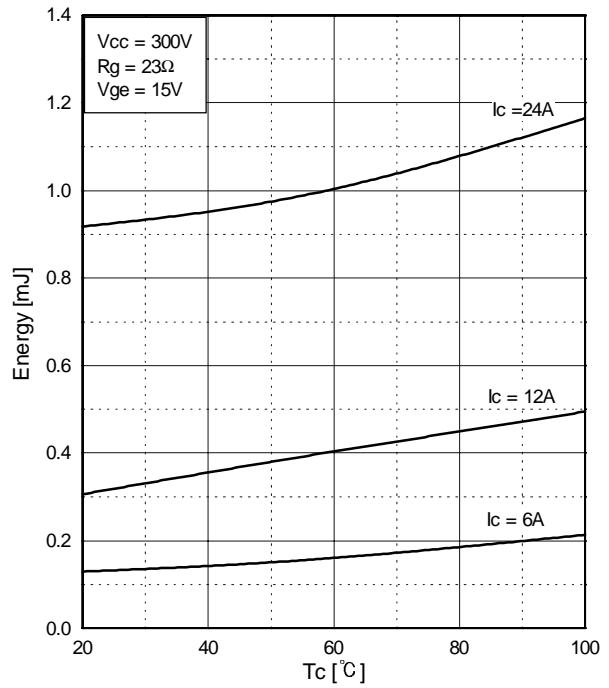


Fig.9 Typical Switching Loss vs. Case Temperature

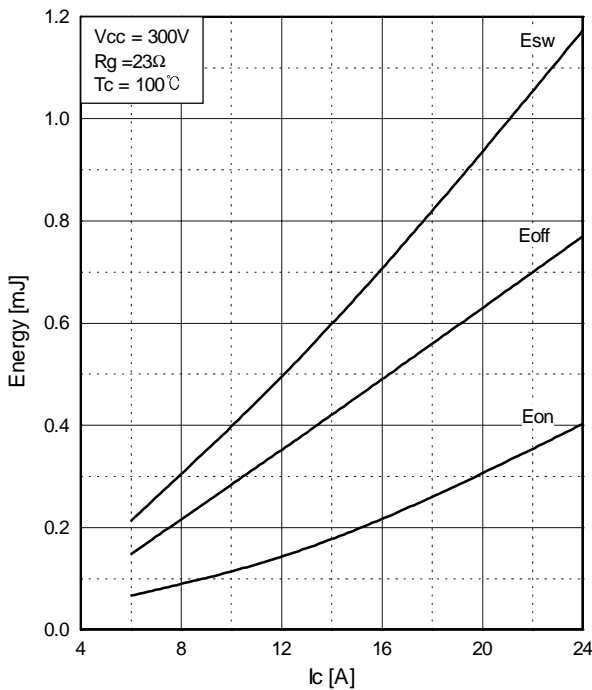


Fig.10 Typical Switching loss vs. Collector to Emitter Current

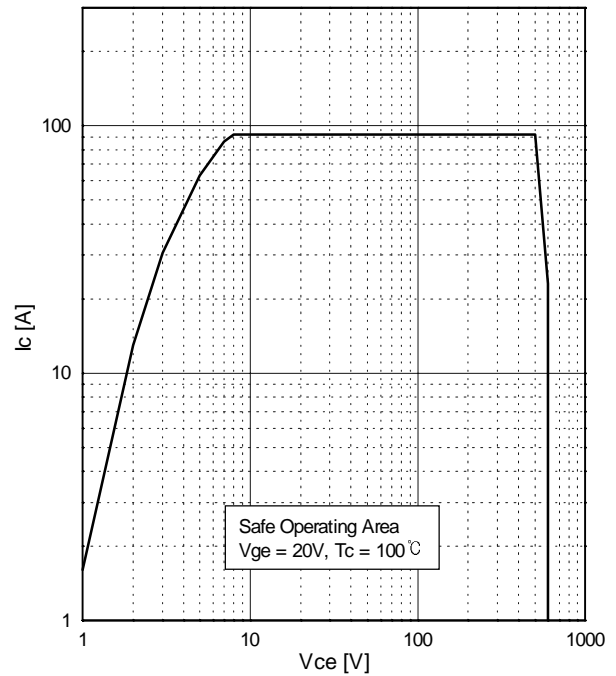


Fig.11 Turn-off SOA

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