

# SILICON TRANSISTOR 2SD2583

# AUDIO FREQUENCY AMPLIFIER, SWITCHING NOPN SILICON EPITAXIAL TRANSISTORS

#### **FEATURES**

Low Vce(sat)

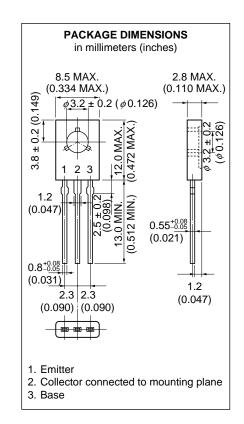
 $V_{CE(sat)} = 0.15 \text{ V Max } (@Ic/IB = 1.0 \text{ A}/50 \text{ mA})$ 

High DC Current Gain

hef = 150 to 600 (@Vce = 2.0 V, Ic = 1.0 A)

#### **ABSOLUTE MAXIMUM RATINGS**

Maximum Voltage and Current (T<sub>A</sub> = 25 °C) Collector to Base Voltage V<sub>CB0</sub> 30 V Collector to Emitter Volteage 30 V VCE0 Emitter to Base Voltage  $V_{EB0}$ 6.0 V Collector Current (DC) 5.0 A Ic(DC) Collector Current (Pulse)\* 10 A IC(Pulse) Base Current (DC) 2.0A I<sub>B(DC)</sub> \* PW ≤ 10ms, Duty Cycle ≤ 10 % Maximum Power Dissipation Total Power Dissipation (Tc = 25 °C) Рτ 10 W Total Power Dissipation (T<sub>A</sub> = 25 °C) 1.0 W Maximum Temperature 150 °C Junction Temperature Τį Storage Temperature Tstq -55 to 150 °C

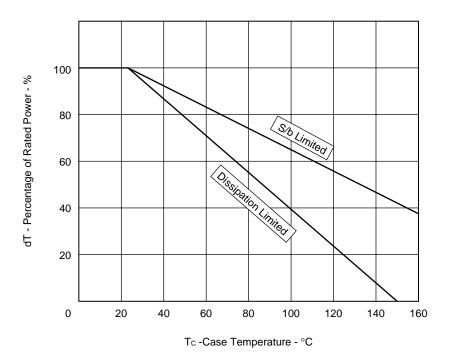


#### ELECTRICAL CHARACTERISTICS (TA = 25 °C)

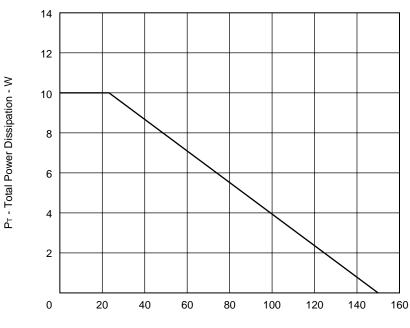
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Collector Cutoff Currnet	Ісво	Vcb = 30 V, IE = 0			100	nA
Emitter Cutoff Current	I <sub>EB0</sub>	VEB = 6.0 V, Ic = 0			100	nA
DC Current Gain	h <sub>FE1</sub>	VcE = 2.0 V, Ic = 1.0 A	150		600	_
DC Current Gain	hFE2	VcE = 2.0 V, Ic = 4.0 A	50			_
Collector Saturation Voltage	VCE(sat)1	Ic = 1.0 A, I <sub>B</sub> = 50 mA		0.07	0.15	V
Collector Saturation Voltage	VCE(sat)2	Ic = 2.0 A, I <sub>B</sub> = 0.1 A		0.13	0.25	V
Collector Saturation Voltage	VCE(sat)3	Ic = 4.0 A, I <sub>B</sub> = 0.2 A		0.24	0.50	V
Base Saturation Voltage	V <sub>BE(sat)</sub>	Ic = 2.0 A, I <sub>B</sub> = 0.1 A		0.86	1.50	V
Gain Bandwidth Product	f⊤	VcE = 10 V, IE = 50 mA		120		MHz
Output Capacitance	Соь	VcB = 10 V, IE = 0, f = 1 MHz		77		pF

The information in this document is subject to change without notice.

## DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA

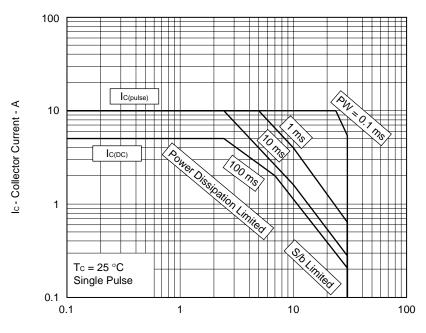


## TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



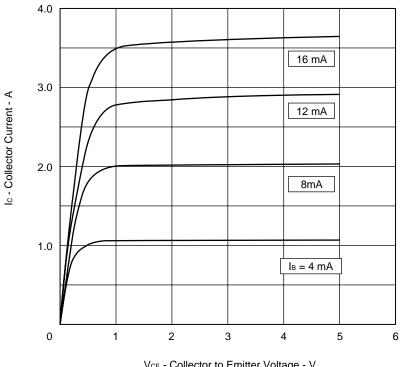
Tc -Case Temperature - °C

# FORWARD BIAS SAFE OPERATING AREA



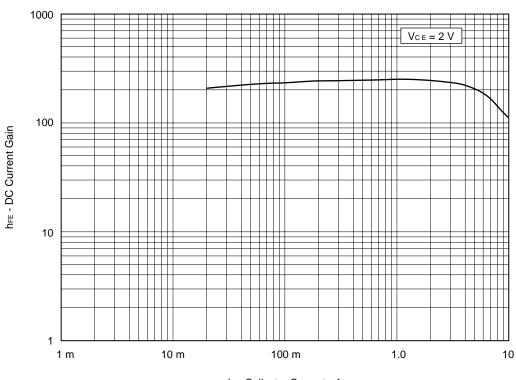
 $V_{\text{CE}}$  - Collector to Emitter Voltage - V

## Collector to Emitter Voltage vs Collector Current



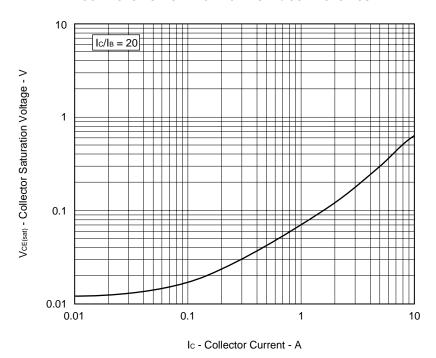
Vce - Collector to Emitter Voltage - V

## DC Current Gain vs Collector Current

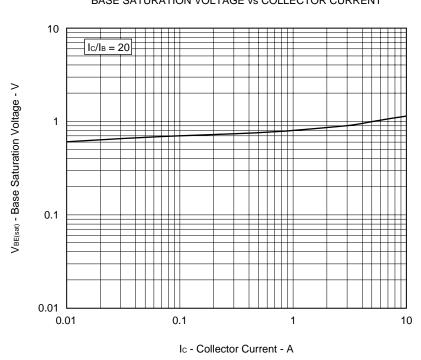


Ic - Collector Current - A

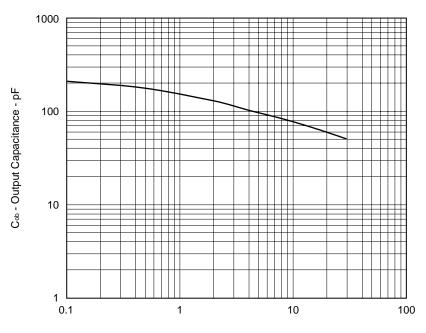
# COLLECTOR SATURATION VOLTAGE vs COLLECTOR CURRENT



# BASE SATURATION VOLTAGE vs COLLECTOR CURRENT



# OUTPUT CAPACITANCE vs COLLECTOR TO BASE VOLTAGE



 $V_{\text{CB}}$  - Collector to Base Voltage - V



# REFERENCE

Document Name	Document No.	
NEC semiconductor device reliability/quality control system	TEI-1202	
Quality grade on NEC semiconductor devices	IEI-1209	
Semiconductor device mounting technology manual	C10535E	
Semiconductor device package manual	C10943X	
Guide to quality assurance for semiconductor devices	MEI-1202	
Semiconductor selection guide	X10679E	

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Anti-radioactive design is not implemented in this product.

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