NJM2249M

NJM2249L

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3-INPUT VIDEO SWITCH

GENERAL DESCRIPTION

The NJM2249 is 3-input video switch for video and audio signal. One input terminals has sink-chip clamp function and so it is applied to fixed DC level of video sighal. Two other input terminals are transistor base input for luminant signal and so luminant level may be easily fixed by outer circuit. Its operating supply voltage range is 4.75 to 13V and bandwidth is 10MHz. Cross-talk is 70dB (at 4.43MHz).

FEATURES

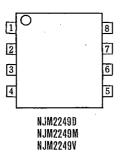
JRC

- Operating Voltage $(V^+=+4.75V \sim +13V)$
- 3 Input-1 Output
- Internal Clamp Function (VIN1)
- Internal Luminance Signal Control Function (VIN2, VIN3)
- Cross-talk 70dB(at 4.43MHz)
- Wide Frequency Range
- Package Outline DIP8, DMP8, SIP8, SSOP8
- Bipolar Technology

APPLICATION

• VCR, Video Camera, AV-TV, Video Disc Player





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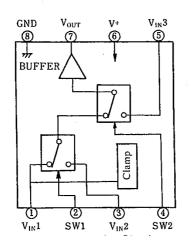
PIN FUNCTION
1. VIN 1
2.SW1
3. VIN 2
4.SW2
5. Vin 3
6. V+
7. Vour
8. GND

PACKAGE OUTLINE

NJM2249D

NJM2249V

BLOCK DIAGRAM



INPUT CONTROL SIGNAL-OUTPUT SIGNAL

SW I	SW 2	OUTPUT SIGNAL
L	L	VIN 1
Н	L	V _{IN} 2
L/H	H	V _{IN} 3

ABSOLUTE MAXIMUM RATINGS			(Ta=25°C)	
PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	V*	15	v	
Power Dissipation	PD	(DIP8) 500	mW	
		(DMP8) 300	mW	
		(SSOP8) 250	mW	
		(SIP8) 800	mW	
Operating Temperature Range	Topr	-20~+75		
Storage Temperature Range	• Tstg	-40~+125	Ĉ	

ELECTRICAL CHARACTERISTICS:

(V⁺=5V, Ta=25°C) [·]

PARAMETERS	SYMBOLS	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Recommended Supply Voltage	V+		4.75		13.0	V
Operating Current	lcc	S1=S2=S3=S4=S5=1		10.5	13.0	mA
Voltage Gain	Gv	$V_1 = 2.5 V_{P-P}$, 100kHz, V_0/V_1	-0.5	—	+0.5	dB
Fequency Characteristics	Gr	$V_1 = 2.0 V_{P-P}, V_0(10 M Hz) / V_0(100 k Hz)$	- 1.0	0	+1.0	dB
Differential Gain	DG	V ₁ =2V _{P-P} , Staircase Signal	—	0	—	%
Differential Phase	DP	$V_1 = 2V_{P-P}$, Staircase Signal	-	0	—	deg
Cross-talk	СТ	$V_1 = 2.0V_{P-P}$, 4.43MHz, V_0/V_1 (note 1)		- 70	-	dB
Switch Change Voltage	V _{CH}	All inside SW: ON	2.4		—	V
	VCL	All inside SW: OFF		-	0.8	V I
Output Impedance	Ro			10	-	Ω

(Note 1): Tested on all combination except three below.

 $100 \,\mu$ F

a) S1=2, S4=S5=1 b) S2=2, S4=2, S5=1 c) S3=2, S5=2

(Note2) : Unless specified, tested with $V_{BIAS}I = V_{BIAS}2 = 3V$.

(Note 3) : If it is not shown about switch condition, it is tested on three condition below.

a) S1=2, S2=S3=S4=S5=1 b) S1=1, S2=2, S3=1, S4=2, S5=1 c) S1=S2=1, S3=2, S4=1 or 2, S5=2 (Note 4): V_{IN}I clamp voltage is about 2/5 of supply voltage (about 2.0V if V+=5V).

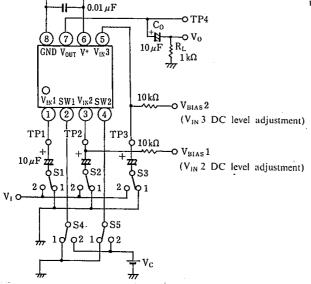
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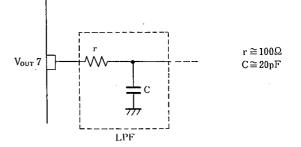
TEST CIRCUIT

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This IC requires $IM \Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.

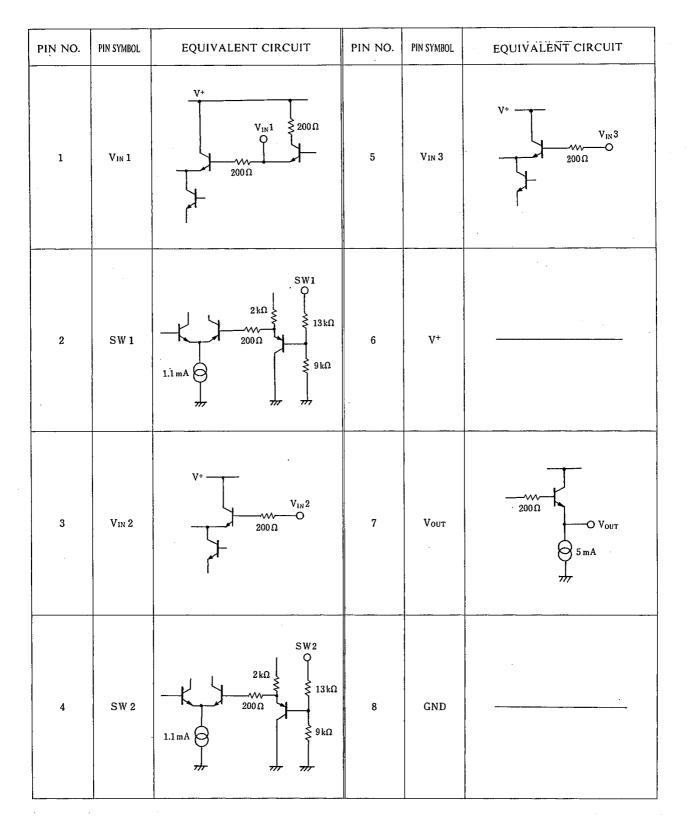
SPECIAL CARES TO BE TAKEN WHEN APPLICATION







TERMINAL FUNCTION



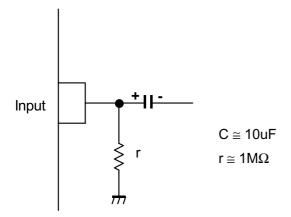
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■APPLICATION

This IC requires $1M\Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



This IC requires 0.1uF capacitor between INPUT and GND, $1M\Omega$ resistance between INPUT and GND for clamp type input at mute mode.

