TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

TPC6104

Notebook PC Applications Portable Equipment Applications

• Low drain-source ON resistance: RDS (ON) = 33 m Ω (typ.)

• High forward transfer admittance: $|Y_{fs}| = 12 S \text{ (typ.)}$

• Low leakage current: $IDSS = -10 \mu A (max) (VDS = -20 V)$

Enhancement mode: V_{th} = -0.5 to -1.2 V (V_{DS} = -10 V, I_D = -200 μA)

Maximum Ratings (Ta = 25°C)

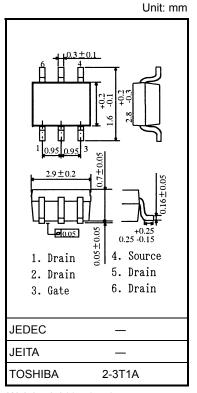
Character	ristics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-20	V
Drain-gate voltage (R	_{GS} = 20 kΩ)	V_{DGR}	-20	V
Gate-source voltage		V _{GSS}	±8	V
Drain current	DC (Note 1)	I _D	-5.5	۸
Diain current	Pulse (Note 1)	I _{DP}	-22	Α
Drain power dissipation	on (t = 5 s) (Note 2a)	P_{D}	2.2	W
Drain power dissipation	on (t = 5 s) (Note 2b)	P_{D}	0.7	W
Single pulse avalanch	e energy (Note 3)	E _{AS}	4.9	mJ
Avalanche current		I _{AR}	-2.75	Α
Repetitive avalanche	energy (Note 4)	E _{AR}	0.22	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 5 s) (Note 2a)	R _{th (ch-a)}	56.8	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	178.5	°C/W

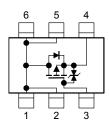
Note: Note 1, Note 2, Note 3, Note 4 and Note 5: See the next page.

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 0.011 g (typ.)

Circuit Configuration



Electrical Characteristics (Ta = 25°C)

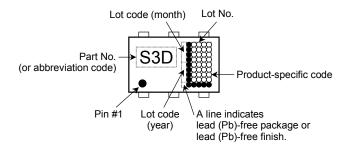
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	_		±10	μА	
Drain cut-off curr	ent			-10	μА			
Drain source bre	akdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-20	_	_	V	
Drain-source breakdown voltage		V _{(BR)DSX}	$I_D = -10$ mA, $V_{GS} = 8$ V	-12	_	_	v	
Gate threshold v	oltage	V _{th}	$V_{DS} = -10 \text{ V}, \ I_D = -200 \ \mu\text{A}$	-0.5	_	-1.2	V	
		R _{DS (ON)}	$V_{GS} = -1.8 \text{ V}, I_D = -1.4 \text{ A}$	_	78	120		
Drain-source ON resistance		R _{DS (ON)}	$V_{GS} = -2.5 \text{ V}, I_D = -2.8 \text{ A}$	_	49	60	mΩ	
		R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$	_	33	40		
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -2.8 \text{ A}$	6	12	_	S	
Input capacitance		C _{iss}		_	1430	_	pF	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	200	_		
Output capacitance		Coss		_	240	_		
Switching time	Rise time	t _r	0 V 7 F ln = -2 8 A	_	8.5	_	ns	
	Turn-on time	t _{on}	$V_{GS} = 0 \text{ V} $	_	15	_		
	Fall time	t _f	2 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	_	20	_		
	Turn-off time	t _{off}	$V_{DD} \simeq -10 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	66	_		
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ -16 V, V _{GS} = -5 V,	_	19	_		
Gate-source charge		Q _{gs}	$I_D = -5.5 \text{ A}$	_	14	_	nC	
Gate-drain ("miller") charge		Q _{gd}		_	5	_		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Charact	eristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	_	_	_	-22	Α
Forward voltage	(diode)	V _{DSF}	$I_{DR} = -5.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

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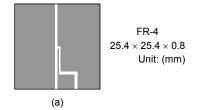
Marking (Note 5)

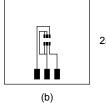


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) $(t=5\ s)$

(b) Device mounted on a glass-epoxy board (b) (t = 5 s)





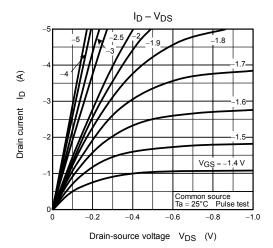
 $\begin{aligned} & \text{FR-4} \\ 25.4 \times 25.4 \times 0.8 \\ & \text{Unit: (mm)} \end{aligned}$

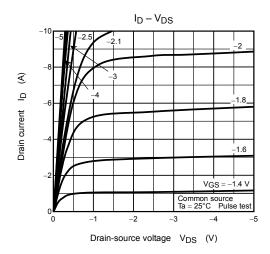
Note 3: $V_{DD} = -16 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = -2.75 \text{ A}$

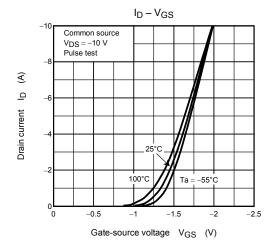
Note 4: Repetitive rating;:pulse width limited by maximum channel temperature

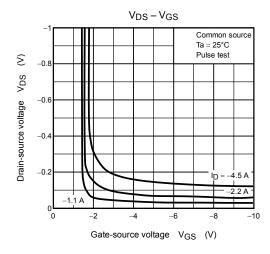
Note 5: • on lower left of the marking indicates Pin 1.

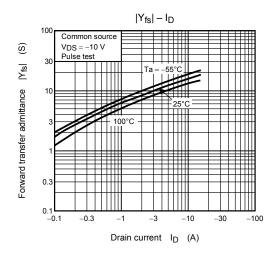
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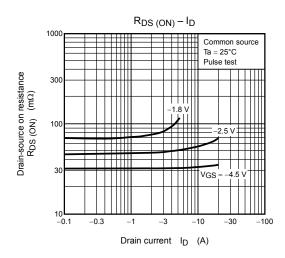


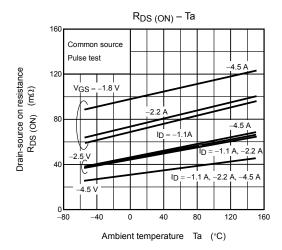


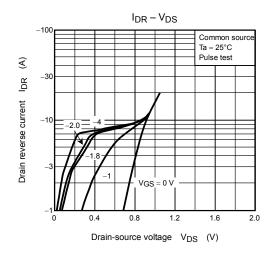


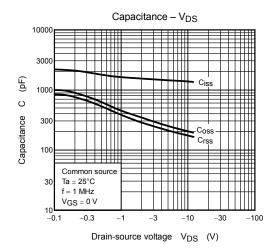


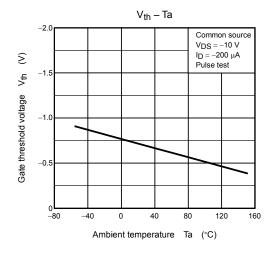


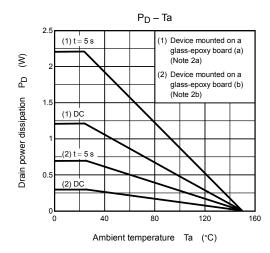


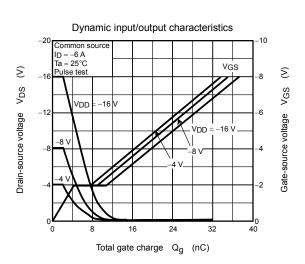




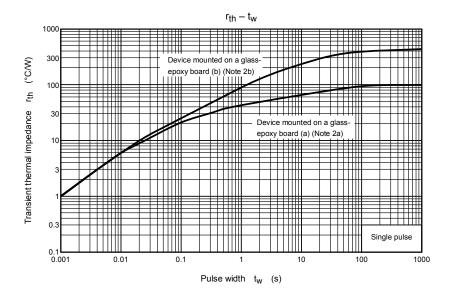


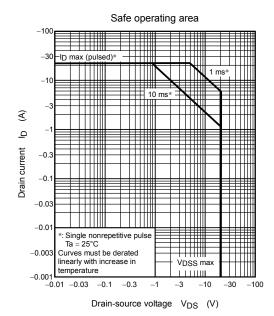






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