

IHB100S

100 Watt Single Output Half Brick DC/DC Converter







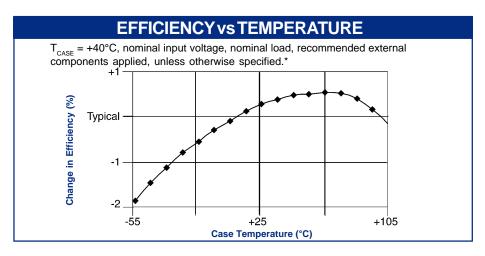


- 33 75V Input Range
- 1500VDC Isolation Between Input and Output
- High Efficiency: 86% Typical
- Operation to 100°C Baseplate Temperature
- 50μS Transient Recovery, 0-90% Load Step
- Primary & Secondary Remote On/Off
- External Synchronization
- IHB100S Series Are Approved to UL/CUL 1950, EN 60950

The IHB100S series standard half brick modules are designed for today's demanding industrial applications. Available in two wide range inputs, these isolated converters offer many features in the standard models. With a complement of safety agency approvals and low noise operations, the converters respond extremely fast to change in load conditions. Inherent in the design are very well-controlled output voltage and no need for minimum loading.

PRODUCT SELECTION CHART						
MODEL	INPUT VOLTAGE (VDC)	RATED VOUT (VDC)	RATED MAXIMUM IOUT (A)			
IHB100S4803	48 (33-75)	3.3	30			
IHB100S4805	48 (33-75)	5.1	20			

ABSOLUTEMAX. RATINGS					
Output Short-Circuit Duration	Continuous				
Baseplate Temperature	+100°C				
Lead Temperature (soldering, 10 seconds max)	+300°C				
Storage Temperature	+125°C				
Input to Output Isolation	1500 VDC				



 $\begin{tabular}{ll} SPECIFICATIONS, ALL MODELS \\ Specifications are at T_{CASE} = +40 °C nominal input voltage unless otherwise specified. \\ \end{tabular}$

	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
	Voltage Range		33	48	75	VDC
	Reflected Ripple Current	Peak - Peak			370	mA
	Input Ripple Rejection	DC to 1KHz	50	60		dB
5	Maximum Input Current	Output Power = 100W				
屲		V _{IN} = 30V			5	Α
Z	No Load Power Dissipation	$P_{OUT} = 0, V_{IN.Min} < V_{IN} < V_{IN.Max}$			6	W
	Inrush Charge	.,			0.247	mC
	Quiescent Operating Current					
	Primary On/Off Disabled			7.5	10	mA
	Secondary On/Off Disabled			15	20	mA

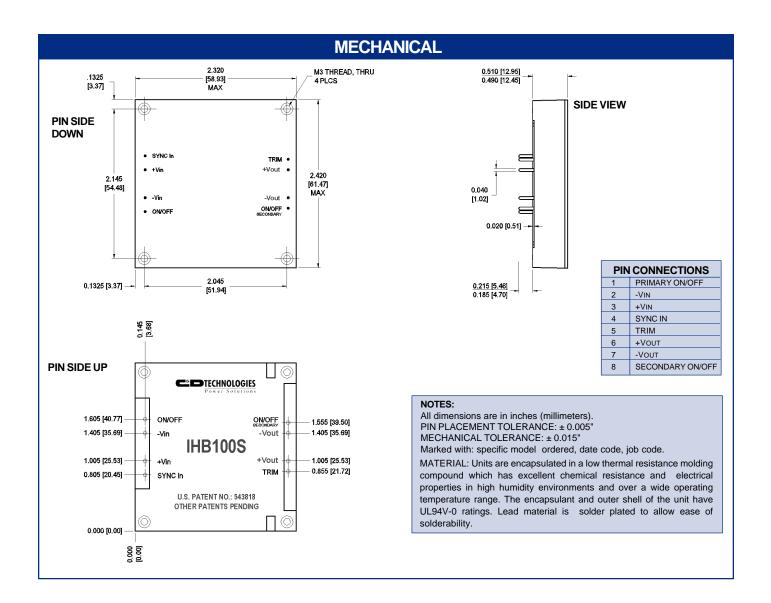
	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
	ISOLATION					
	Input to Output	Peak Test	1500			VDC
	Input to Baseplate		1500			VDC
	Resistance, Input - Output		10			MΩ
	Capacitance, Input - Output			2000		pF
	Leakage Current	V _{ISO} = 240VAC, 60Hz		180		μA, rms
	GENERAL					
	Set Point Accuracy	V _{IN} = Nominal, I _O =I _{NOM}			1	%
	Turn-on Time	Within 1% of Nominal V _{OUT}		3.5	5	mSec
	Remote On/Off Control Inputs	001				
1	Primary	Open Collector/Drain				
	Sink Current-Logic Low	$V_{IN} = V_{MAX}$			7	mA
	Vlow	iii iiii			0.8	V
7	Vhigh				Open Collector	
GENERAL	Secondary	Open Collector/Drain				
	Sink Current-Logic Low				100	μΑ
	Vlow				0.4	V
ш	Vhigh				Open Collector	
U	External Synchronization Input					
	Frequency		440		520	KHz
	Pulse Width		150		320	nSec
	Source Impedence				47	Ω
	Input High Voltage		4		5	V
	Input Low Voltage		0		1	V
	Input Impedance			470		Ω
	Switching Frequency		470	480	490	KHz
	Weight				3 (85)	oz (g)
	TEMPERATURE	Case Temperature				
	Operation/Specification		-40		+100	°C
	Storage		-55		+125	°C
	Shutdown		+100		+115	°C
	Thermal Inpedance	Case to Ambient		8.2		°C/W

 $[\]ensuremath{^{\star}}$ See Application Notes available on the web at www.cdpowerelectronics.com

	PARAMETER	CONDITIONS	Min	V _{out} Nom	Max	UNITS
\vdash	Output Power	100 Watts Max		50	100	W
	Set Point Voltage	I _{O Nom}		3.3		V
出出	Output Current, I _{OUT}		0	15	30.0	A
5	Output Ripple, p-p	DC to 20MHz*		100	200	mV
0	Output Adjust Range	Å	3.15		3.80	V
~	Output Temperature Drift			.02	.05	%/°C
4803	Line Regulation	$V_{IN,Min} \le V_{IN} \le V_{IN,Max}$ $I_{O} = I_{O,Nom}$		0.05	0.10	%
S	Load Regulation	Min Load to Rated Load		0.50	1.00	%
S00	Current Limit Inception			38		Α
2	Short-Circuit Current			30	38	Α
<u>m</u>	Transient Response	50 to 100% Load Step				
I	Peak Deviation			150	250	mV
	Settling Time	V _{OUT} , 1% of V _{OUT} , _{Nom}		35	50	μSec
	Overvoltage Limit		4.2		5.0	V
	Efficiency	V _{IN} =NOM, I _O =30A	83	84		%

	PARAMETER	CONDITIONS	Min	V _{OUT} Nom	Max	UNITS
Η.	Output Power	100 Watts Maximum		50	100	W
	Set Point Voltage	I _{ONom}		5.1		V
P	Output Current, I _{оит}		0	10	20	Α
15	Output Ripple, p-p	DC to 20MHz*		100	150	mV
\Box	Output Adjust Range	*	4.60		5.50	V
	Output Temperature Drift			.02	.05	%/°C
55	Line Regulation	$V_{IN,Min} \leq V_{IN} \leq V_{IN,Max}$				
805		I _O = I _{O, Nom}		0.05	0.10	%
00S4	Load Regulation	Min Load to Rated Load		0.5	1.0	%
18	Current Limit Inception			26.0		Α
Ö	Short-Circuit Current			20.0	26.0	Α
B.	Transient Response	50 to 100% Load Step				
1 #	Peak Deviation			200	300	mV
	Settling Time	V _{OUT} , 1% of V _{OUT, Nom}		35	50	μSec
_	Overvoltage Limit	35. 351,113	6.0		7.0	V
	Efficiency	V _{IN} =NOM, I _O =20A	86	87		%

^{*} See Application Notes available on the web at www.cdpowerelectronics.com



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