

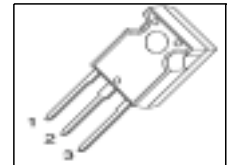
### Cool MOS™ Power Transistor

#### Feature

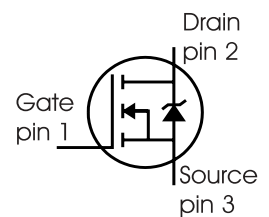
- New revolutionary high voltage technology
- Ultra low gate charge
- Periodic avalanche rated
- Extreme dv/dt rated
- High peak current capability
- Improved transconductance

|                     |      |          |
|---------------------|------|----------|
| $V_{DS} @ T_{jmax}$ | 650  | V        |
| $R_{DS(on)}$        | 0.38 | $\Omega$ |
| $I_D$               | 11   | A        |

P-TO247



| Type       | Package | Ordering Code | Marking |
|------------|---------|---------------|---------|
| SPW11N60C3 | P-TO247 | Q67040-S4418  | 11N60C3 |



#### Maximum Ratings

| Parameter  | Symbol              | Value       | Unit             |
|--|---------------------|-------------|------------------|
| Continuous drain current<br>$T_C = 25\text{ }^\circ\text{C}$<br>$T_C = 100\text{ }^\circ\text{C}$                        | $I_D$               | 11<br>7     | A                |
| Pulsed drain current, $t_p$ limited by $T_{jmax}$  | $I_{D\text{ puls}}$ | 33          |                  |
| Avalanche energy, single pulse<br>$I_D = 5.5\text{ A}$ , $V_{DD} = 50\text{ V}$  | $E_{AS}$            | 340         | mJ               |
| Avalanche energy, repetitive $t_{AR}$ limited by $T_{jmax}$ <sup>1</sup><br>$I_D = 11\text{ A}$ , $V_{DD} = 50\text{ V}$ | $E_{AR}$            | 0.6         |                  |
| Avalanche current, repetitive $t_{AR}$ limited by $T_{jmax}$   | $I_{AR}$            | 11          | A                |
| Reverse diode dv/dt<br>$I_S = 11\text{ A}$ , $V_{DS} = 480\text{ V}$ , $T_j = 125\text{ }^\circ\text{C}$                 | dv/dt               | 6           | V/ns             |
| Gate source voltage static   | $V_{GS}$            | $\pm 20$    | V                |
| Gate source voltage AC ( $f > 1\text{ Hz}$ )   | $V_{GS}$            | $\pm 30$    |                  |
| Power dissipation, $T_C = 25\text{ }^\circ\text{C}$  | $P_{tot}$           | 125         | W                |
| Operating and storage temperature  | $T_j, T_{stg}$      | -55... +150 | $^\circ\text{C}$ |

**Maximum Ratings**

| Parameter   | Symbol  | Value | Unit |
|---|---------|-------|------|
| Drain Source voltage slope<br>$V_{DS} = 480\text{ V}, I_D = 11\text{ A}, T_j = 125\text{ °C}$ | $dv/dt$ | 50    | V/ns |

**Thermal Characteristics**

| Parameter  | Symbol     | Values |      |      | Unit |
|--|------------|--------|------|------|------|
|  |            | min.   | typ. | max. |      |
| Thermal resistance, junction - case                            | $R_{thJC}$ | -      | -    | 1    | K/W  |
| Thermal resistance, junction - ambient, leaded                 | $R_{thJA}$ | -      | -    | 62   |      |
| Soldering temperature,<br>1.6 mm (0.063 in.) from case for 10s | $T_{sold}$ | -      | -    | 260  | °C   |

**Electrical Characteristics, at  $T_j=25\text{ °C}$  unless otherwise specified**

| Parameter                                   | Symbol        | Conditions  | Values |              |           | Unit |
|---|---------------|---|--------|--------------|-----------|------|
|   |               |   | min.   | typ.         | max.      |      |
| Drain-source breakdown voltage              | $V_{(BR)DSS}$ | $V_{GS}=0V, I_D=0.25mA$   | 600    | -            | -         | V    |
| Drain-Source avalanche<br>breakdown voltage | $V_{(BR)DS}$  | $V_{GS}=0V, I_D=11A$  | -      | 700          | -         |      |
| Gate threshold voltage                      | $V_{GS(th)}$  | $I_D=500\mu A, V_{GS}=V_{DS}$   | 2.1    | 3            | 3.9       |      |
| Zero gate voltage drain current             | $I_{DSS}$     | $V_{DS}=600V, V_{GS}=0V,$<br>$T_j=25\text{ °C},$<br>$T_j=150\text{ °C}$ | -      | -            | 25<br>250 | μA   |
| Gate-source leakage current                 | $I_{GSS}$     | $V_{GS}=30V, V_{DS}=0V$   | -      | -            | 100       | nA   |
| Drain-source on-state resistance            | $R_{DS(on)}$  | $V_{GS}=10V, I_D=7A,$<br>$T_j=25\text{ °C}$<br>$T_j=150\text{ °C}$      | -      | 0.34<br>0.92 | 0.38<br>- | Ω    |
| Gate input resistance                       | $R_G$         | $f=1MHz, \text{open Drain}$   | -      | 0.86         | -         |      |

**Electrical Characteristics** , at  $T_j = 25\text{ }^\circ\text{C}$ , unless otherwise specified

| Parameter   | Symbol       | Conditions   | Values |      |      | Unit |
|---|--------------|--|--------|------|------|------|
|   |              |  | min.   | typ. | max. |      |
| Transconductance  | $g_{fs}$     | $V_{DS} \geq 2 \cdot I_D \cdot R_{DS(on)max}$ ,<br>$I_D = 7\text{A}$                         | -      | 8.3  | -    | S    |
| Input capacitance   | $C_{iss}$    | $V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ ,<br>$f = 1\text{MHz}$                          | -      | 1200 | -    | pF   |
| Output capacitance  | $C_{oss}$    |  | -      | 390  | -    |      |
| Reverse transfer capacitance                                  | $C_{rss}$    |  | -      | 30   | -    |      |
| Effective output capacitance, <sup>2)</sup><br>energy related | $C_{o(er)}$  | $V_{GS} = 0\text{V}$ ,<br>$V_{DS} = 0\text{V to } 480\text{V}$                               | -      | 45   | -    | pF   |
| Effective output capacitance, <sup>3)</sup><br>time related   | $C_{o(tr)}$  |  | -      | 85   | -    |      |
| Turn-on delay time  | $t_{d(on)}$  | $V_{DD} = 380\text{V}$ , $V_{GS} = 0/10\text{V}$ ,<br>$I_D = 11\text{A}$ , $R_G = 6.8\Omega$ | -      | 10   | -    | ns   |
| Rise time   | $t_r$        |  | -      | 5    | -    |      |
| Turn-off delay time   | $t_{d(off)}$ |  | -      | 44   | 70   |      |
| Fall time   | $t_f$        |  | -      | 5    | 9    |      |

**Gate Charge Characteristics**

|                       |                 |   |   |     |    |    |
|-----------------------|-----------------|---|---|-----|----|----|
| Gate to source charge | $Q_{gs}$        | $V_{DD} = 480\text{V}$ , $I_D = 11\text{A}$   | - | 5.5 | -  | nC |
| Gate to drain charge  | $Q_{gd}$        |   | - | 22  | -  |    |
| Gate charge total     | $Q_g$           | $V_{DD} = 480\text{V}$ , $I_D = 11\text{A}$ ,<br>$V_{GS} = 0\text{ to } 10\text{V}$ | - | 45  | 60 |    |
| Gate plateau voltage  | $V_{(plateau)}$ | $V_{DD} = 480\text{V}$ , $I_D = 11\text{A}$   | - | 5.5 | -  | V  |

<sup>1</sup> Repetitive avalanche causes additional power losses that can be calculated as  $P_{AV} = E_{AR} \cdot f$ .

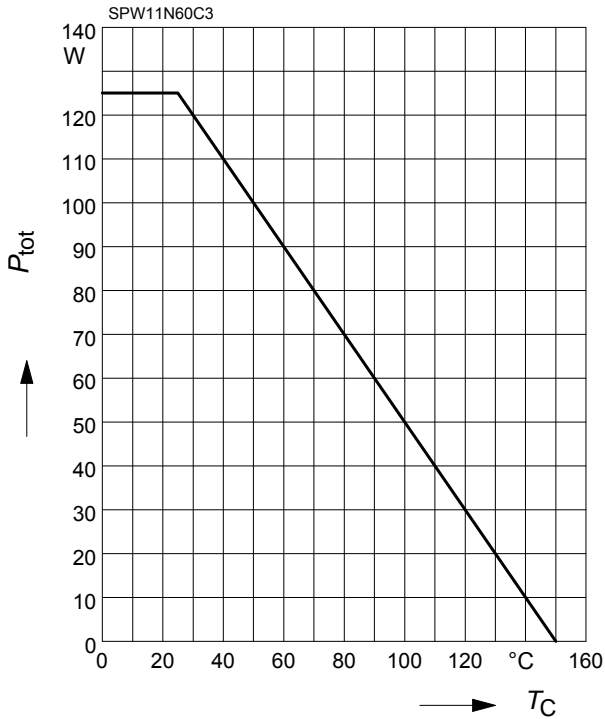
<sup>2</sup>  $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>3</sup>  $C_{o(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .



**1 Power dissipation**

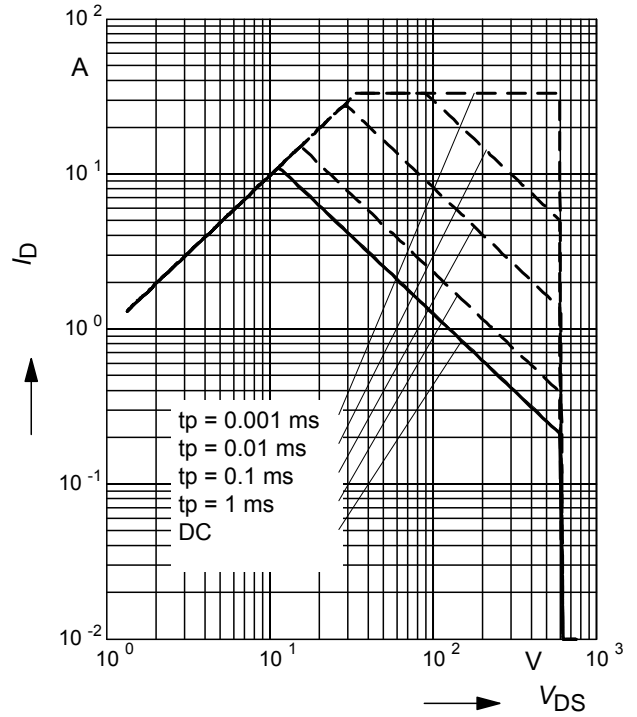
$$P_{tot} = f(T_C)$$



**2 Safe operating area**

$$I_D = f(V_{DS})$$

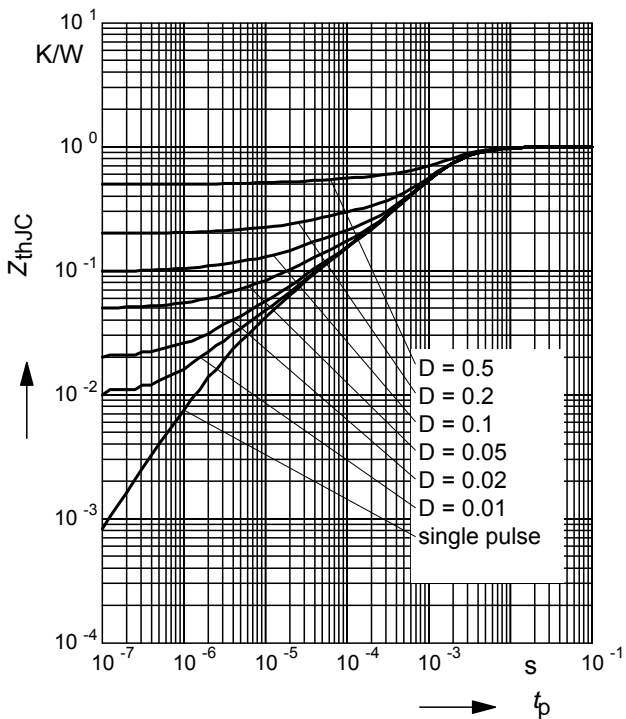
parameter :  $D = 0$  ,  $T_C = 25^\circ\text{C}$



**3 Transient thermal impedance**

$$Z_{thJC} = f(t_p)$$

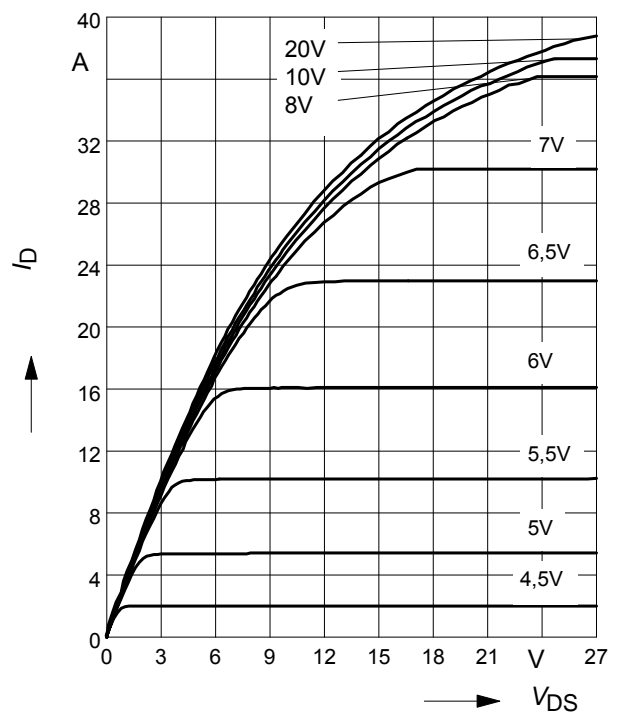
parameter:  $D = t_p/T$



**4 Typ. output characteristic**

$$I_D = f(V_{DS}); T_j = 25^\circ\text{C}$$

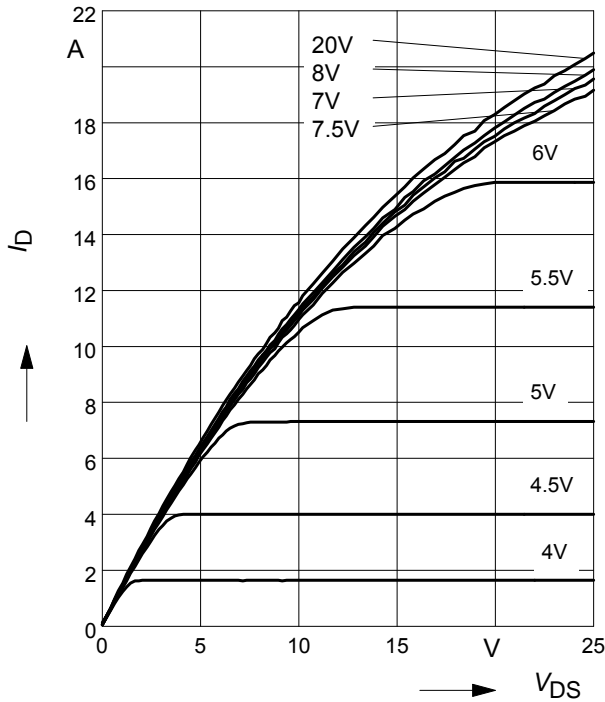
parameter:  $t_p = 10 \mu\text{s}$  ,  $V_{GS}$



**5 Typ. output characteristic**

$I_D = f(V_{DS}); T_j = 150^\circ\text{C}$

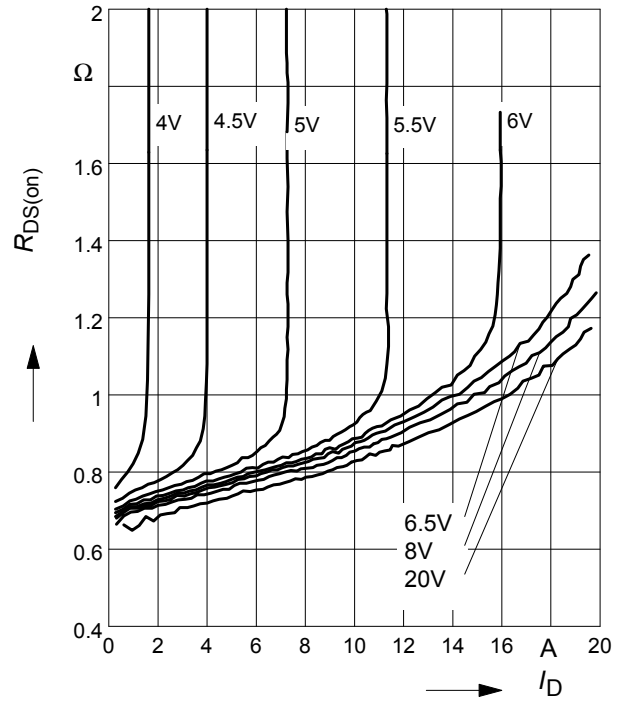
parameter:  $t_p = 10 \mu\text{s}, V_{GS}$



**6 Typ. drain-source on resistance**

$R_{DS(on)} = f(I_D)$

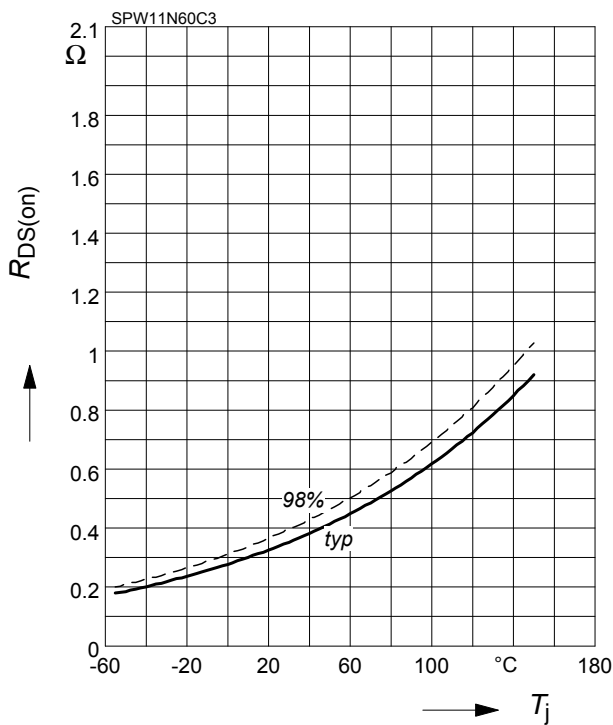
parameter:  $T_j = 150^\circ\text{C}, V_{GS}$



**7 Drain-source on-state resistance**

$R_{DS(on)} = f(T_j)$

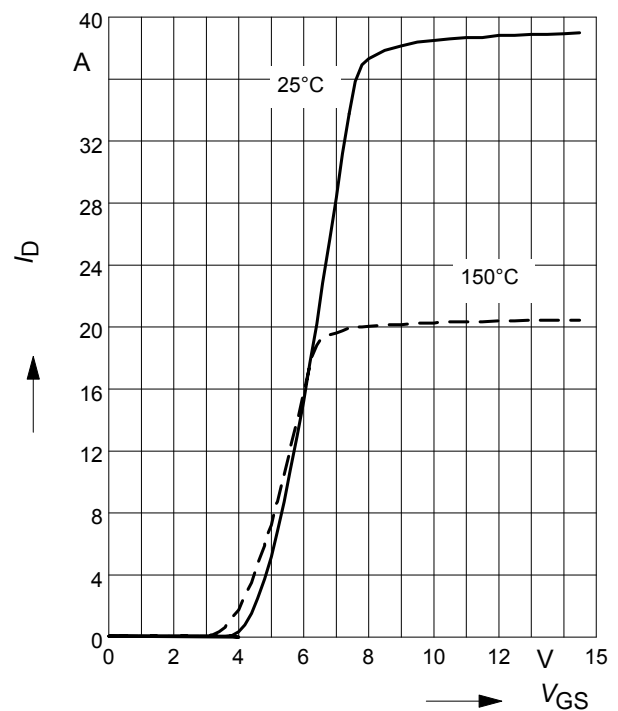
parameter:  $I_D = 7 \text{ A}, V_{GS} = 10 \text{ V}$



**8 Typ. transfer characteristics**

$I_D = f(V_{GS}); V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$

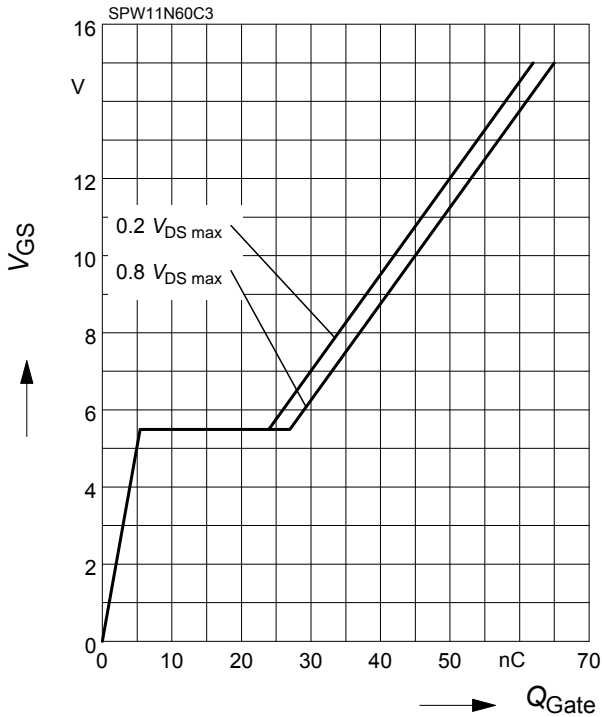
parameter:  $t_p = 10 \mu\text{s}$



**9 Typ. gate charge**

$V_{GS} = f(Q_{Gate})$

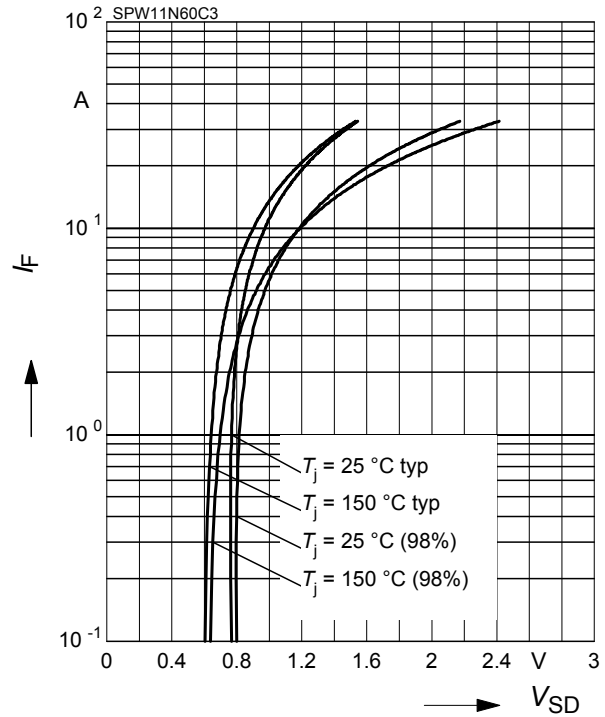
parameter:  $I_D = 11$  A pulsed



**10 Forward characteristics of body diode**

$I_F = f(V_{SD})$

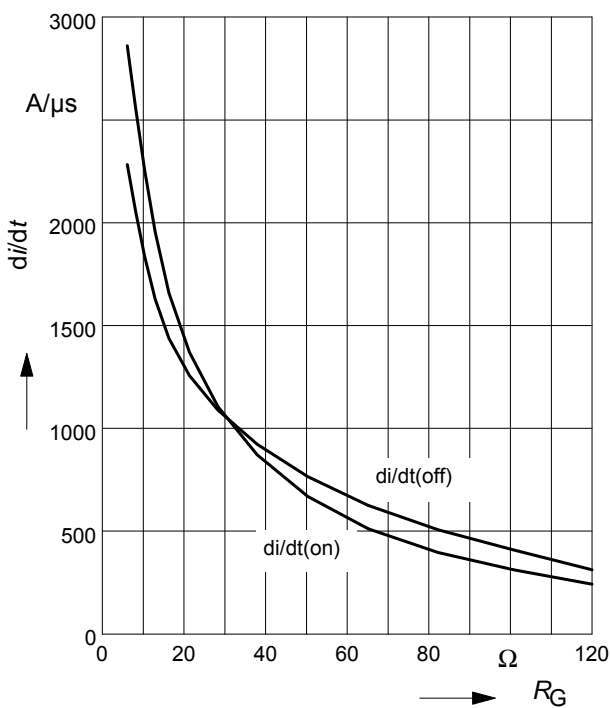
parameter:  $T_j, t_p = 10 \mu s$



**11 Typ. drain current slope**

$di/dt = f(R_G)$ , inductive load,  $T_j = 125^\circ C$

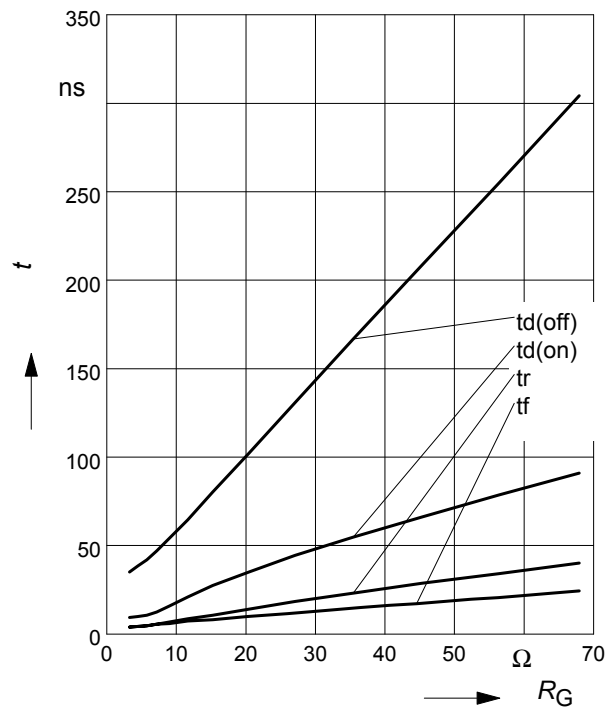
par.:  $V_{DS}=380V, V_{GS}=0/+13V, I_D=11A$



**12 Typ. switching time**

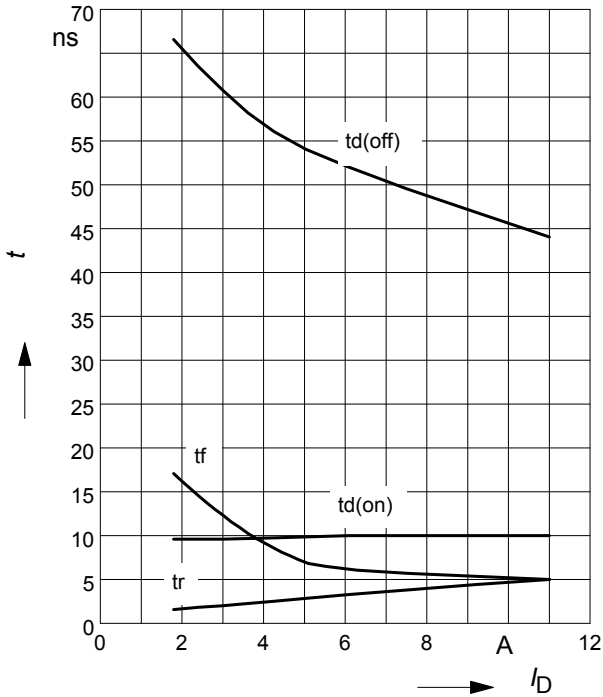
$t = f(R_G)$ , inductive load,  $T_j=125^\circ C$

par.:  $V_{DS}=380V, V_{GS}=0/+13V, I_D=11 A$



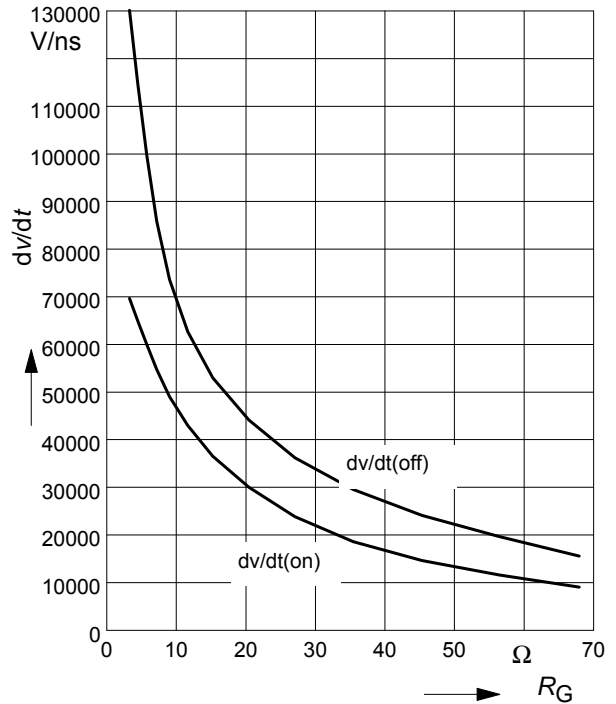
**13 Typ. switching time**

$t = f(I_D)$ , inductive load,  $T_j=125^\circ\text{C}$   
 par.:  $V_{DS}=380\text{V}$ ,  $V_{GS}=0/+13\text{V}$ ,  $R_G=6.8\Omega$



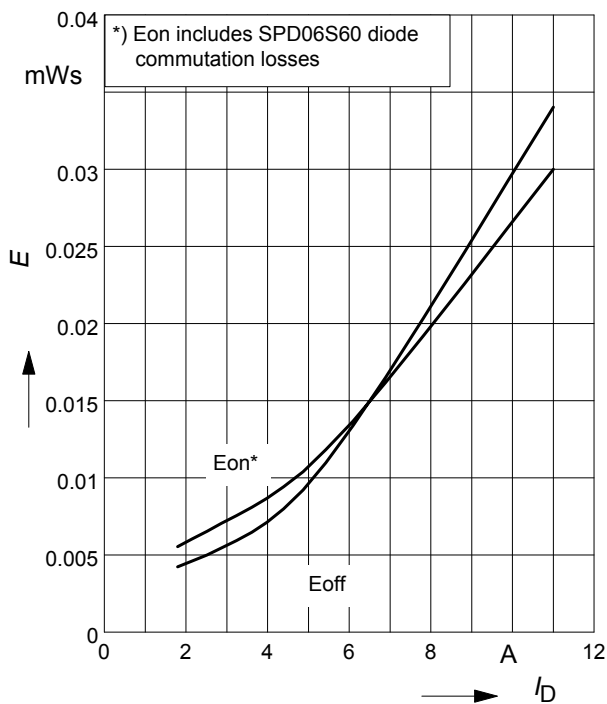
**14 Typ. drain source voltage slope**

$dv/dt = f(R_G)$ , inductive load,  $T_j = 125^\circ\text{C}$   
 par.:  $V_{DS}=380\text{V}$ ,  $V_{GS}=0/+13\text{V}$ ,  $I_D=11\text{A}$



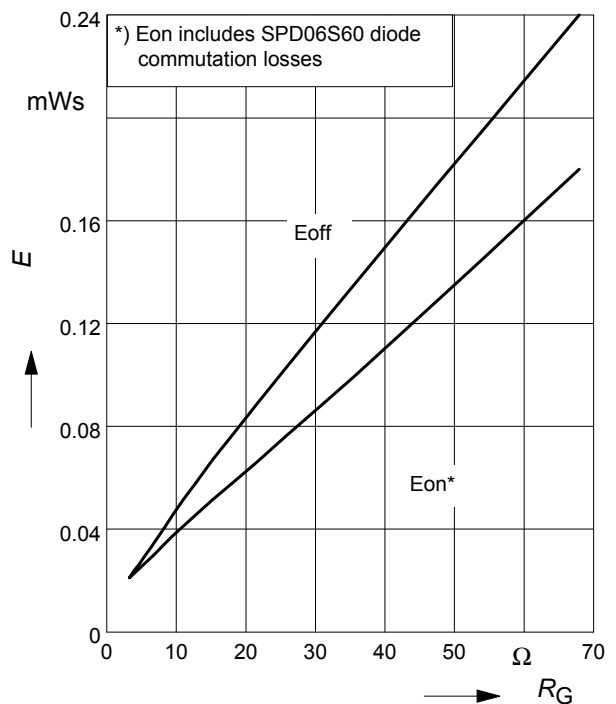
**15 Typ. switching losses**

$E = f(I_D)$ , inductive load,  $T_j=125^\circ\text{C}$   
 par.:  $V_{DS}=380\text{V}$ ,  $V_{GS}=0/+13\text{V}$ ,  $R_G=6.8\Omega$



**16 Typ. switching losses**

$E = f(R_G)$ , inductive load,  $T_j=125^\circ\text{C}$   
 par.:  $V_{DS}=380\text{V}$ ,  $V_{GS}=0/+13\text{V}$ ,  $I_D=11\text{A}$

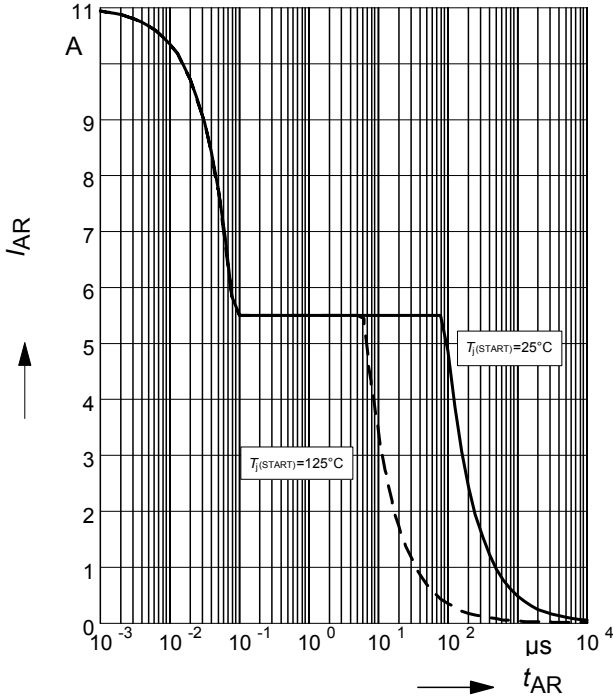




**17 Avalanche SOA**

$I_{AR} = f(t_{AR})$

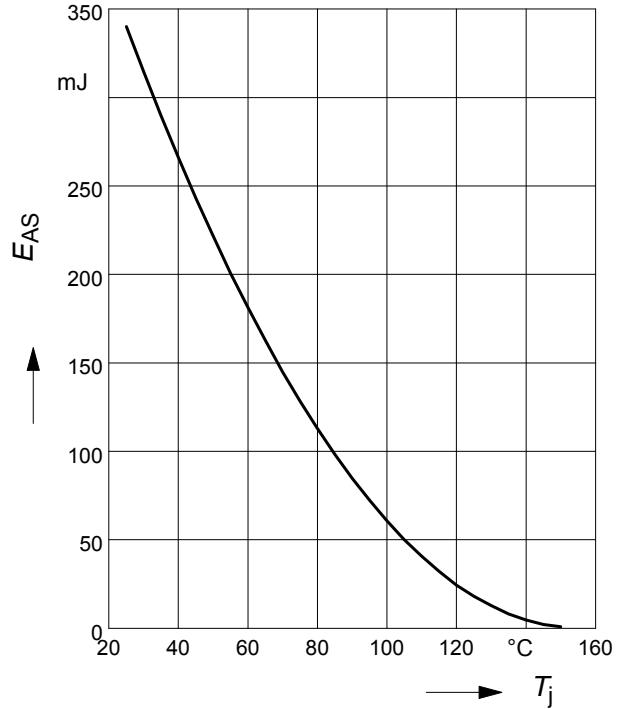
par.:  $T_j \leq 150\text{ }^\circ\text{C}$



**18 Avalanche energy**

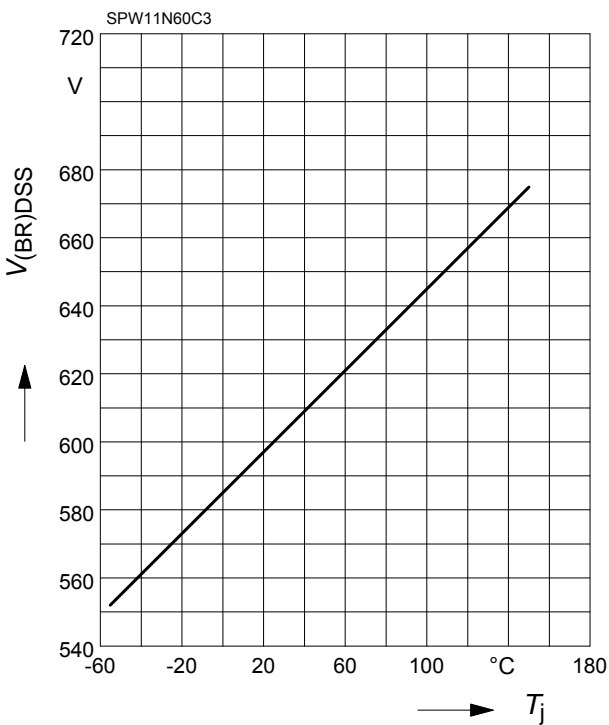
$E_{AS} = f(T_j)$

par.:  $I_D = 5.5\text{ A}$ ,  $V_{DD} = 50\text{ V}$



**19 Drain-source breakdown voltage**

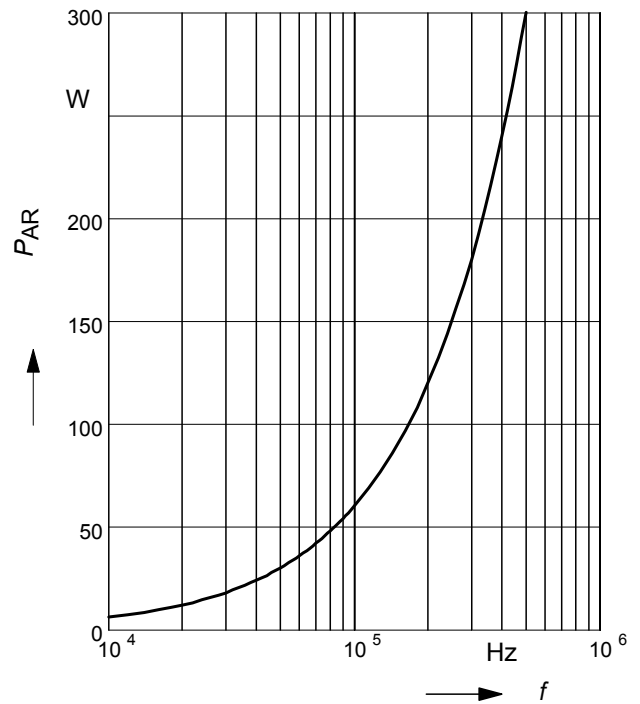
$V_{(BR)DSS} = f(T_j)$



**20 Avalanche power losses**

$P_{AR} = f(f)$

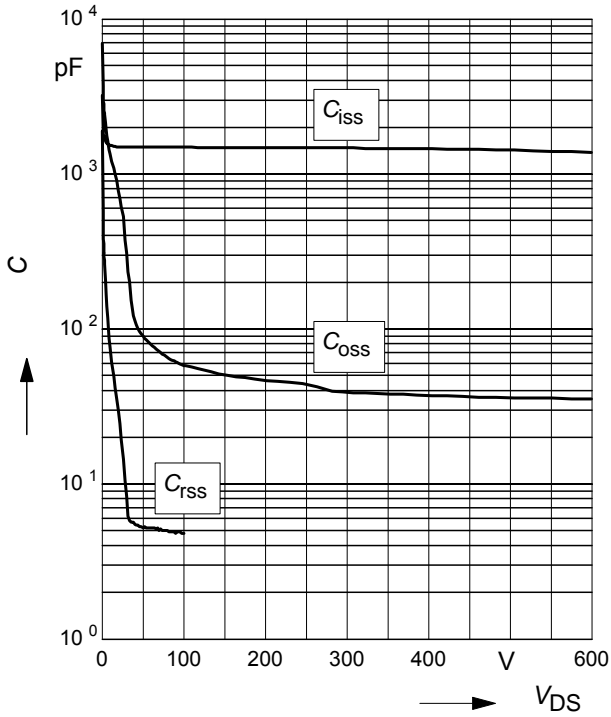
parameter:  $E_{AR} = 0.6\text{ mJ}$



**21 Typ. capacitances**

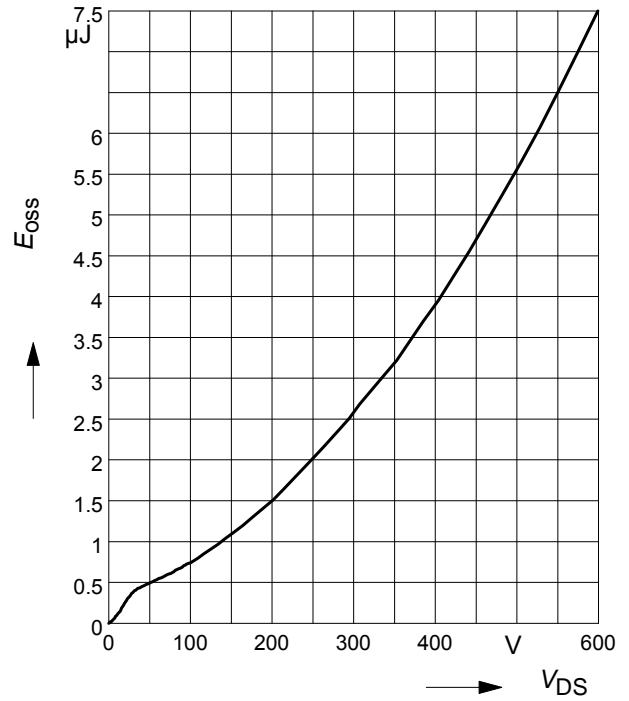
$C = f(V_{DS})$

parameter:  $V_{GS}=0V, f=1\text{ MHz}$

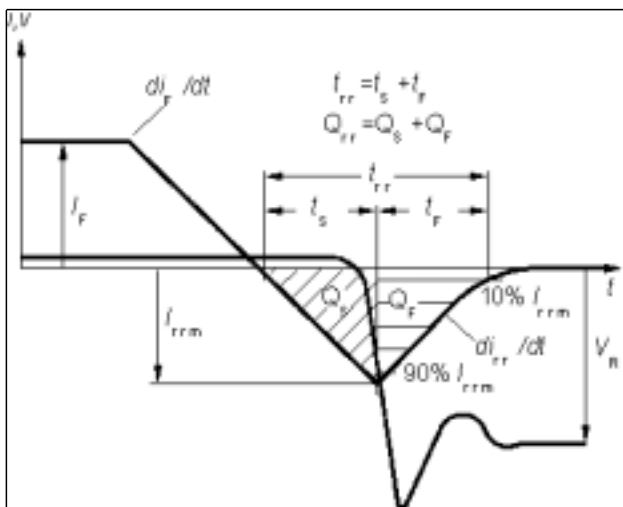


**22 Typ. C<sub>OSS</sub> stored energy**

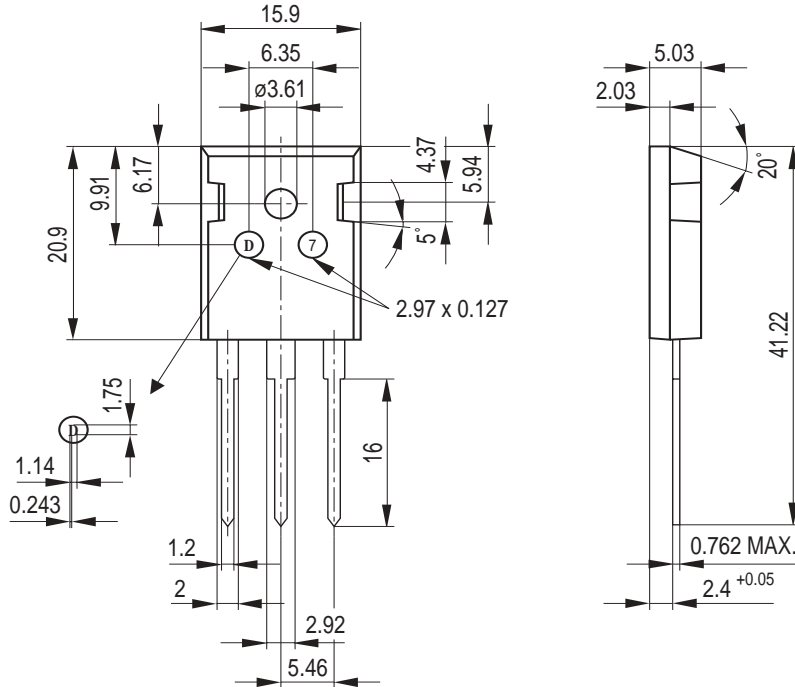
$E_{Oss}=f(V_{DS})$



Definition of diodes switching characteristics



P-TO-247-3-1



General tolerance unless otherwise specified: Leadframe parts:  $\pm 0.05$   
 Package parts:  $\pm 0.12$

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