4-bit Latch/4-to-16-Line Decoder

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Description

This device presents a 4 to 16 line decoder with latched inputs. The HD74AC4514 presents a high level at the selected output.

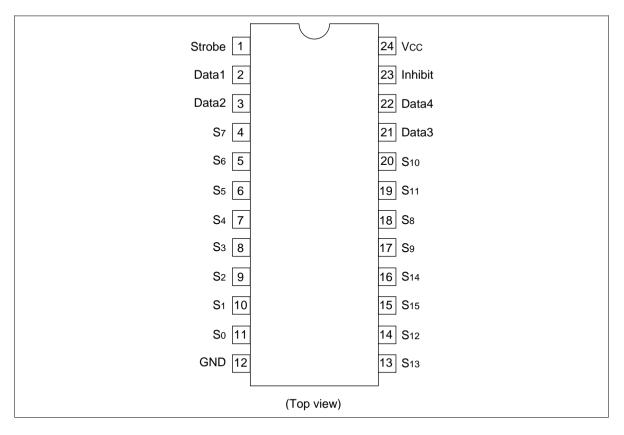
This device consists of four storage latches with common strobe and inhibit (\overline{G}) inputs. When a low signal is applied to the strobe input, the input data is stored, decoded, and presented to the output. When strobe is high, all sixteen HD74HC4514 outputs are at a low logic level.

Feature

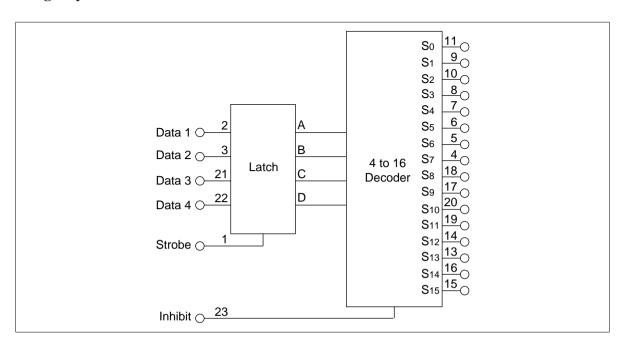
• Outputs Source/Sink 24 mA



Pin Arrangement



Logic Symbol



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Pin Names

D₁ to D₄ Data Inputs

Strobe Data Strobe Input

Data1 to 4 Data Inputs S_0 to S_{15} Outputs

Inhibit Data Enable Input

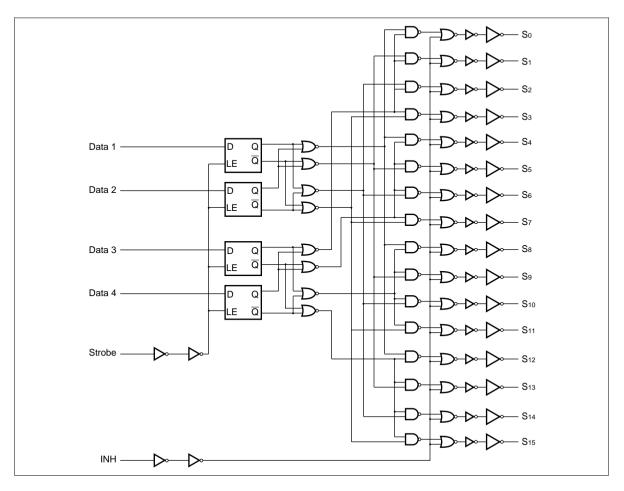
Function Table (Strobe = High)

Data Inputs

		-				
Inhibit	D	С	В	Α	Select Outputs	
L	L	L	L	L	S ₀	
L	L	L	L	Н	S₁	
L	L	L	Н	L	S ₂	
L	L	L	Н	Н	S ₃	
L	L	Н	L	L	S ₄	
L	L	Н	L	Н	S ₅	
L	L	Н	Н	L	S ₆	
L	L	Н	Н	Н	S ₇	
L	Н	L	L	L	S ₈	
L	Н	L	L	Н	S ₉	
L	Н	L	Н	L	S ₁₀	
L	Н	L	Н	Н	S ₁₁	
L	Н	Н	L	L	S ₁₂	
L	Н	Н	L	Н	S ₁₃	
L	Н	Н	Н	L	S ₁₄	
L	Н	Н	Н	Н	S ₁₅	
Н	Х	Х	Χ	Х	All output "L"	

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Logic Diagram



DC Characteristics (unless otherwise specified)

Item	Symbol	Max	Unit	Condition
Maximum quiescent supply current	I _{cc}	80	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = Worst case
Maximum quiescent supply current	I _{cc}	8.0	μΑ	$V_{IN} = V_{CC}$ or ground, $V_{CC} = 5.5 \text{ V}$, Ta = 25°C

AC Characteristics: HD74AC4514

			Ta = + C _L = 50			Ta = −4 C _L = 50	0°C to +85°C pF	
Item	Symbol	V _{cc} (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	12.0	15.5	1.0	17.0	ns
D_n to \overline{O}_n		5.0	1.0	9.0	11.0	1.0	12.0	
Propagation delay	t _{PHL}	3.3	1.0	12.5	15.5	1.0	17.0	ns
D_n to \overline{O}_n		5.0	1.0	9.0	11.0	1.0	12.0	
Propagation delay	t _{PLH}	3.3	1.0	9.5	15.0	1.0	16.0	ns
\overline{OE} to \overline{O}_{n}		5.0	1.0	7.0	10.5	1.0	11.5	
Propagation delay	t _{PHL}	3.3	1.0	9.0	15.0	1.0	16.5	ns
\overline{OE} to \overline{O}_{n}		5.0	1.0	6.5	10.5	1.0	11.5	
Propagation delay	t _{PLH}	3.3	1.0	14.0	19.0	1.0	21.0	ns
$\overline{\text{LE}}$ to $\overline{\text{O}}_{\text{n}}$		5.0	1.0	10.0	13.5	1.0	15.0	
Propagation delay	t _{PHL}	3.3	1.0	14.5	19.0	1.0	21.0	ns
$\overline{\text{LE}}$ to $\overline{\text{O}}_{\text{n}}$		5.0	1.0	10.5	13.5	1.0	15.0	

Note: 1. Voltage Range 3.3 is 3.3 V \pm 0.3 V Voltage Range 5.0 is 5.0 V \pm 0.5 V

AC Operating Requirements: HD74AC4514

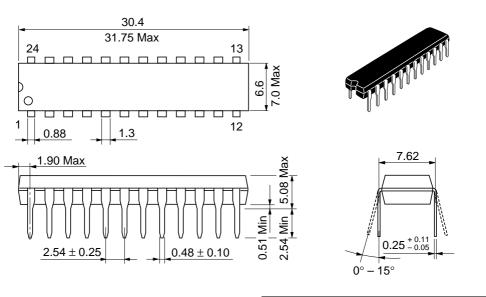
			Ta = +25°C C _L = 50 pF		Ta = -40°C to +85°C C _L = 50 pF	
Item	Symbol	V _{cc} (V)*1	Тур	Guaranteed	Minimum	Unit
Setup time, HIGH or LOW	t _{su}	3.3	1.5	3.5	4.0	ns
D _n to Strobe		5.0	1.0	3.0	3.5	_
Hold time, HIGH or LOW	t _h	3.3	-1.0	2.5	3.0	ns
D _n to Strobe		5.0	-0.5	1.5	2.0	_
Pulse width, HIGH	t _w	3.3	3.0	5.5	7.0	ns
		5.0	3.0	4.5	5.0	_

Note: 1. Voltage Range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$

Capacitance

Item	Symbol	Тур	Unit	Condition
Input capacitance	C _{IN}	4.5	pF	V _{CC} = 5.5 V
Power dissipation capacitance	C_{\scriptscriptstylePD}	10.0	pF	V _{CC} = 5.0 V

Unit: mm



Hitachi Code	DP-24N
JEDEC	_
EIAJ	Conforms
Weight (reference value)	1.84 g

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