

SANYO

No.3793

2SC4440

NPN Triple Diffused Planar Silicon Transistor
 Very High-Definition Monochrome Display
 Horizontal Deflection Output Applications

Features

- High reliability (Adoption of HVP process).
- Fast switching speed.
- High blocking voltage.
- Wide ASO.
- Adoption of MBIT process.
- Micaless package facilitating easy mounting.

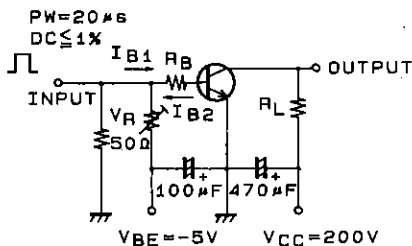
Absolute Maximum Ratings at Ta = 25°C

			unit
Collector-to-Base Voltage	V _{CB0}	600	V
Collector-to-Emitter Voltage	V _{CEO}	400	V
Emitter-to-Base Voltage	V _{EBO}	7	V
Collector Current	I _C	7	A
Collector Current (Pulse)	I _{CP}	PW ≤ 300 μs, duty cycle ≤ 10%	14 A
Collector Dissipation	P _C	2.0	W
		T _c = 25°C	
Junction Temperature	T _j	30	W
Storage Temperature	T _{stg}	150	°C
		-55 to +150	°C

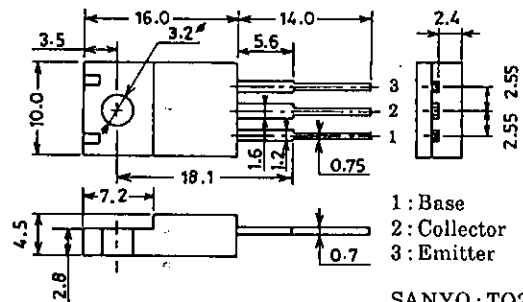
Electrical Characteristics at Ta = 25°C

			min	typ	max	unit
Collector Cutoff Current	I _{CB0}	V _{CB} = 400V, I _E = 0			10	μA
	I _{CES}	V _{CE} = 600V			0.5	mA
Collector Sustain Voltage	V _{CEO(sus)}	I _C = 100mA, I _B = 0	400			V
Emitter Cutoff Current	I _{EBO}	V _{EB} = 5V, I _C = 0			1	mA
C-E Saturation Voltage	V _{CE(sat)}	I _C = 4A, I _B = 0.8A			0.8	V
B-E Saturation Voltage	V _{BE(sat)}	I _C = 4A, I _B = 0.8A			1.5	V
DC Current Gain	h _{FE(1)}	V _{CE} = 5V, I _C = 0.8A	15			
	h _{FE(2)}	V _{CE} = 5V, I _C = 4A	10		20	
Gain-Bandwidth Product	f _T	V _{CE} = 10V, I _C = 0.8A		20		MHz
Storage Time	t _{stg}	I _C = 5A, I _{B1} = 1A, I _{B2} = -2A			3.0	μs
Fall Time	t _f	R _L = 40Ω, V _{CC} = 200V			0.2	μs

Switching Time Test Circuit

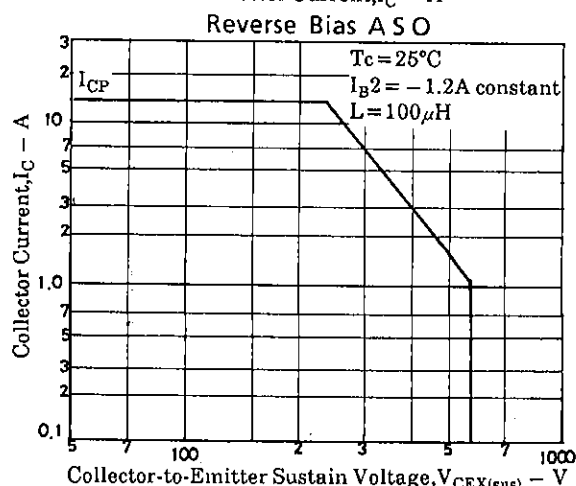
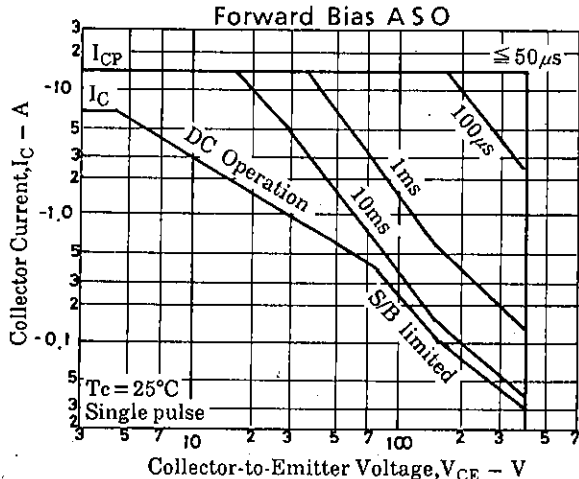
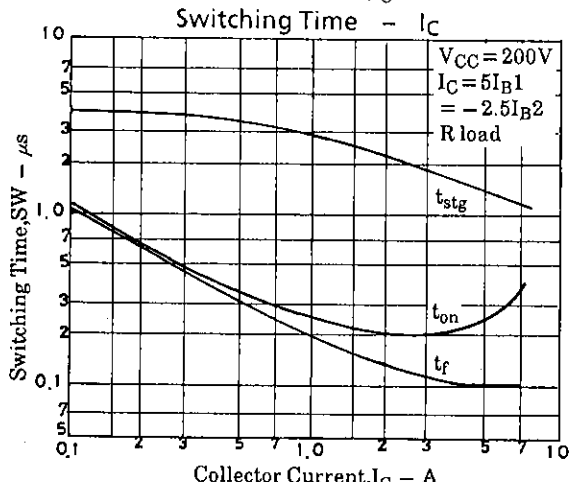
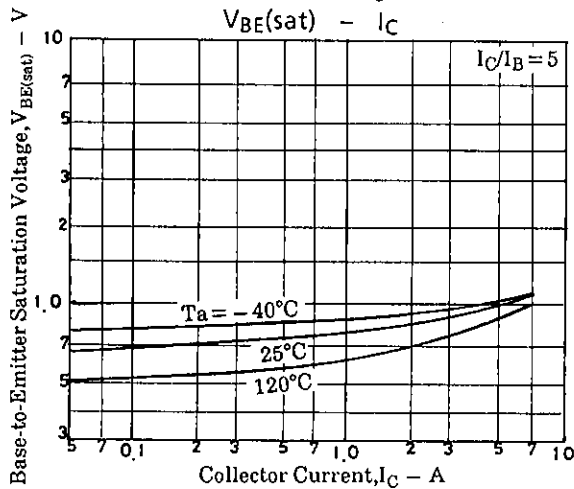
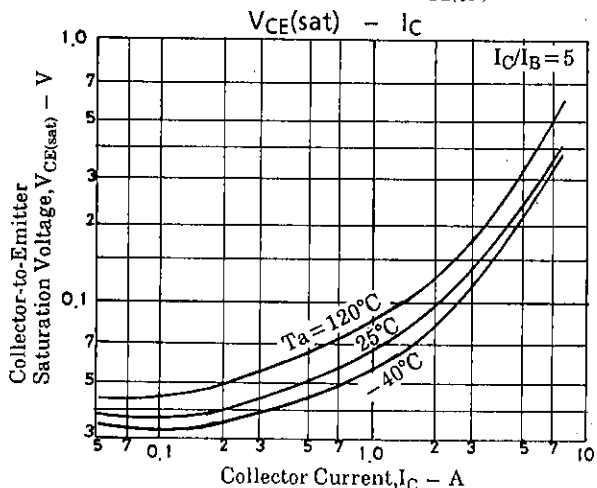
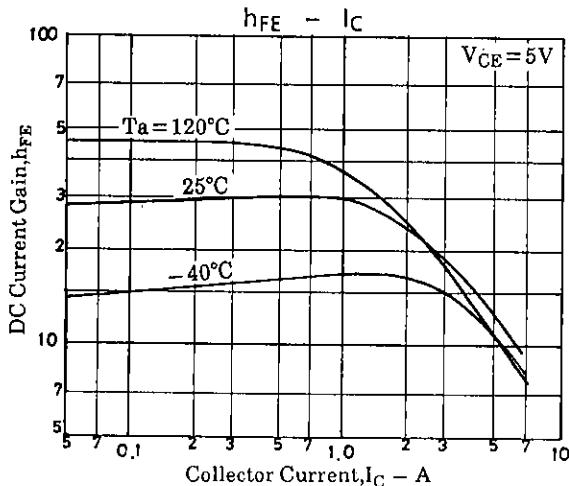
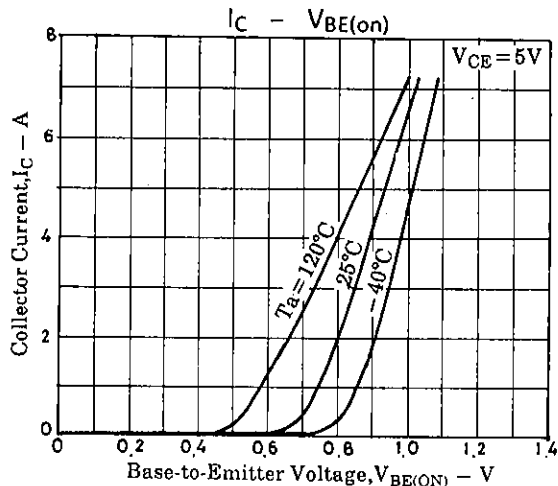
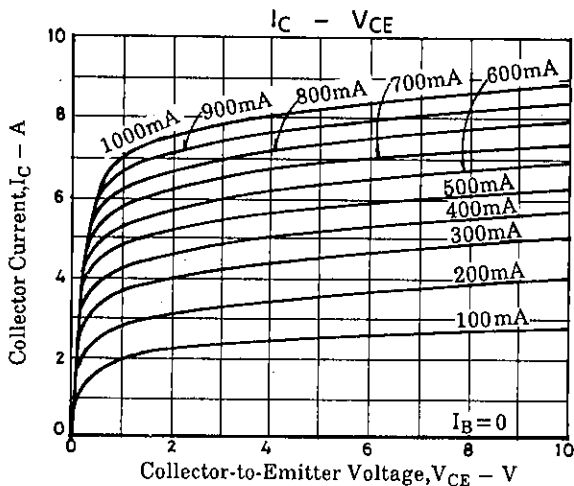


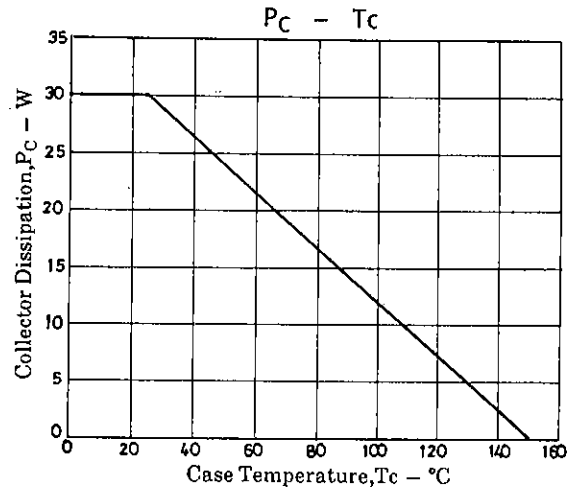
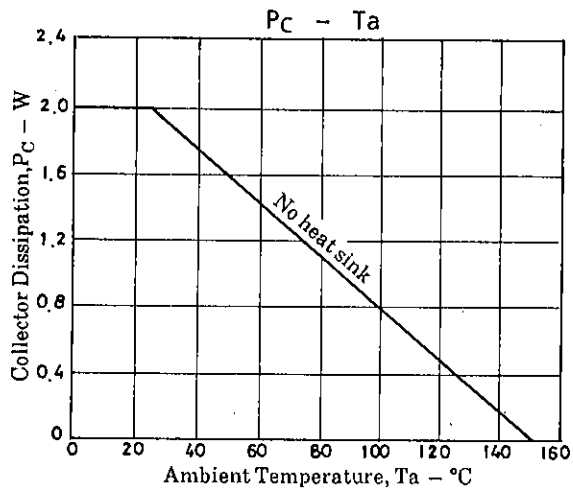
Package Dimensions 2041A
(unit : mm)



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