

ECN3053F

ECN3053 is a single chip driver IC which has 6 MOS-Gated devices for its output. It is suitable for controlling 3-phase DC brushless motors and 3-phase induction motors.

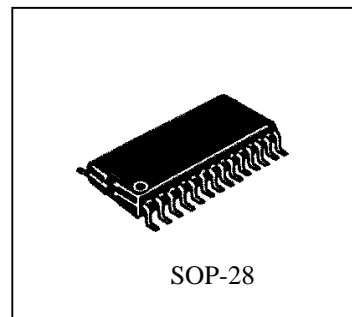
Functions

- Lower arm Over current (OC) Protection
- Under voltage Protection
- Fault Output function

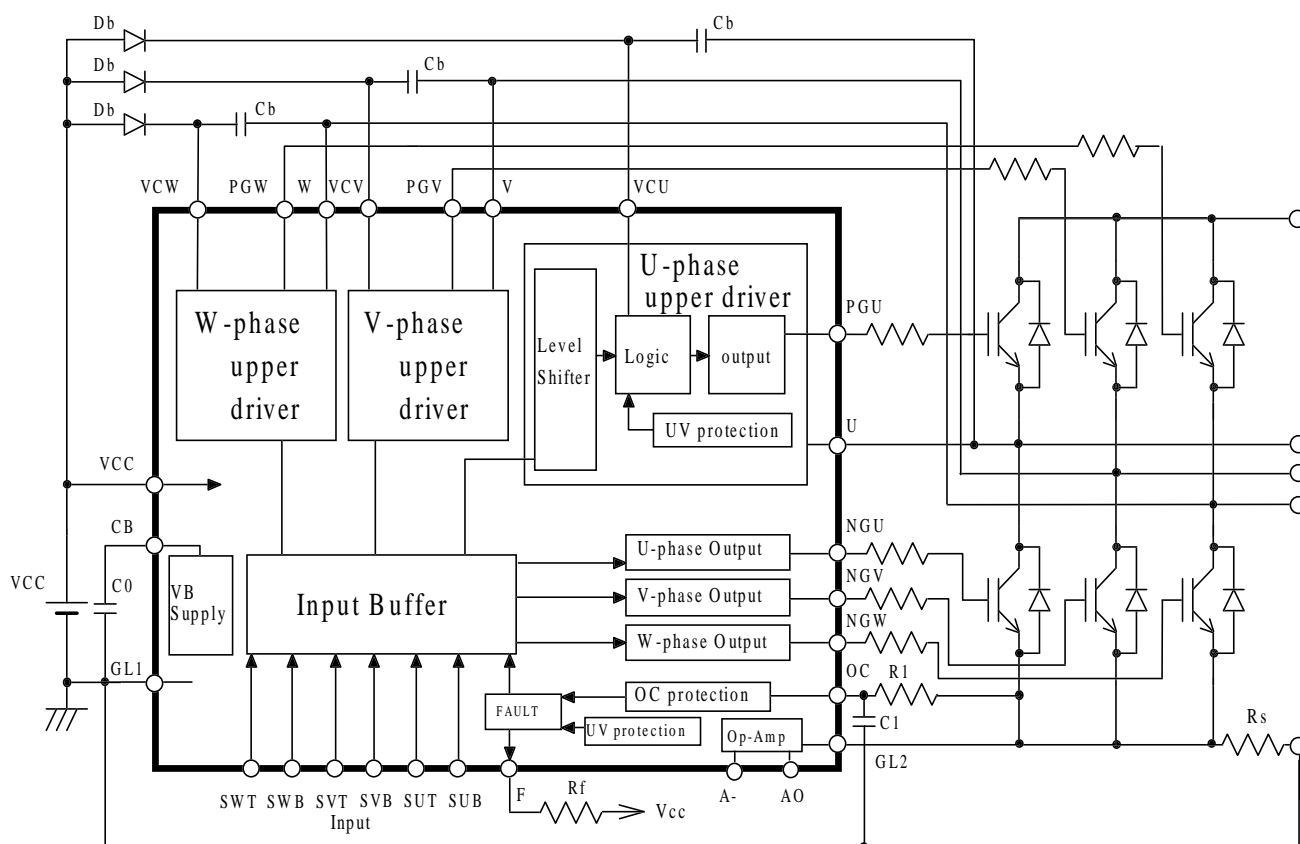
Features

- It can be controlled by PWM with 6 inputs from an external microprocessor.
- 6 logic inputs are compatible with 5V CMOS and LSTTL outputs.

Package



Block Diagram



ECN3053F

1. General

This specification shall be applied to the following semiconductor integrated circuits.

- 1) Type : ECN3053F
- 2) Application : 3-phase Brushless Motor, 3-phase Induction Motor
- 3) Structure : Monolithic IC
- 4) Package : SOP28

2. Maximum Allowable Ratings (Ta=25°C)

No.	ITEMS	SYMBOLS	UNIT	VALUES	CONDITIONS
1	Output Device Breakdown voltage	Vbv	V	620	Between Vcu,v,w & GL1
2	GL2 terminal voltage	Vgl2	V	-5~Vcc	Vcc=18Vmax at GL2=-5V
3	U,V,W terminal Voltage	Vu,v,w	V	-5~600	
4	Supply voltage	Vcc	V	20	
5	Input voltage	Vin	V	-0.5~Vcc+0.5	
6	Operating Junction Temperature	Tjop	°C	-20~125	
7	Storage Temperature	Tstg	°C	-40~150	

Note 1 : Thermal resistance Rja

PACKAGE	SOP28	Unit
Single	121	°C/W
Mounted	84	°C/W
PCB size,Density *	120x21x1.6 (30%)	mm

* This figure varies depending on the mounting condition.

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3. Electrical Characteristics

Unless otherwise noted, $T_a=25\text{ }^\circ\text{C}$, $V_{u,v,w}$ to $GL1=374\text{V}$, $V_{cc}=15\text{V}$. (suffix T=top, B=bottom arm)

No	ITEMS	SYMBOLS	UNIT	MIN.	TYP.	MAX.	CONDITIONS
1	Stand-by current	Is1	mA	-	6.5	10	$V_{in}=H$ or L, Between V_{cc} -GL1
		Is2	μA	-	15	30	Between V_{cu} -U, V_{cv} -V, V_{cw} -W 15V, $V_{in}=H$ or L
2	Input Voltage (Output is L)	V_{IH}	V	3.5	-	-	Input=H or L
	Input Voltage (Output is H)	V_{IL}	V	-	-	1.5	
3	Output Source Current	I_{o+}	A	0.2	0.25	-	V_{cu} -PGU, V_{cv} -PGV, V_{cw} -PGW =15V, V_{cc} -NGU, V, W =15V PW<10 μs
4	Output Sink Current	I_{o-}	A	0.4	0.5	-	PGU-U, PGV-V, PGW-W =15V, NGU, V, W -GL2=15V PW<10 μs
5	High level Output Voltage	V_{OH}	mV	-	-	100	$V_{cu,v,w}$ -PGU, V, W & V_{cc} -NGU, V, W $V_{in}=0$, $I_{o}=0$
6	Low level Output Voltage	V_{OL}	mV	-	-	100	PGU, V, W-U, V, W & NGU, V, W -GL2 $V_{in}=5\text{V}$, $I_{o}=0$
7	Leakage Current at HV terminal	IL	μA	-	-	50	$V_{cu,v,w}=U, V, W=600\text{V}$
8	Input Current	I_{IL}	μA	-200	-	-	$V_{in}=0\text{V}$ Internal Pull up R=200k Ω
9	Input Current	I_{IH}	μA	-120	-	-	$V_{in}=5\text{V}$ Internal Pull up R=200k Ω
10	Vcc Under voltage	Negative Going	V_{uvb}	V	9.5	10.5	11.6
		Reset Hysteresis	V_{rhb}	V	0.1	0.4	0.9
11	$V_{cu,v,w}$ Under Voltage	Negative Going	V_{uvt}	V	8.9	10.5	12.1
		Reset Hysteresis	V_{rht}	V	0.1	0.4	0.9
12	OC Input Positive Going threshold	V_{oc}	V	0.44	0.49	0.54	
13	Fault Output On Resistance	R_{onf}	Ω	-	300	400	F-GL1=0.5V
14	Turn On Delay Time	t_{on}	μs	-	0.8	1.5	CL=1000pF RL=0
15	Turn Off Delay Time	t_{off}	μs	-	0.5	1.2	CL=1000pF RL=0
16	OC Output to Output Shutdown Delay	t_{oc}	μs	-	0.7	1.7	CL=1000pF RL=0
17	OC to Fault Delay	t_{flt}	μs	-	0.6	1.6	CL=1000pF RL=0
18	Fault Reset Delay Time	t_{flrs}	μs	6.5	10	20	CL=1000pF RL=0
19	Fault Output terminal Voltage	V_{flt}	V	-0.5	-	$V_{cc}+0.5$	
20	Fault Output Sink Current	I_{flt}	mA	4	-	-	$V(F&GL)=2\text{V}$
21	VB Output Voltage	VB	V	6.8	7.5	8.2	
22	VB Output Current	IB	mA	25	-	-	Delta $V_{load}=0.1\text{V}$
23	Op-Amp Input Offset Voltage	V_{os}	mV	-	-	30	GL2=A-=0.2V
24	Op-Amp High Level Output Voltage	V_{OHa}	V	5.0	7.5	-	A-=0V GL2=1V
25	Op-Amp Low Level Output Voltage	V_{OLa}	mV	-	-	20	A-=1V GL2=0V
26	Op-Amp Output Source Current	I_{srca}	mA	1.0	-	-	A-=0V GL2=1V AO=4V
27	Op-Amp Output Sink Current	I_{snka}	mA	1.0	-	-	A-=1V GL2=0V AO=2V

Note 1: V_{uvb} , V_{rhb} , V_{uvt} and V_{rht} are defined and shown in Fig. 1.

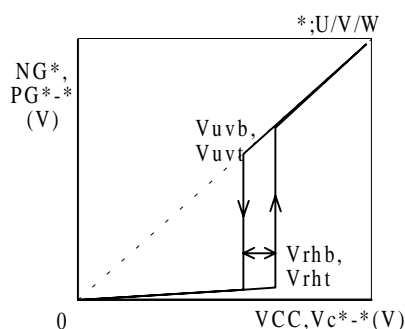


Fig 1. Negative going and reset voltage and hysteresis for the top and bottom arm under voltage circuit

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4. Truth Table

INPUT		OC Input	U phase		V phase		W phase		
			Top arm	Bot.arm	Top arm	Bot.arm	Top arm	Bot.arm	
SUT	L	L	ON	-	-	-	-	-	
	H		OFF	-	-	-	-	-	
SUB	L		-	ON	-	-	-	-	
	H		-	OFF	-	-	-	-	
SVT	L		-	-	ON	-	-	-	
	H		-	-	OFF	-	-	-	
SVB	L		-	-	-	ON	-	-	
	H		-	-	-	OFF	-	-	
SWT	L		-	-	-	-	ON	-	
	H		-	-	-	-	OFF	-	
SWB	L		-	-	-	-	-	ON	
	H		-	-	-	-	-	OFF	
-	-		H	OFF	OFF	OFF	OFF	OFF	OFF
SUT,SUB	L		-	OFF	OFF	-	-	-	-
SVT,SVB	L		-	-	-	OFF	OFF	-	-
SWT,SWB	L		-	-	-	-	-	OFF	OFF

Note 1: Fault output level is referenced Low when over current or under voltage for Vcc is detected.

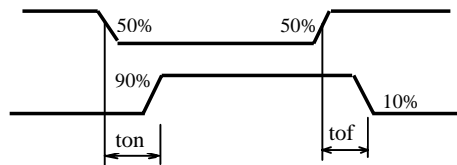
Note 2: Over current protection works when the voltage drop of the external sensing resistor exceeds the threshold voltage Voc(typical 0.49V). In this case all six outputs are turned off and Fault output level becomes low.

Reset after detection is done by feeding high signal to all six inputs or re-supplying Vcc voltage.

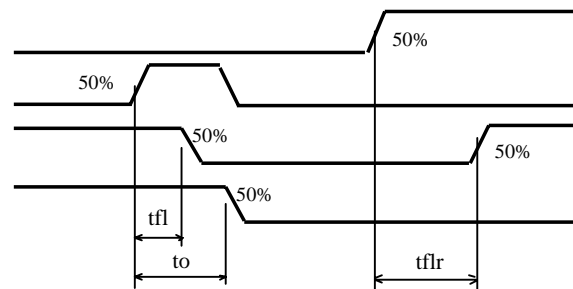
Note 3: The output signal for Fault is reset by feeding high signal to all six inputs.

5. Definition of switching delay

Input
(SUB,T, SVB,T, SWB,T)
Output
(PGU,V,W, NGU,V,W)



Input
OC
F
Output
(PGU,V,W NGU,V,W)



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6. Recommended Operating Conditions

No.	ITEMS	SYMBOLS	UNIT	VALUES TOL.	CONDITIONS
1	Power Supply Voltage	Vcc	V	13.5~16.5	
2	PWM Frequency	fpwm	kHz	1~20	
3	VB Smoothing Capacitor	Co	μF	> 0.22	Stress voltage:VB
4	Boot Strap Capacitor	Cb	μF	3.3	Stress voltage:Vcc
5	Boot Strap Diodes	Db		Hitachi DFG1C6 DFM1F6 or equivalent	600V/1.0A ≤ 0.1μs
6	Sensing Resistor	Rs	Ω	Note 1	
7	OC Filtering Resistor	R1	Ω	Note 2	
8	OC Filtering Capacitor	C1	μF	Note 2	
9	Load resistor for F terminal	Rf	kΩ	≥ 5.6	

Note 1. Over-current detection level is determined by the following equation

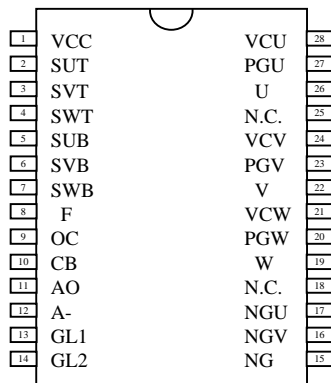
$$I_{oc} = V_{oc} / R_s$$

Note.2 This IC has filters of 0.4us for noise reduction.

But appropriate R1, C1 should be added when noise can not be removed.

7. Pin Assignment

<SOP28>



8. Outline Drawing

< SOP28 >

