

UTC LD3870 LINEAR INTEGRATED CIRCUIT

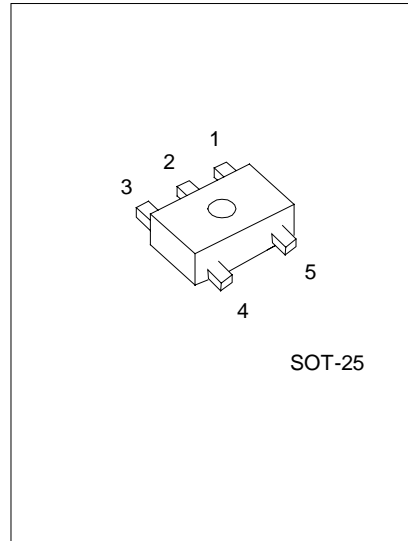
LOW DROPOUT VOLTAGE REGULATOR

DESCRIPTION

The UTC LD3870 is low dropout voltage regulator designed for cellular phone application.

FEATURES

- * High Ripple Rejection: 56dB RR(DC<f<60kHz)
66dB typ. (f=100Hz)
60dB typ. (f=1kHz)
- * Output Noise Voltage: $V_{no}=30 \mu V$, $C_p=0.01 \mu F$
- * Output Current: $I_o(max)=150mA$
- * High Precision Output: $V_o \pm 2\%$
- * Low Dropout Voltage: $V_i-o=0.12V$ typ.
($I_o=60mA, V_o=1.8V$)
- * Input Voltage range: $+2 \sim +14V$ ($V_o=1.5V$ Version)
- * ON/OFF Control: Active High
- * Output capacitor with 4.7uF ceramic capacitor
- * Internal Short Circuit Current Limit
- * Internal Thermal Overload Protection



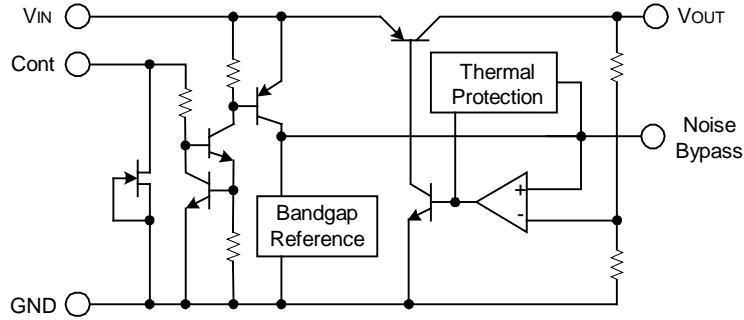
1:CONTROL(Active High) 2:GND
3:NOISE BYPASS 4:Vout 5:VIN

MARKING INFORMATION

PART NUMBER	VOLTAGE	VOLATGE CODE	PART NUMBER	VOLTAGE	VOLATGE CODE	MARKING
LD3870-1.5V	1.5V	15	LD3870-3.1V	3.1V	31	
LD3870-1.8V	1.8V	18	LD370-3.2V	3.2V	32	
LD3870-1.9V	1.9V	19	LD3870-3.3V	3.3V	33	
LD3870-2.0V	2.0V	20	LD3870-3.4V	3.4V	34	
LD3870-2.1V	2.1V	21	LD3870-3.5V	3.5V	35	
LD3870-2.3V	2.3V	23	LD3870-3.6V	3.6V	36	
LD3870-2.4V	2.4V	24	LD3870-3.8V	3.8V	38	
LD3870-2.5V	2.5V	25	LD3870-4.0V	4.0V	40	
LD3870-2.6V	2.6V	26	LD3870-4.5V	4.5V	45	
LD3870-2.7V	2.7V	27	LD3870-4.6V	4.6V	46	
LD3870-2.8V	2.8V	28	LD3870-4.7V	4.7V	47	
LD3870-2.85V	2.85V	2J	LD3870-4.8V	4.8V	48	
LD3870-2.9V	2.9V	29	LD3870-5.0V	5.0V	50	
LD3870-3.0V	3.0V	30				

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BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS (Ta=25)

PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V _{IN}	+14	V
Control Voltage	V _{CONT}	+14(Note 1)	V
Power Dissipation	P _D	200	mW
Operating Temperature	T _{opr}	-40 ~ +85	
Storage Temperature	T _{stg}	-40 ~ +125	

Note 1: When input voltage is less than +14V, the absolute maximum control voltage is equal to the input voltage.

ELECTRICAL CHARACTERISTICS (V_{IN}=V_o+1V, C_{IN}=0.1 μF, C_o=4.7 μF, C_p=0.01 μF, Ta=25)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V _o	I _o =30mA	-2%		+2%	V
Quiescent Current	I _q	I _o =0mA, expect I _{cont}		200	300	μA
Quiescent Current At Control OFF	I _{q(OFF)}	V _{CONT} =0V			100	nA
Output Current	I _o	V _o -0.3V	150	200		mA
Line Regulation	V _o / V _{IN}	V _{IN} =V _o +1V ~ V _o +6V, I _o =30mA			0.10	%/V
Load Regulation	V _o / I _o	I _o =0 ~ 100mA			0.03	%/mA
Dropout Voltage	V _{I-o}	I _o =60mA		0.12	0.2	V
Ripple Rejection	RR	e _{in} =200mVrms, f=1kHz, I _o =10mA, V _{IN} =V _o +2V, V _o =3V Version		60		dB
Average Temperature Coefficient of Output Voltage	V _o / Ta	Ta=0~85, I _o =10mA, V _o =3V Version		0.2		mV/
Output Noise Voltage	V _{NO}	f=10Hz ~ 80kHz, I _o =10mA, V _o =3V Version		30		μVrms
Control Voltage for ON-state	V _{CONT(ON)}		1.6			V
Control Voltage for OFF-state	V _{CONT(OFF)}				0.6	V

Note 2: The above specification is a common specification for all output voltages. Therefore, it may be different from the individual specification for a specific output voltage.

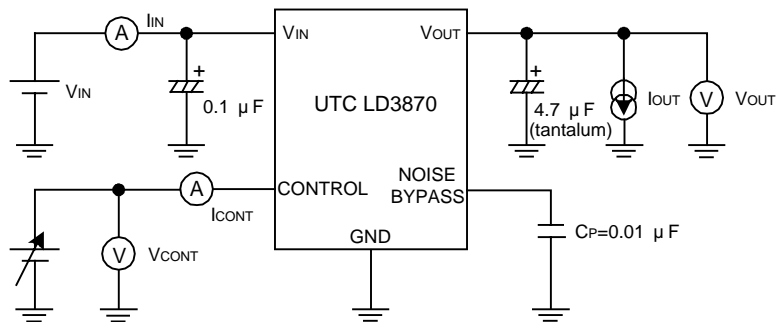
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ELECTRICAL CHARACTERISTICS

($V_o=1.5V$ Version, $V_{IN}=2.4V$, $C_{IN}=0.1 \mu F$, $C_o=4.7 \mu F$, $C_p=0.01 \mu F$, $T_a=25$)

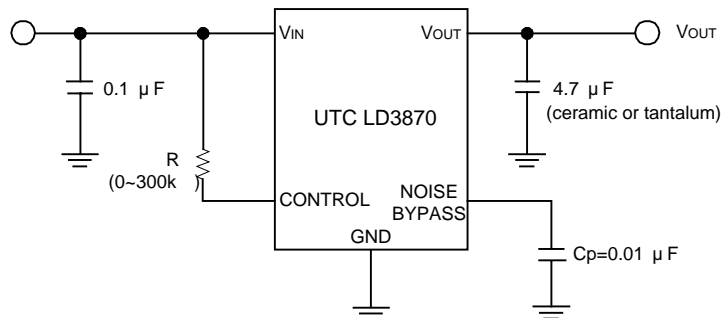
PARAMETER	SYMBOL	TEST CONDITONS	MIN.	TYP.	MAX.	UNIT
Output Voltage	V_o	$I_o=30mA$	-2%		+2%	V
Quiescent Current	I_Q	$I_o=0mA$, expect I_{cont}		200	300	μA
Quiescent Current At Control OFF	$I_{Q(OFF)}$	$V_{CONT}=0V$			100	nA
Output Current	I_o	$V_o=0.3V$	150	200		mA
Line Regulation	V_o / V_{IN}	$V_{IN}=V_o+1V \sim V_o+6V$, $I_o=30mA$			0.10	%/V
Load Regulation	V_o / I_o	$I_o=0 \sim 100mA$			0.03	%/mA
Ripple Rejection	RR	$e_{in}=200mV_{rms}$, $f=1kHz$, $I_o=10mA$, $V_{IN}=V_o+2V$		64		dB
Average Temperature Coefficient of Output Voltage	V_o / T_a	$T_a=0 \sim 85$, $I_o=10mA$		0.13		mV/
Output Noise Voltage	V_{NO}	$f=10Hz \sim 80kHz$, $I_o=10mA$		15		μV_{rms}
Control Voltage for ON-state	$V_{CONT(ON)}$		1.6			V
Control Voltage for OFF-state	$V_{CONT(OFF)}$				0.6	V

TEST CIRCUIT



TYPICAL APPLICATION

In case that ON/OFF Control is not required:

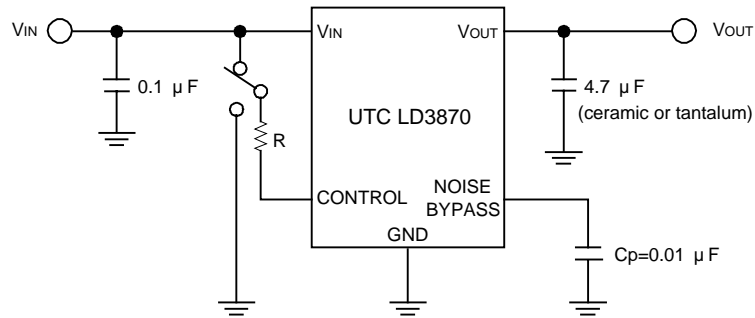


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Connect control terminal to V_{IN} terminal

The quiescent current can be reduced by using a resistance "R". Instead, it increases the minimum operating voltage. For further information, please refer to Figure "Output Voltage vs. Control Voltage".

In use of ON/OFF CONTROL:



State of control terminal:

* "H" → Output is enabled.

* "L" or "open" → Output is disabled.

* Noise bypass Capacitance C_p

Noise bypass capacitance C_p reduces noise generated by hand-gap reference circuit.

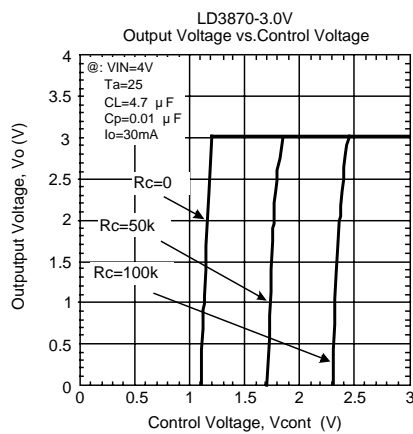
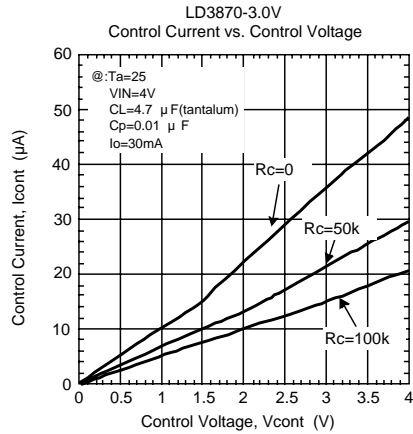
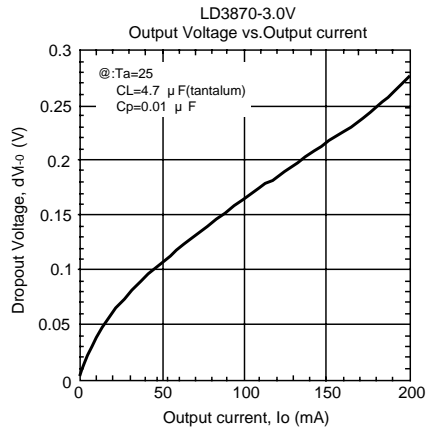
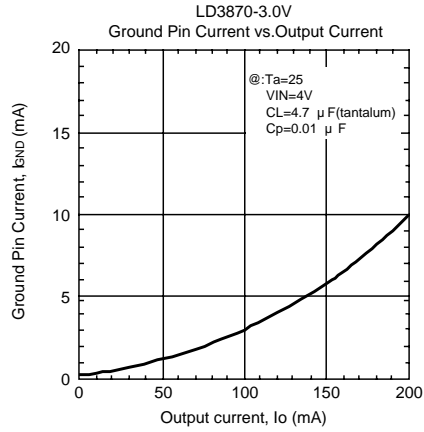
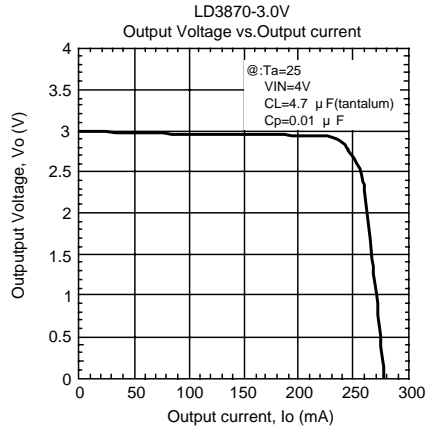
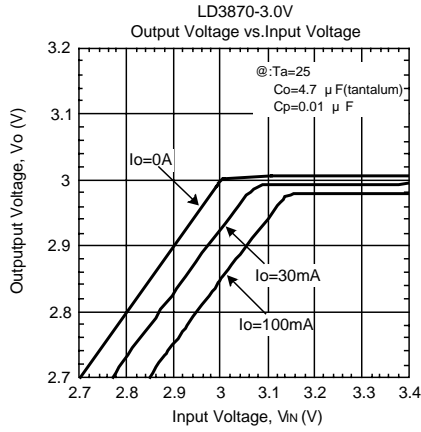
Noise level and ripple rejection will be improved when larger C_p is used.

Use of smaller C_p value may cause oscillation.

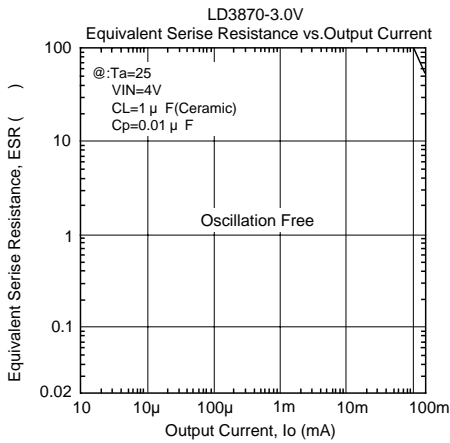
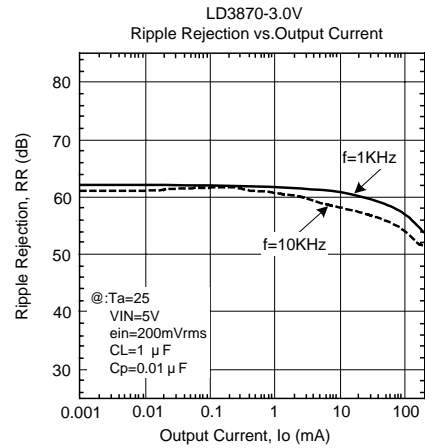
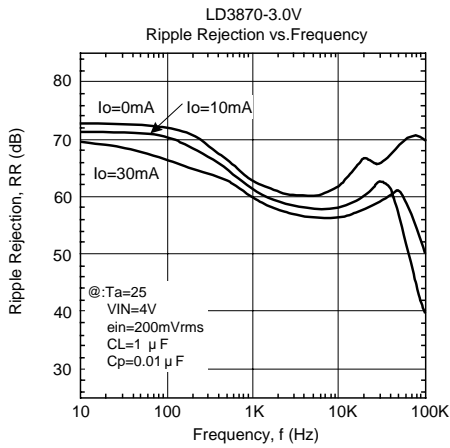
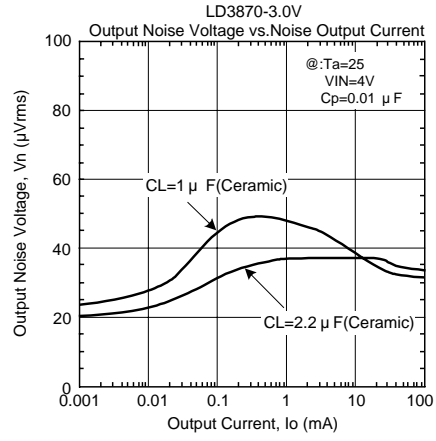
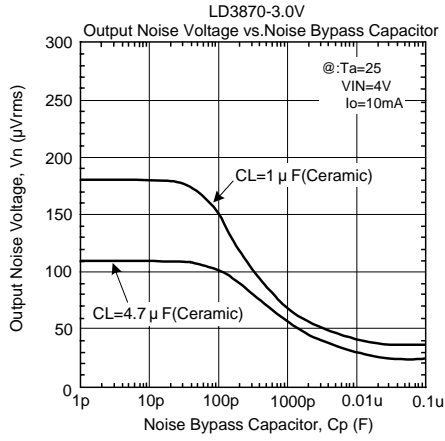
Use the C_p value of 0.01 μ F greater to avoid the problem.

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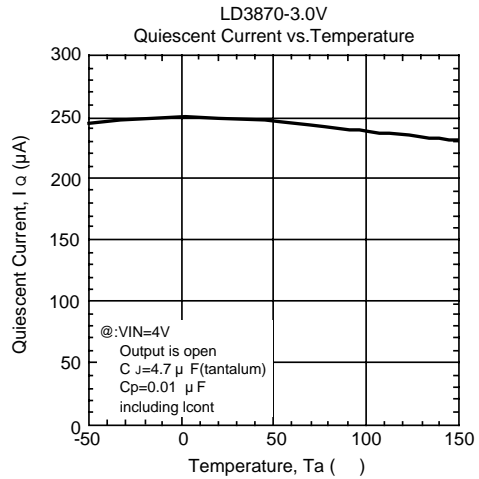
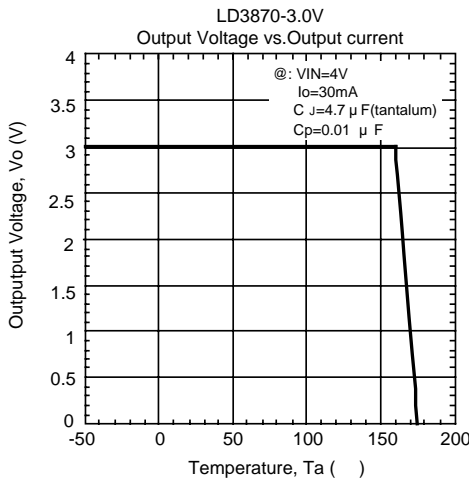
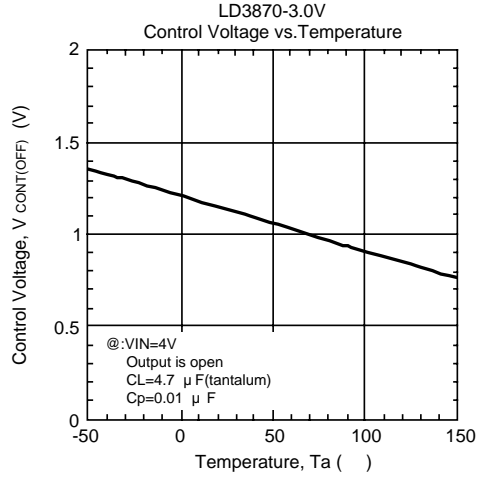
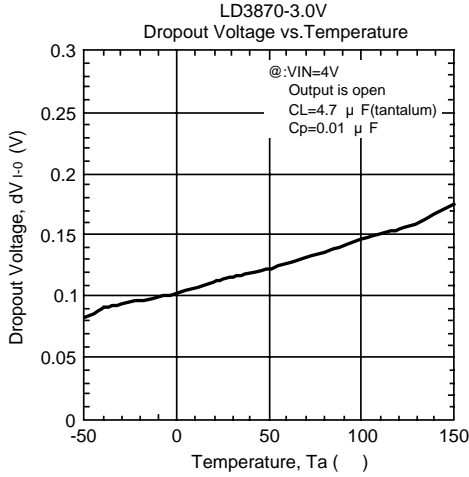
TYPICAL CHARACTERISTICS



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