

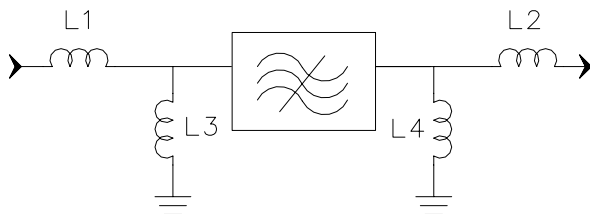
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.75	140	140.25
Insertion Loss	dB	-	13.5	15
1 dB Bandwidth	MHz	31	32.5	-
3 dB Bandwidth	MHz	32	33.5	-
40 dB Bandwidth	MHz	-	37.6	40
Phase Linearity($f_0 \pm 12.9\text{MHz}$)	deg	-	4	6
Passband Variation	dB	-	0.8	1
Group delay variation ($f_0 \pm 12.9\text{MHz}$)	nsec	-	70	100
Ultimate Rejection	dB	40	43	-
Absolute delay	usec	-	0.96	-
Substrate Material			YZ	
Ambient Temperature	°C		25	
Package Size	SMD1365 (13.3 x 6.5 mm Nominal Footprint)			

Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

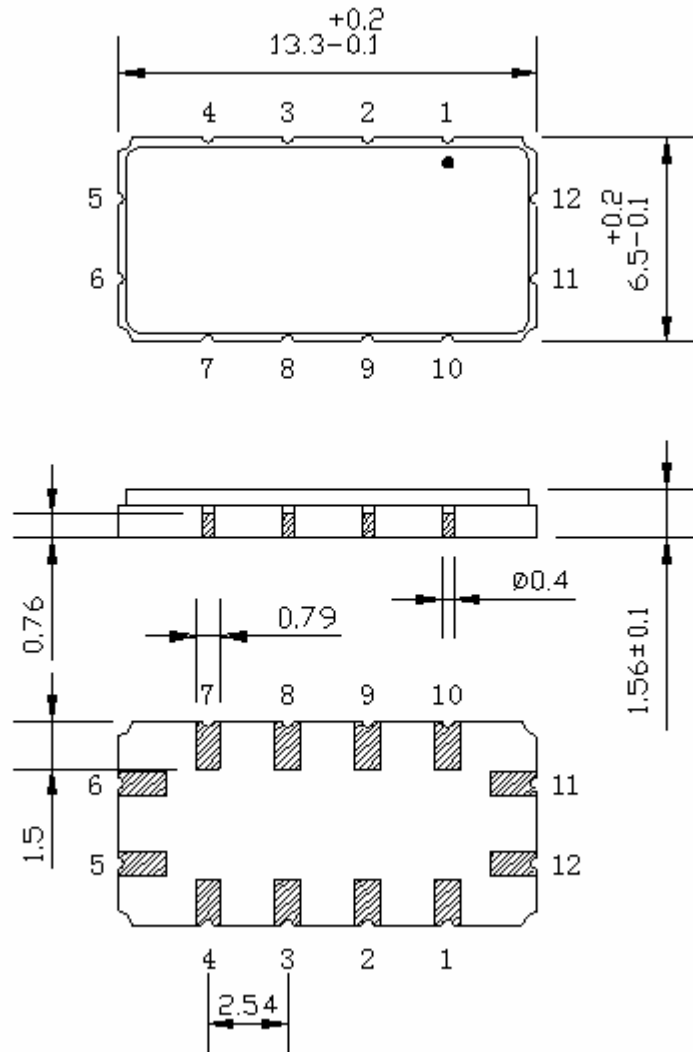


L1=10+82nH L2=82+18nH
L3=180nH L4=220nH
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

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Package Dimension



Input :11
Output:5
Others:Grounded

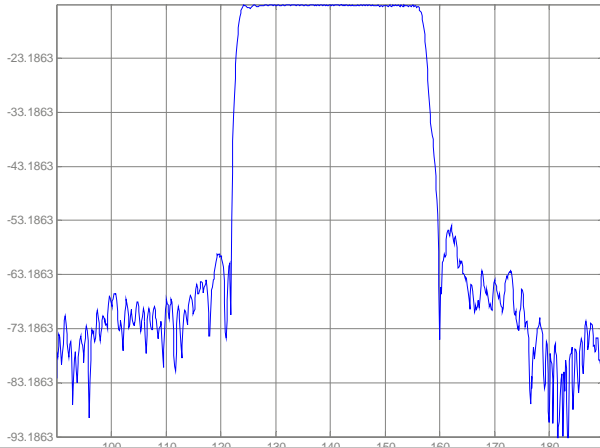


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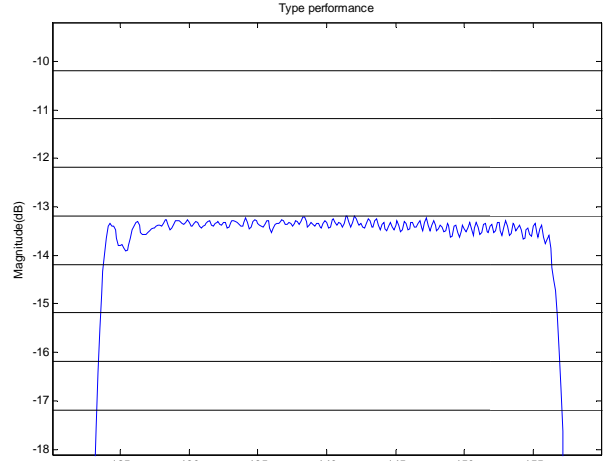
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Typical Performance

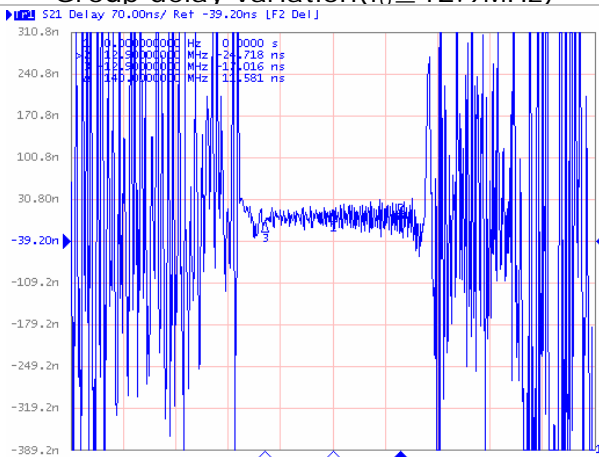
Frequency Respond



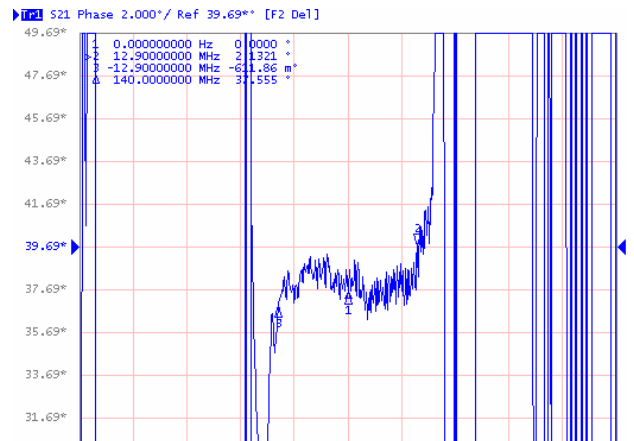
Passband Respond



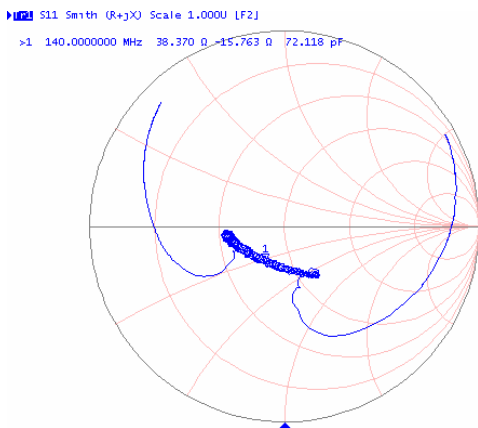
Group delay variation($f_0 \pm 12.9\text{MHz}$)



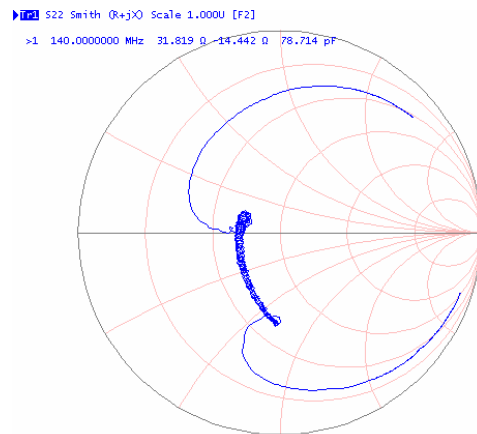
Phase Linearity($f_0 \pm 12.9\text{MHz}$)



Simth Chart S11



Simth Chart S22



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