

ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO: (P4-P5-P6) : (J3-J6) (P1-P2-P3) : (J1-J2)

: 1CT : 1CT ± 3%

: 1CT : 1.41CT ± 3%

NOTES:

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

2.0 INDUCTANCE: (P6-P4) (P3-P1)

: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bigs

3.0 LEAKAGE INDUCTANCE: P6-P5-P4 (WITH J6 AND J3 SHORT) : 0.3 uH MAX. @ 1MHz P3-P2-P1 (WITH J2 AND J1 SHORT) : 0.3 uH MAX. @ 1MHz

: 30pf MAX @ 1MHz

4.0 INTERWINDING CAPACITANCE: (P6, P5, P4) TO (J6, J3) (P3, P2, P1) TO (J2, J1)

: 30pf MAX @ 1MHz

5.0 DC RESISTANCE: (J6-J3)=(J2-J1)

: 1.2 ohms Max.

Bel Stewart Connector 11118 Susquehanna Trail, South

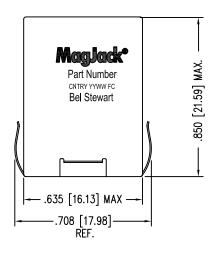
Glen Rock, Pa 17327-9199 717.234.7512

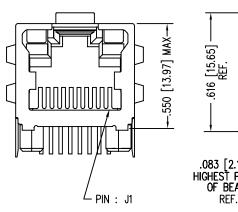
http://www.stewartconnector.com

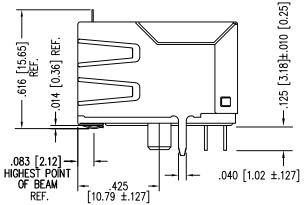
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DRAWING NO. SHEET OF 3

SI - 50017







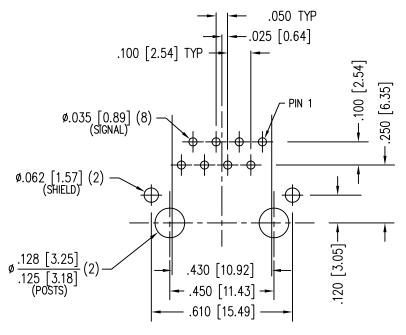
NOTES:

1. CONNECTOR MATERIALS:

HOUSING: THERMOPLASTIC UL94 V-0 CONTACT/SHIELD: COPPER ALLOY SHIELD PLATING: NICKEL OR TIN CONTACT PLATING: SELECTIVE GOLD,

50 MICRO-INCHES MIN. IN CONTACT AREA.

- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. WAVE SOLDER COMPATIBLE PREHEAT 125°C/90SECS.



P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE TOLERANCE ±.003 [0.08] UNLESS OTHERWISE SPECIFIED

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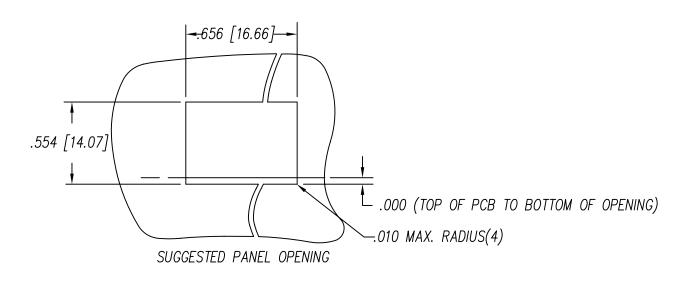
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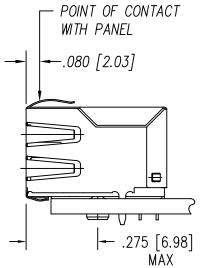
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SHEET DRAW

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SI-50017 REV. 05





- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

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