



#### **FEATURES**

- High efficiency: 85.5% @ 1.2V/ 12A
- Size: 47.2mm x 29.5mm x 8.5mm (1.86" x 1.16" x 0.33")
- Low profile: 0.33"
- Industry standard footprint and pin out
- Surface mountable
- · Fixed frequency operation
- Input UVLO, Output OCP, OVP, OTP
- No minimum load required
- 2:1 input voltage range
- Basic insulation
- 2250 isolation
- ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility
- UL/cUL 60950 (US & Canada) recognized, and TUV (EN60950) certified

# Delphi Series S24SA, 2"x1" Family DC/DC Power Modules: 18~36Vin, 1.2V, 12A out

The Delphi Series S24SA, surface mountable, 24V input, single output, isolated DC/DC converter is the latest offering from a world leader in power system and technology and manufacturing -- Delta Electronics, Inc. This product family provides up to 36 watts of power or up to 12A of output current (for output voltage 1.8V or below) in a low profile 2"x1" industry standard form factor and pinout. The S24SA operates from a wide input range of 18V to 36V, output ranges from 1.2V to 12V. For 1.2V at 12A full load, the efficiency is up to 85.5%. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions. All models are protected from abnormal input/output voltage and current conditions, and feature 2250V input/output isolation and basic insulation.

#### **OPTIONS**

- Positive on/off logic
- SMD or Through hole mounting

#### **APPLICATIONS**

- Telecom/DataCom
- Wireless Networks
- Optical Network Equipment
- Server and Data Storage
- Industrial/Test Equipment

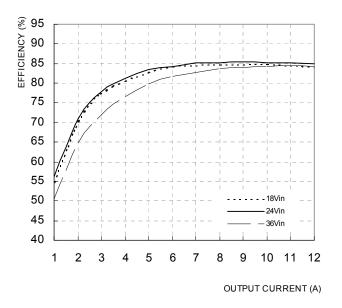


### **TECHNICAL SPECIFICATIONS**

( $T_A$ =25°C, airflow rate=200 LFM,  $V_{in}$ =24Vdc, nominal Vout unless otherwise noted.)

| PARAMETER                               | NOTES and CONDITIONS                            | S24SA1R212NRFA |      |            |                  |  |
|---|---|----------------|------|------------|------------------|--|
|   |   | Min.           | Тур. | Max.       | Units            |  |
| ABSOLUTE MAXIMUM RATINGS                |   |                |      |            |                  |  |
| nput Voltage                            |   | 0.0            |      | 50         | \                |  |
| Continuous  Operating Temperature       | Diagonal franta Fig. 40 For accounting a circle | -0.3           |      | 50         | Vdc<br>°C        |  |
| Storage Temperature                     | Please refer to Fig. 18 For measuring point     | -40<br>-55     |      | 110<br>125 | °C               |  |
| nput/Output Isolation Voltage           |   | -55            |      | 2250       | Vdc              |  |
| INPUT CHARACTERISTICS                   |   |                |      | 2250       | vuc              |  |
| Operating Input Voltage                 |   | 18             | 24   | 36         | V                |  |
| Input Under-Voltage Lockout             |   | 10             | 24   | 30         | V                |  |
| Turn-On Voltage Threshold               |   |                | 17   | 18         | V                |  |
| Turn-Off Voltage Threshold              |   | 13             | 15   |            | V                |  |
| Lockout Hysteresis Voltage              |   | 1              | 2    | 3          | V                |  |
| Maximum Input Current                   | 100% Load, 18Vin                                |                | _    | 2.2        | A                |  |
| No-Load Input Current                   | 10070 2000, 101111                              |                | 40   |            | mA               |  |
| Off Converter Input Current             |   |                | 7    |            | mA               |  |
| Inrush Current(I <sup>2</sup> t)        |   |                | 0.01 |            | A <sup>2</sup> s |  |
| Input Reflected-Ripple Current          | P-P thru 12µH inductor, 5Hz to 20MHz            |                | 5    |            | mA               |  |
| Input Voltage Ripple Rejection          | 120 Hz  |                | 50   |            | dB               |  |
| OUTPUT CHARACTERISTICS                  |   |                |      |            |                  |  |
| Output Voltage Set Point                | Vin=24V, lo=50%lo.max, Ta=25°ℂ                  | 1.17           | 1.20 | 1.23       | V                |  |
| Output Voltage Regulation               |   |                |      |            |                  |  |
| Over Load                               | Io=Io,min to Io,max                             |                | ±2   | ±10        | mV               |  |
| Over Line                               | Vin=18V to36V                                   |                | ±2   | ±5         | mV               |  |
| Over Temperature                        | Ta=-40°C to 85°C                                |                | 100  | 300        | ppm/°(           |  |
| Total Output Voltage Range              | Over sample load, line and temperature          | 1.15           |      | 1.25       | V                |  |
| Output Voltage Ripple and Noise         | 5Hz to 20MHz bandwidth                          |                |      |            |                  |  |
| Peak-to-Peak                            | Full Load, 1µF ceramic, 10µF tantalum           |                | 20   | 50         | mV               |  |
| RMS                                     | Full Load, 1μF ceramic, 10μF tantalum           |                | 5    | 15         | mV               |  |
| Operating Output Current Range          |   | 0              |      | 12         | Α                |  |
| Output DC Current-Limit Inception       | Output Voltage 10% Low                          | 13.2           |      | 18         | Α                |  |
| DYNAMIC CHARACTERISTICS                 |   |                |      |            |                  |  |
| Output Voltage Current Transient        | 24V, 10μF Tan & 1μF Ceramic load cap, 0.1A/μs   |                |      |            |                  |  |
| Positive Step Change in Output Current  | 50% lo,max to 75% lo,max                        |                | 30   | 75         | mV               |  |
| Negative Step Change in Output Current  | 75% lo,max to 50% lo.max                        |                | 30   | 75         | mV               |  |
| Settling Time to 1% of Final value      |   |                | 200  |            | μs               |  |
| Turn-On Transient                       |   |                |      |            |                  |  |
| Start-Up Time, From On/Off Control      |   |                |      | 12         | ms               |  |
| Start-Up Time, From Input               |   |                |      | 12         | ms               |  |
| Maximum Output Capacitance              | Full load; 5% overshoot of Vout at startup      |                |      | 2200       | μF               |  |
| EFFICIENCY                              |   |                |      |            |                  |  |
| 100% Load                               |   |                | 85.5 |            | %                |  |
| SOLATION CHARACTERISTICS                |   |                |      | 0050       |                  |  |
| Isolation Voltage                       |   | 10             |      | 2250       | V                |  |
| Isolation Resistance                    |   | 10             | 4500 |            | ΜΩ               |  |
| Isolation Capacitance                   |   |                | 1500 |            | pF               |  |
| FEATURE CHARACTERISTICS                 |   |                | 200  |            |                  |  |
| Switching Frequency                     |   |                | 330  |            | kHz              |  |
| ON/OFF Control, (Logic Low-Module ON)   | Marriaff at the 15 ft A O vi A                  |                |      | 0.0        |                  |  |
| Logic Low                               | Von/off at Ion/off=1.0mA                        | 0              |      | 0.8        | V                |  |
| Logic High                              | Von/off at Ion/off=0.0 µA                       |                |      | 15         | V                |  |
| ON/OFF Current                          | Ion/off at Von/off=0.0V                         |                |      | 1          | mA               |  |
| Leakage Current                         | Logic High, Von/off=15V                         | 40             |      | 50         | uA               |  |
| Output Voltage Trim Range               | Across Trim Pin & +Vo or –Vo, Pout≤max rated    | -10            |      | +10<br>160 | <u>%</u>         |  |
| Output Over-Voltage Protection(Hiccup)  | Over full temp range; % of nominal Vout         | 115            |      | 100        | 70               |  |
| GENERAL SPECIFICATIONS  Calculated MTBF | lo=80% of lo, max; Ta=25°C, Airflow=200LFM      |                | 6.2  |            | Mhour            |  |
| Calculated IVI I DF                     | 10-00% OF 10, Max, Ta=25 C, All HOW=200LFIVI    |                |      |            |                  |  |
| Weight                                  |   |                | 18   |            | grams            |  |

#### **ELECTRICAL CHARACTERISTICS CURVES**



**Figure 1:** Efficiency vs. load current for minimum, nominal, and maximum input voltage at 25°C.

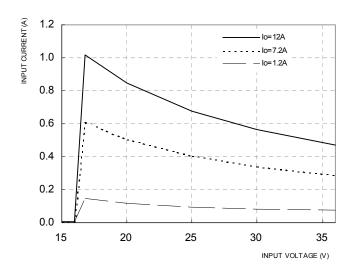
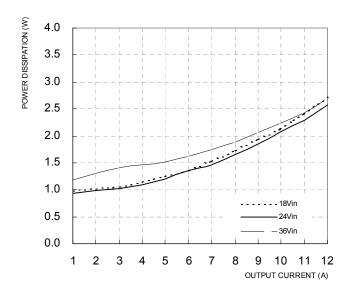
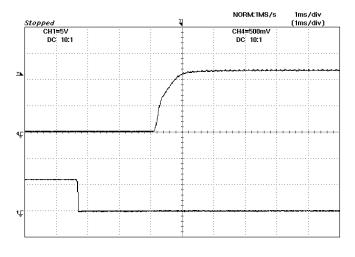


Figure 3: Typical input characteristics at room temperature.

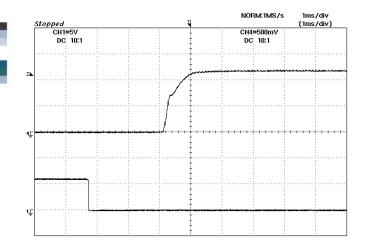


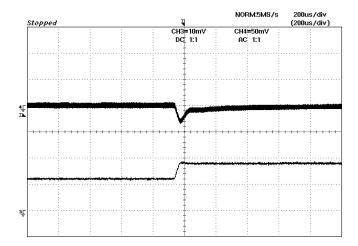
**Figure 2:** Power dissipation vs. load current for minimum, nominal, and maximum input voltage at 25°C.



**Figure 4:** Turn-on transient at full rated load current (1 ms/div). Top Trace: Vout (500mV/div); Bottom Trace: ON/OFF Control (5V/div).

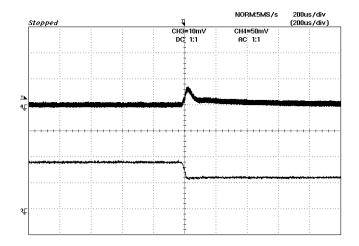
#### **ELECTRICAL CHARACTERISTICS CURVES**

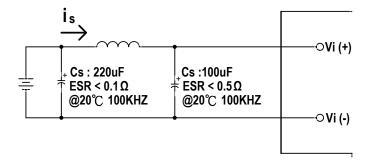




**Figure5**: Turn-on transient at zero load current (1 ms/div). Top Trace: Vout (500mV/div); Bottom Trace: ON/OFF Control (5V/div).

Figure 6: Output voltage response to step-change in load current (50%-75% of Io, max; di/dt = 0.1A/μs). Load cap: 10μF, 100 mΩESR tantalum capacitor and 1μF ceramic capacitor. Top Trace: Vout (50mV/div), Bottom Trace: lout (5A/div).





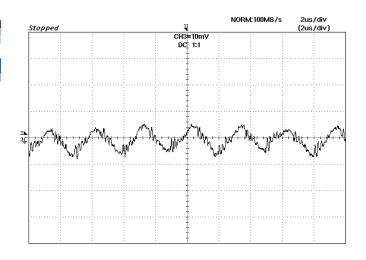
**Figure 7:** Output voltage response to step-change in load current (75%-50% of Io, max; di/dt = 0.1A/μs). Load cap:  $10\mu F$ ,  $100 \text{ m}\Omega ESR$  tantalum capacitor and  $1\mu F$  ceramic capacitor. Top Trace: Vout (50mV/div), Bottom Trace: lout 5A/div).

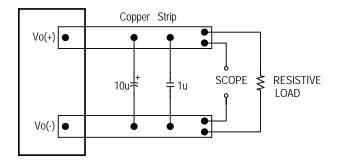
**Figure 8:** Test set-up diagram showing measurement points for Input Reflected Ripple Current (Figure 9).

Note: Measured input reflected-ripple current with a simulated

Note: Measured input reflected-ripple current with a simulated source Inductance ( $L_{TEST}$ ) of 12  $\mu$ H. Capacitor Cs offset possible battery impedance.

#### **ELECTRICAL CHARACTERISTICS CURVES**





**Figure 9**: Input Reflected Ripple Current,  $i_S$ , at full rated output current and nominal input voltage with  $12\mu H$  source impedance and  $100\mu F$  electrolytic capacitor (2 mA/div).

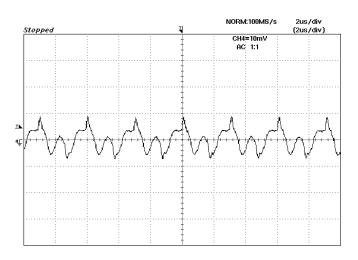
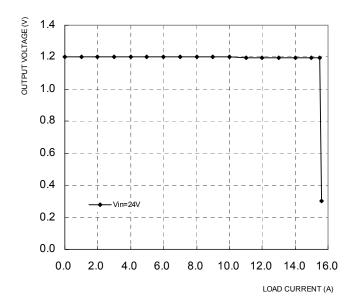


Figure 10: Output voltage noise and ripple measurement test setup. Scope measurement should be made using a BNC cable (length shorter than 20 inches). Position the load between 51 mm to 76 mm (2 inches to 3 inches) from the module.



**Figure 11:** Output voltage ripple at nominal input voltage and rated load current (10 mV/div). Load capacitance:  $1\mu F$  ceramic capacitor and  $10\mu F$  tantalum capacitor. Bandwidth: 20 MHz.

Figure 12: Output voltage vs. load current showing typical current limit curves and converter shutdown points.

#### **DESIGN CONSIDERATIONS**

#### **Input Source Impedance**

The impedance of the input source connecting to the DC/DC power modules will interact with the modules and affect the stability. A low ac-impedance input source is recommended. If the source inductance is more than a few  $\mu H,$  we advise adding a 10 to 100  $\mu F$  electrolytic capacitor (ESR < 0.7  $\Omega$  at 100 kHz) mounted close to the input of the module to improve the stability.

#### **Layout and EMC Considerations**

Delta's DC/DC power modules are designed to operate in a wide variety of systems and applications. For design assistance with EMC compliance and related PWB layout issues, please contact Delta's technical support team. An external input filter module is available for easier EMC compliance design. Application notes to assist designers in addressing these issues are pending release.

#### Safety Considerations

The power module must be installed in compliance with the spacing and separation requirements of the enduser's safety agency standard if the system in which the power module is to be used must meet safety agency requirements.

When the input source is 60Vdc or below, the power module meets SELV (safety extra-low voltage) requirements. If the input source is a hazardous voltage which is greater than 60 Vdc and less than or equal to 75 Vdc, for the module's output to meet SELV requirements, all of the following must be met:

- The input source must be insulated from any hazardous voltages, including the ac mains, with reinforced insulation.
- One Vi pin and one Vo pin are grounded, or all the input and output pins are kept floating.
- The input terminals of the module are not operator accessible.
- A SELV reliability test is conducted on the system where the module is used to ensure that under a single fault, hazardous voltage does not appear at the module's output.

Do not ground one of the input pins without grounding one of the output pins. This connection may allow a non-SELV voltage to appear between the output pin and ground.

This power module is not internally fused. To achieve optimum safety and system protection, an input line fuse is highly recommended. The safety agencies require a normal-blow fuse with 5A maximum rating to be installed in the ungrounded lead. A lower rated fuse can be used based on the maximum inrush transient energy and maximum input current.

#### Soldering and Cleaning Considerations

Post solder cleaning is usually the final board assembly process before the board or system undergoes electrical testing. Inadequate cleaning and/or drying may lower the reliability of a power module and severely affect the finished circuit board assembly test. Adequate cleaning and/or drying is especially important for un-encapsulated and/or open frame type power modules. For assistance on appropriate soldering and cleaning procedures, please contact Delta's technical support team.

#### FEATURES DESCRIPTIONS

#### **Over-Current Protection**

The modules include an internal output over-current protection circuit, which will endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the modules will automatically shut down (hiccup mode).

The modules will try to restart after shutdown. If the overload condition still exists, the module will shut down again. This restart trial will continue until the overload condition is corrected.

#### **Over-Voltage Protection**

The modules include an internal output over-voltage protection circuit, which monitors the voltage on the output terminals. If this voltage exceeds the over-voltage set point, the module will shut down (Hiccup mode). The modules will try to restart after shutdown. If the fault condition still exists, the module will shut down again. This restart trial will continue until the fault condition is corrected.

#### **Over-Temperature Protection**

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the temperature exceeds the over-temperature threshold the module will shut down.

The module will try to restart after shutdown. If the overtemperature condition still exists during restart, the module will shut down again. This restart trial will continue until the temperature is within specification.

#### Remote On/Off

The remote on/off feature on the module can be either negative or positive logic. Negative logic turns the module on during a logic low and off during a logic high. Positive logic turns the modules on during a logic high and off during a logic low.

Remote on/off can be controlled by an external switch between the on/off terminal and the Vi(-) terminal. The switch can be an open collector or open drain.

For negative logic if the remote on/off feature is not used, please short the on/off pin to Vi(-). For positive logic if the remote on/off feature is not used, please leave the on/off pin floating.

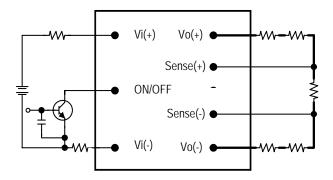


Figure 13: Remote on/off implementation

#### **Remote Sense (Optional)**

Remote sense compensates for voltage drops on the output by sensing the actual output voltage at the point of load. The voltage between the remote sense pins and the output terminals must not exceed the output voltage sense range given here:

$$[Vo(+) - Vo(-)] - [SENSE(+) - SENSE(-)] \le 10\% \times Vout$$

This limit includes any increase in voltage due to remote sense compensation and output voltage set point adjustment (trim).

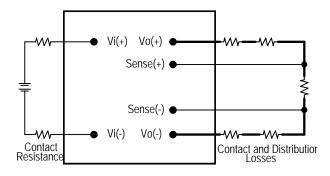


Figure 14: Effective circuit configuration for remote sense operation

If the remote sense feature is not used to regulate the output at the point of load, please connect SENSE(+) to Vo(+) and SENSE(-) to Vo(-) at the module.

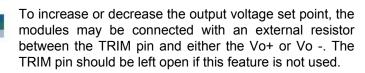
The output voltage can be increased by both the remote sense and the trim; however, the maximum increase is the larger of either the remote sense or the trim, not the sum of both.

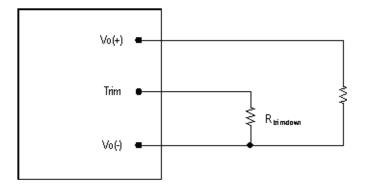
When using remote sense and trim, the output voltage of the module is usually increased, which increases the power output of the module with the same output current.

Care should be taken to ensure that the maximum output power does not exceed the maximum rated power.

### FEATURES DESCRIPTIONS (CON.)

#### Output Voltage Adjustment (TRIM)

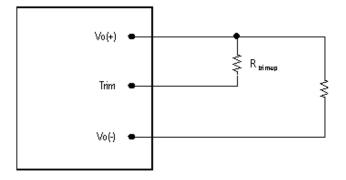




**Figure 15:** Circuit configuration for trim-down (decrease output voltage)

If the external resistor is connected between the TRIM and Vo- pins, the output voltage set point decreases. The external resistor value required to obtain a percentage of output voltage change  $\triangle Vo\%$  is defined as:

$$Rtrim - down = \frac{1089}{\Delta Vo\%} - 62 [K\Omega]$$
 Ex. When trim-down -10% (1.2V X 0.9 = 1.08V) 
$$Rtrim - down = \frac{1089}{10} - 62 = 46.9 [K\Omega]$$



**Figure 16:** Circuit configuration for trim-up (increase output voltage)

If the external resistor is connected between the TRIM and Vo+ pins, the output voltage set point increases. The external resistor value required to obtain a percentage output voltage change △Vo% is defined as:

$$Rtrim - up = \frac{15.9(100 + \Delta Vo\%) - 1089}{\Delta Vo\%} - 62 \big[ \mathrm{K}\Omega \big]$$

Ex. When trim-up +10%  $(1.2V \times 1.1 = 1.32V)$ 

$$Rtrim - up = \frac{15.9(100 + 10) - 1089}{10} - 62 = 4 \big[ \text{K}\Omega \big]$$

Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

#### THERMAL CONSIDERATIONS

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

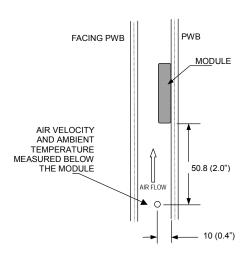
#### **Thermal Testing Setup**

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module or a heat sink is 6.35mm (0.25").

#### **Thermal Derating**

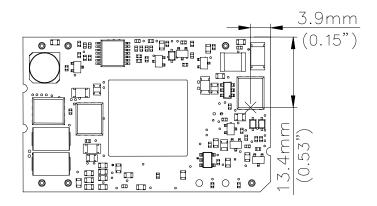
Heat can be removed by increasing airflow over the module. The module's maximum hot spot temperature is To. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.



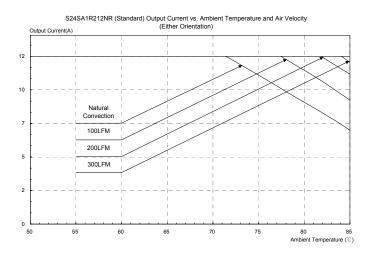
Note: Wind Tunnel Test Setup Figure Dimensions are in millimeters and (Inches)

Figure 17: Wind tunnel test setup

#### THERMAL CURVES

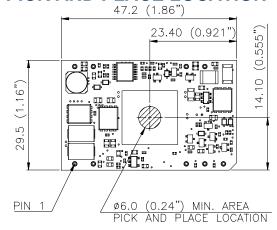


**Figure 18:** Hot spot temperature measured point \*The allowed maximum hot spot temperature is defined at 110  $\mathcal C$ 



**Figure 19:** Output current vs. ambient temperature and air velocity (Either Orientation)

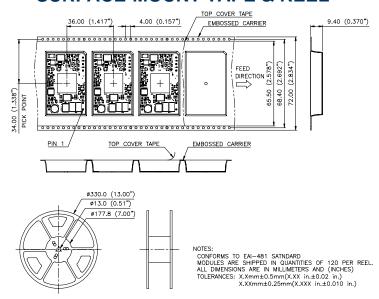
#### PICK AND PLACE LOCATION



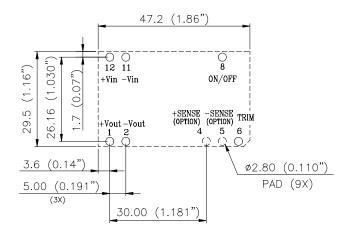
NOTES:

ALL DIMENSIONS ARE IN MILLIMETERS AND (INCHES) TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.) X.XXmm±0.25mm(X.XXX in.±0.010 in.)

#### **SURFACE-MOUNT TAPE & REEL**



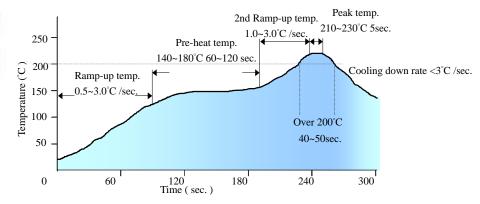
### **RECOMMENDED PAD LAYOUT (SMD)**



#### RECOMENDED P.W.B PAD LAYOUT

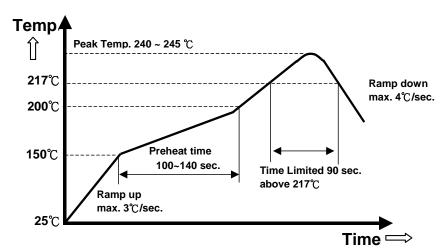
NOTES:

### LEADED (Sn/Pb) PROCESS RECOMMEND TEMP. PROFILE



Note: The temperature refers to the pin of S24SA, measured on the pin +Vout joint.

### LEAD FREE (SAC) PROCESS RECOMMEND TEMP. PROFILE



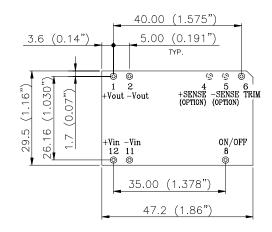
Note: The temperature refers to the pin of S24SA, measured on the pin +Vout joint.

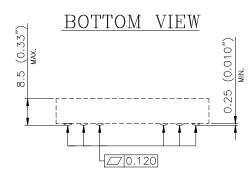
#### **MECHANICAL DRAWING**

### **Surface-Mount Module**

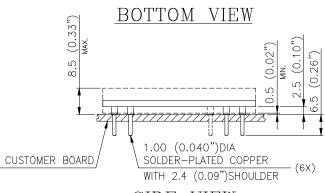
#### 40.00 (1.575") 5.00 (0.191") 3.6 (0.14") 4 5 6 +SENSE -SENSE TRIM (OPTION) (OPTION) 1 2 +Vout -Vout 26.16 (1.030") 29.5 (1.16") (0.07) 7.7 +Vin −Vin 12 11 —— ○ ON/OFF ø2.35 (0.093") DIA. 35.00 (1.378") SOLDER-PLATÉD COPPER (6X) 47.2 (1.86")

### **Through-Hole Module**





## SIDE VIEW



### SIDE VIEW

NOTES:

DIMENSIONS ARE IN MILLIMETERS AND (INCHES) TOLERANCES: X.Xmm±0.5mm(X.XX in.±0.02 in.) X.XXmm±0.25mm(X.XXX in.±0.010 in.)

| Pin No.      | Name            | Function                |
|--------------|-----------------|-------------------------|
| 1            | +Vout           | Positive output voltage |
| 2            | -Vout           | Negative output voltage |
| 6            | Trim            | Output voltage trim     |
| 8            | ON/OFF          | ON/OFF logic            |
| 11           | -Vin            | Negative input voltage  |
| 12           | +Vin            | Positive input voltage  |
| Optional Pin | Name            | Function                |
| 4            | +Sense (Option) | Positive sense pin      |
| 5            | -Sense (Option) | Negative sense pin      |

#### PART NUMBERING SYSTEM

| S                 | 24               | S                    | Α                 | 1R2                  | 12                | N               | R                         | F           | Α              |
|-------------------|------------------|----------------------|-------------------|----------------------|-------------------|-----------------|---------------------------|-------------|----------------|
| Form<br>Factor    | Input<br>Voltage | Number of<br>Outputs | Product<br>Series | Output<br>Voltage    | Output<br>Current | ON/OFF<br>Logic | Pin Type                  |             | Option<br>Code |
| S- Small<br>Power | 24V              | S- Single            | A- Advanced       | 1R2-1.2V<br>1R5-1.5V | 03- 3.0A          | N- Negative     | R- SMD<br>T- Through hole | (Lead Free) | A-6 pins, no   |

#### **MODEL LIST**

| MODEL NAME     | INPUT   |      | OUTPUT |      | EFF @ 100% LOAD |  |  |
|----------------|---------|------|--------|------|-----------------|--|--|
| S24SA1R212NRFA | 18V~36V | 2.2A | 1.2V   | 12A  | 85.5%           |  |  |
| S24SA1R512NRFA | 18V~36V | 2.2A | 1.5V   | 12A  | 87.0%           |  |  |
| S24SA1R812NRFA | 18V~36V | 2.2A | 1.8V   | 12A  | 87.5%           |  |  |
| S24SA2R510NRFA | 18V~36V | 3.2A | 2.5V   | 10A  | 88.5%           |  |  |
| S24SA3R310NRFA | 18V~36V | 3.2A | 3.3V   | 10A  | 90.0%           |  |  |
| S24SA05006NRFA | 18V~36V | 3.2A | 5.0V   | 6.6A | 90.0%           |  |  |
| S24SA12003NRFA | 18V~36V | 3.2A | 12V    | 3.0A | 89.5%           |  |  |

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