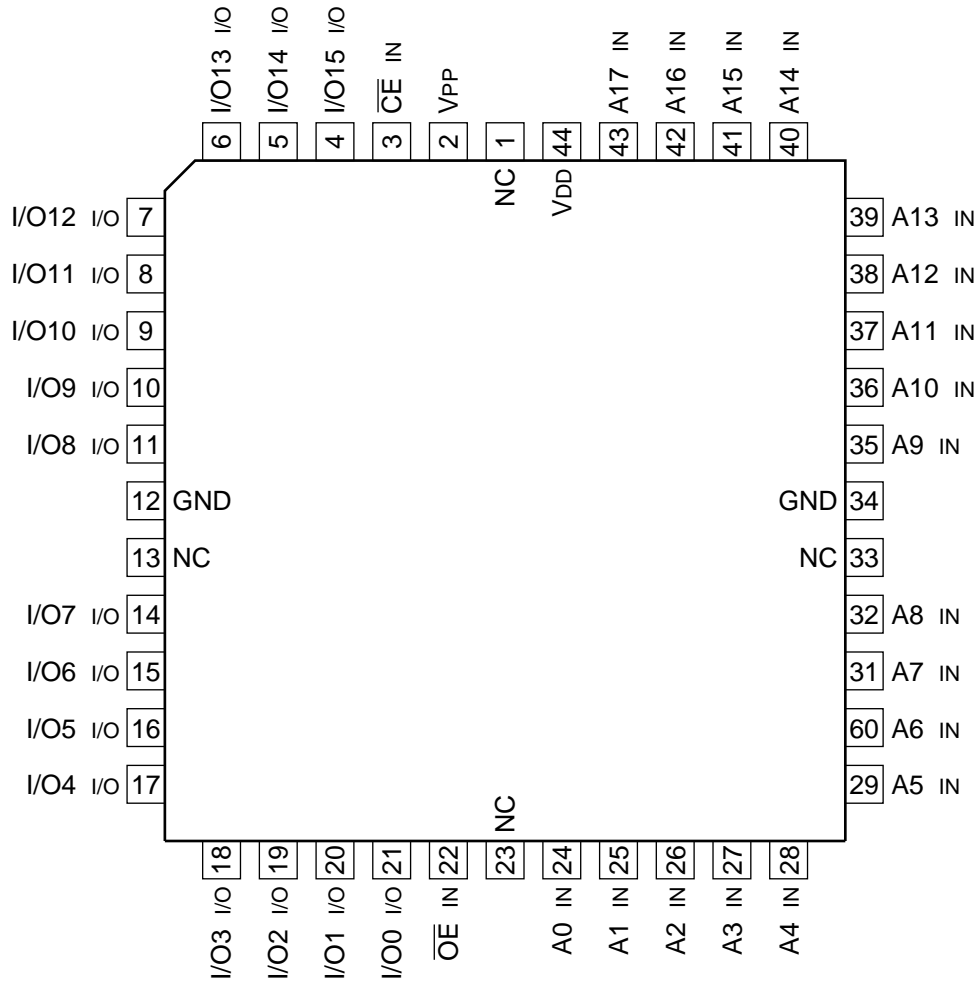


C-MOS 4 M (262,144 × 16)-BIT UV EPROM

—TOP VIEW—



INPUT

- A0 - A17 : ADDRESS
- \overline{CE} : CHIP ENABLE
- \overline{OE} : OUTPUT ENABLE

INPUT/OUTPUT

I/O0 - I/O15 : DATA

OTHER

VPP : PROGRAMMABLE POWER SUPPLY

