

| | | | |
|---|--|-------------|------------|
|  Integrated Solutions Technology, Inc. | Title IST3028 Specification 120-output LCD Common Gate Driver IC | 文件編號 DOC# | 版次 Rev |
| | | IST-RD-0032 | 001 |
| | 生效日期 Effective Date : | | 12/08/2003 |

Specification

| | | | |
|------------------------------|-----------------------------|--------------------------|---------------------------|
| Written by Department | Written by / Date | Approved by QRA Manager | Issued by D.C.C. |
| Research & Development | Charlie Cheng 11/25/2003 | Joey Hsiao 12/08/2003 | Kuo-wei Lee 12/08/2003 |

Controlled by DCC

Copy List

| | | | | | | | |
|-----------|-----|-----|-----|-----|-----|-----|-----|
| Code Name | 100 | 200 | 300 | 400 | 500 | 600 | 700 |
| Dept. | HR | S/M | MFG | R&D | CH | QRA | MIS |
| | -- | -- | -- | ✓ | ✓ | ✓ | -- |



IST3028 120-output LCD Common Gate Driver IC

DESCRIPTION

The IST3028 is a 120-output common gate driver IC and suitable for driving medium/large size dot matrix LCD panels. The IST3028 provides function for both single and dual mode operations, also suitable for a low power consumption, and high-precision LCD.

FEATURES

- Number of LCD drive outputs: 120
- Supply voltage for the logic system: +2.5 to +5.5 V
- Supply voltage for LCD drive: +10.0 to + 40.0 V
- Low power consumption
- Low output impedance
- Operating temperature: -20 to +85°C
- Shift clock frequency : 4 MHz(MAX.)
- Built-in 120-bit bi-directional shift register
(Divisible by 60 bits x 2)
- Available in a single mode (120-bit shift register)
or in a dual mode(60-bit shift register x 2)

(1) $Y_1 \rightarrow Y_{120}$ Single mode

(2) $Y_{120} \rightarrow Y_1$ Single mode

(3) $Y_1 \rightarrow Y_{60}, Y_{61} \rightarrow Y_{120}$ Dual mode

(4) $Y_{120} \rightarrow Y_{61}, Y_{60} \rightarrow Y_1$ Dual mode

- Shift register circuits may reset by active /DISPOFF

Package : TCP/Gold Bumped Chip