FREEDM™-32P672 Development Kit

OVERVIEW

The FREEDM™-32P672 Development Kit is a self contained hardware and software reference design tool, designed to operate on a Pentium-based PC running Windows 95/98.

The kit provides a platform to develop and integrate software with a state-of-the-art Frame Engine and Data Link Manager design.

HARDWARE

The kit contains a FREEDM™-32P672 evaluator board that has been developed to fit into a PCI-based desktop computer.

This board consists of a PM7380 FREEDM™-32P672 device, the necessary interface circuitry, clock distribution circuitry, and various user configurable headers.

The board is configured, monitored, and powered through the PC's motherboard.

SOFTWARE

The kit contains an interactive program, complete with graphical user interface, that is used to monitor and configure the FREEDM™-32P672.

The software, installed from the kit's CD-ROM, provides a visual interface to control and monitor the operation of the board.

Direct access to the FREEDM™-32P672 (provided via the PC's PCI bus) allows the user to:

- Read or write any of the FREEDM™-32P672 Normal Mode Registers.

- Generate packets to be processed by the FREEDM™-32P672.

- Develop software for use with the FREEDM™-32P672.

FEATURES

- Supports a PCI interface, providing microprocessor access to the FREEDM™-32P672 via the host PC.

- Provides a demonstration platform of the FREEDM™-32P672 functions and performance.

- Windows 95 or 98 compatible.

- Graphical User Interface allows quick point-and-click access to complex operations.

- Includes a searchable help file for fast, easy access to documentation.

- Optionally configures the FREEDM™-32P672 automatically for various tests (Automatic Mode). Alternatively, Manual mode allows the user to exercise more configuration control.

- Provides an extensive statistics-gathering facility to allow quick analysis of board performance.

FREEDM-32P672 DEVELOPMENT KIT BOARD PHOTO

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TEST MODES

The FREEDM™-32P672 Development Kit Software contains an extensive suite of test cases including:

- Unchannelized T1 loopback mode.

- Unchannelized E1 loopback mode.

- 16 T1/ 16 E1 unchannelized loopback mode.

- Unchannelized 52 Mbit/s /DS3 /Mixed mode data loopback.

- Unchannelized 2.048 Mbit/s H-MVIP data loopback.

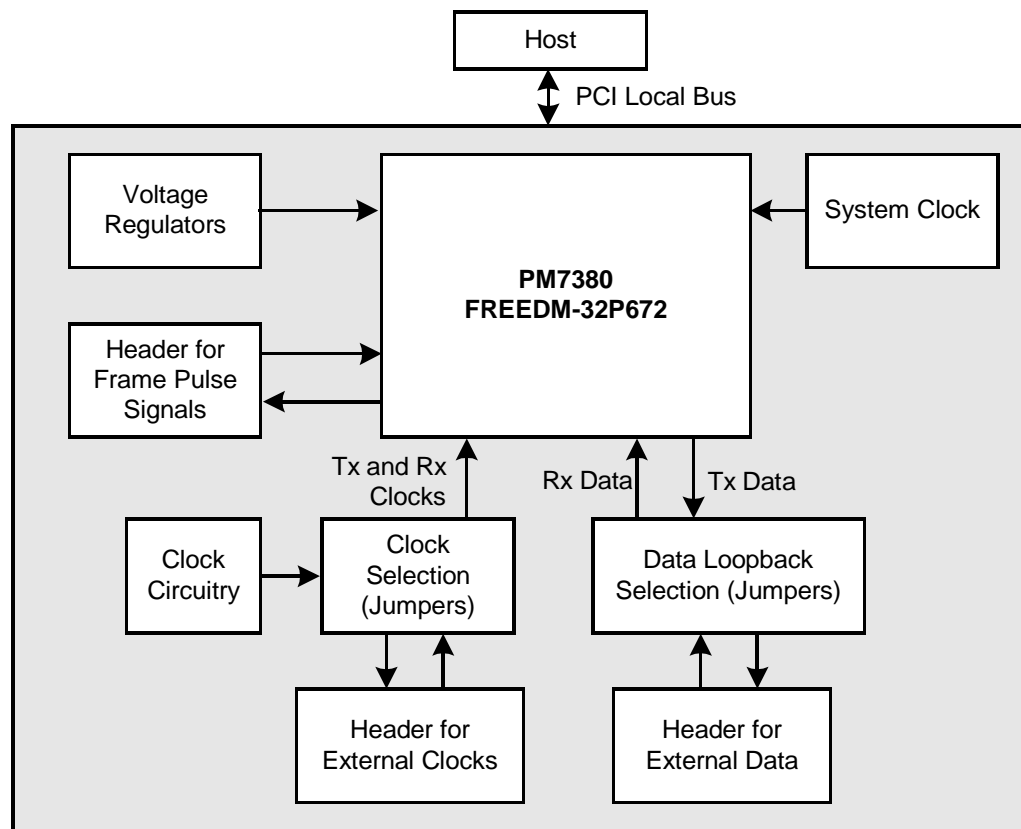- Unchannelized 8.192 Mbit/s H-MVIP data loopback.

PM2352-KIT CONTENTS

- Installation CD-ROM
  - target software binaries
  - source code
  - documentation
  - FREEDM™-32P672 Development Kit Card

NOTE:

The following items are not supplied by PMC-Sierra, and are the responsibility of the user:

- Personal computer

BLOCK DIAGRAM