



LOW-POWER LOW-OFFSET-VOLTAGE TINY C-MOS SINGLE OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJU7091A, 92A and 93A are single C-MOS operational amplifier operated on a single-power-supply, low voltage and low operating current.

The input offset voltage is lower than 2mV.

The minimum operating voltage is 1V and the output stage permits output signals to swing between both of the supply rails.

The input bias current is as low as less than 1pA, consequently the very small signal around the ground level can be amplified.

Furthermore, this series is packaged with very small MTP-5, therefore it can be especially applied to portable items.

■ PACKAGE OUTLINE

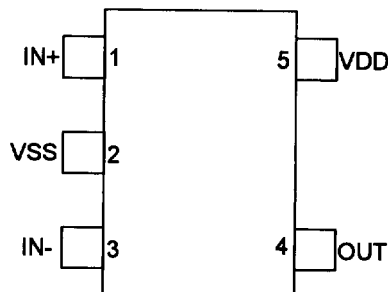


NJU709XAF

■ FEATURES

- Single-Power-Supply
- Wide Operating Voltage ($V_{DD}=1\sim 5.5V$)
- Wide Output Swing Range ($V_{OM}\geq 2.9V$ min. at $V_{DD}=3.0V$)
- Low Operating Current
- Low Offset Voltage ($V_{IO}=2mV$ max.)
- Low Bias Current ($I_{IB}=1pA$ typ.)
- Compensation Capacitor Incorporated
- Package Outline MTP-5
- C-MOS Technology

■ PIN CONFIGURATION (Top View)



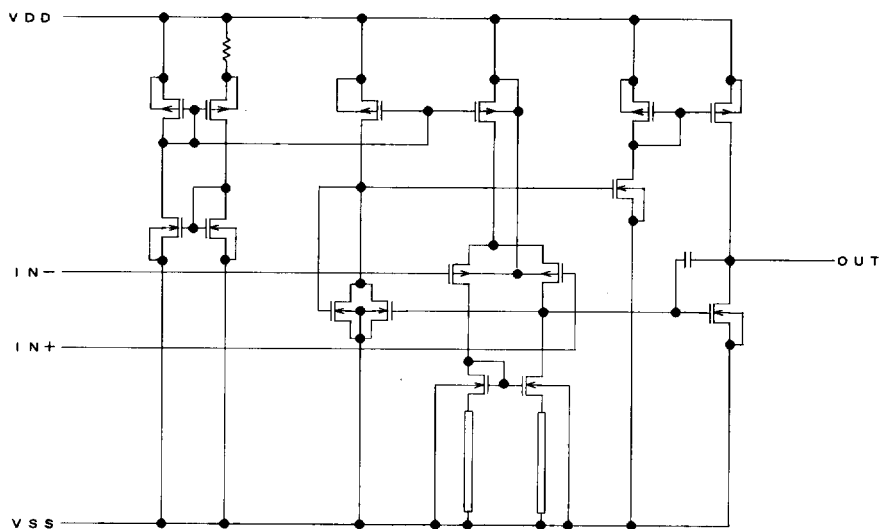
■ LINE-UP

($V_{DD}=3.0V$)

	NJU7091A	NJU7092A	NJU7093A	UNIT
Operating Current	15	80	200	μA (typ.)
Slew Rate	0.1	1.0	2.4	$V/\mu s$ (typ.)
Unity Gain Bandwidth	0.2	1.0	1.0	MHz (typ.)



■ EQUIVALENT CIRCUIT





■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{DD}	6.5	V
Differential Input Voltage	V _{ID}	±6.5 Note1	V
Common Mode Input Voltage	V _{IC}	- 0.3~6.5	V
Power Dissipation	P _D	200	mW
Operating Temperature	T _{opr}	- 40~+ 85	°C
Storage Temperature	T _{stg}	- 55~+125	°C

Note1) If the supply voltage (V_{DD}) is less than 6.5V, the input voltage must not over the V_{DD} level though 6.5V is limit specified.

Note2) Decoupling capacitor should be connected between V_{DD} and V_{SS} due to the stabilized operation for the circuit.

■ ELECTRICAL CHARACTERISTICS

NJU7091A

(Ta=25°C, V_{DD}=3.0V, R_L=∞)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{IO}	V _{IN} =1/2V _{DD}			2	mV
Input Offset Current	I _{IO}			1		pA
Input Bias Current	I _{IB}			1		pA
Input Impedance	R _{IN}			1		TΩ
Large Signal Voltage Gain	A _V		60	70		dB
Input Common Mode Voltage Range	V _{ICM}		0~2.5			V
Maximum Output Swing Voltage	V _{OM1}	R _L =1MΩ	V _{DD} -0.1			V
	V _{OM2}	R _L =1MΩ			V _{SS} +0.1	
Common Mode Rejection Ratio	CMR	V _{IN} =0~2.5V	55	65		dB
Supply Voltage Rejection Ratio	SVR	V _{DD} =1.5~5.5V	60	70		dB
Operating Current	I _{DD}			15	25	μA
Slew Rate	SR			0.1		V/μs
Unity Gain Bandwidth	F _t	A _V =40dB C _L =10pF		0.2		MHz

NOTE3) The source current is less than 2.9μA. (at V_{DD}-0.1(V))



NJU7092A

(Ta=25°C, V_{DD}=3.0V, R_L=∞)

P A R A M E T E R	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{IO}	V _{IN} =1/2V _{DD}			2	mV
Input Offset Current	I _{IO}			1		pA
Input Bias Current	I _{IB}			1		pA
Input Impedance	R _{IN}			1		TΩ
Large Signal Voltage Gain	A _v		60	70		dB
Input Common Mode Voltage Range	V _{ICM}		0~2.5			V
Maximum Output Swing Voltage	V _{OM1}	R _L =100kΩ	V _{DD} -0.1			V
	V _{OM2}	R _L =100kΩ			V _{SS} +0.1	
Common Mode Rejection Ratio	CMR	V _{IN} =0~2.5V	55	65		dB
Supply Voltage Rejection Ratio	SVR	V _{DD} =1.5~5.5V	60	70		dB
Operating Current	I _{DD}			80	160	μA
Slew Rate	SR			1.0		V/μs
Unity Gain Bandwidth	F _t	A _v =40dB C _L =10pF		1.0		MHz

NOTE4) The source current is less than 29μA. (at V_{DD}-0.1(V))

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NJU7093A

(Ta=25°C, V_{DD}=3.0V, R_L=∞)

P A R A M E T E R	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{IO}	V _{IN} =1/2V _{DD}			2	mV
Input Offset Current	I _{IO}			1		pA
Input Bias Current	I _{IB}			1		pA
Input Impedance	R _{IN}			1		TΩ
Large Signal Voltage Gain	A _v		60	70		dB
Input Common Mode Voltage Range	V _{ICM}		0~2.5			V
Maximum Output Swing Voltage	V _{OM1}	R _L =50kΩ	V _{DD} -0.1			V
	V _{OM2}	R _L =50kΩ			V _{SS} +0.1	
Common Mode Rejection Ratio	CMR	V _{IN} =0~2.5V	55	65		dB
Supply Voltage Rejection Ratio	SVR	V _{DD} =1.5~5.5V	60	70		dB
Operating Current	I _{DD}			200	400	μA
Slew Rate	SR			2.4		V/μs
Unity Gain Bandwidth	F _t	A _v =40dB C _L =10pF		1.0		MHz

NOTE5) The source current is less than 58μA. (at V_{DD}-0.1(V))