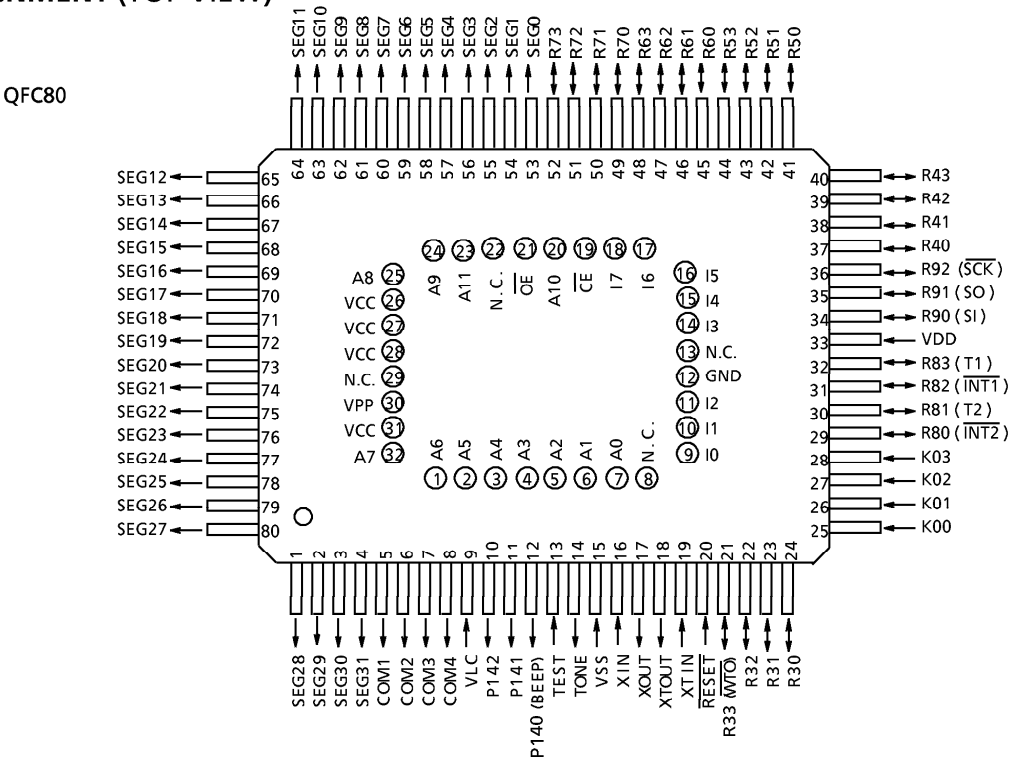


CMOS 4-BIT MICROCONTROLLER

TMP47C956AG

The 47C956A, which is equipped with an EPROM as program memory, is a piggyback type evaluator chip used for development and operational confirmation of the 47C456A application systems (programs).
The 47C956A is pin compatible with the 47C456A which is mask-programmed ROM device.

PIN ASSIGNMENT (TOP VIEW)



PIN FUNCTION (Top of the package)

PIN NAME	Input / Output	FUNCTIONS
A11 to A0	Output	Program memory address output
I7 to I0	Input	Program memory data input
$\overline{CE}$	Output	Chip enable signal output
$\overline{OE}$		Output enable signal output
VCC	Power supply	+ 5 V (connected with VDD)
GND		0 V (connected with VSS)

A.C. CHARACTERISTICS

PARAMETER	SYMBOL	CONDITIONS	Min.	Typ.	Max.	UNIT
Address Delay Time	t_{AD}	$V_{SS} = 0\text{ V}$, $V_{DD} = 2.7\text{ to }6.0\text{ V}$ $C_L = 100\text{ pF}$ $T_{opr} = -30\text{ to }60\text{ }^{\circ}\text{C}$	—	—	150	ns
Data Setup Time	t_{IS}		150	—	—	ns
Data Hold Time	t_{IH}		50	—	—	ns

NOTES FOR USE

(1) Program memory

The program area is shown in Figure 1.

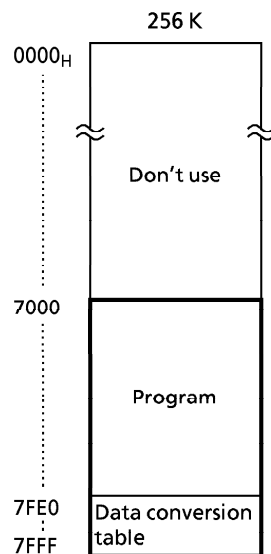


Figure 1. Program area

(2) I/O ports

Input/Output circuitries of the 47C956A I/O ports are similar to the code WB of the 47C456A.

When this chip is used as evaluator with other I/O code, it is necessary to provide the external resistors.

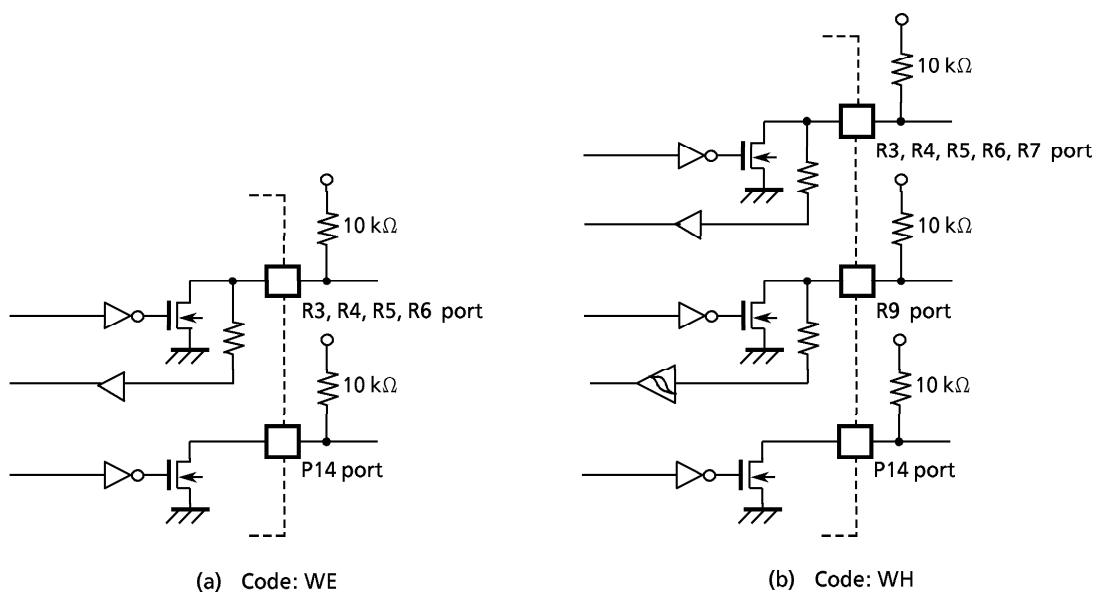


Figure 2. I/O code and external circuitry