TOSHIBA S6785G

TOSHIBA HIGH SPEED THYRISTOR SILICON PLANAR TYPE

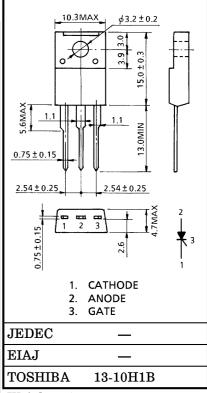
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HIGH SPEED SWITCHING AND CONTROL APPLICATIONS

MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage and Repetitive Peak Reverse Voltage	V _{DRM} V _{RRM}	400	v	
Non-Repetitive Peak Reverse Voltage (Non-Repetitive $<5ms$, $T_j = 0 \sim 125$ °C)	V _{RSM}	500	V	
Average On-State Current (Half Sine Waveform)	I _{T (AV)}	3	A	
R.M.S On-State Current	I _T (RMS)	4.7	A	
Peak One Cycle Surge On-State		60 (50Hz)	A	
Current (Non-Repetitive)	ITSM	66 (60Hz)		
I ² t Limit Value	${ m I}^2{ m t}$	18	A^2 s	
Peak Gate Power Dissipation	P_{GM}	5	W	
Average Gate Power Dissipation	P _G (AV)	0.5	W	
Peak Forward Gate Voltage	v_{FGM}	10	V	
Peak Reverse Gate Voltage	v_{RGM}	-6	V	
Peak Forward Gate Current	I_{GM}	2	A	
Junction Temperature	T_{j}	-40~125	°C	
Storage Temperature Range	$\mathrm{T_{stg}}$	-40~125	°C	

Unit in mm



Weight: 1.7g

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TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

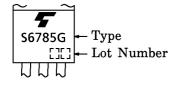
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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	MAX.	UNIT
Repetitive Peak Off-State Current and Repetitive Peak	$I_{ m DRM}$	$V_{DRM} = V_{RRM} = Rated, T_j = 125$ °C		1.0	mA
Reverse Current	$I_{ m RRM}$			2.0	
Peak On-State Voltage	$ m V_{TM}$	$I_{TM} = 20A$	_	2.0	V
Gate Trigger Voltage	v_{GT}	$V_D=6V, R_L=10\Omega$		1.5	V
Gate Trigger Current	I_{GT}			25.0	mA
Gate Non-Trigger Voltage	v_{GD}	V_D =Rated, Tc=100°C		_	V
Gate Non-Trigger Current	$_{ m IGD}$			_	mA
Turn-On Time	t_{gt}	V_D =Rated, I_{TM} =3A, I_G =120mA, $t_{gr}<1\mu$ s	_	3.0	μs
Turn-Off Time	$t_{f q}$	V_D =Rated, I_{TM} =20A, V_G =-2.5V, dv/dt \geq 100V/ μ s, T_c =90°C	_	3.5	μs
Critical Rate of Rise of Off- State Voltage	dv / dt	$V_D = Rated, R_{GK} = 100\Omega, V_G = -2.5V,$ $T_C = 125$ °C, Exponential Rise	100	_	V/μs
Holding Current	${ m I_H}$	$R_L=10\Omega$		80.0	mA
Thermal Resistance	$ m R_{th(j-c)}$	Junction to Case, DC	_	4.0	°C/W

MARKING



NUMBER	SYMBOL		MARK	
%1	TYPE	S6785G	S6785G	
※2	Lot Number		Example 8A: January 1998 8B: February 1998 8L: December 1998	

