TOSHIBA MT6L50AE

### TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

# *1*1 T 6 L 5 0 A E

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

TWO devices are built in to the super-thin and extreme super mini (6 pins) package: ES6

#### **MOUNTED DEVICES**

	Q1 : SSM (TESM)	Q2 : SSM (TESM)
Three-pins (SSM/TESM) mold	2SC5256	MT3S04AS
products are corresponded.	(5256FT)	(MT3S04AT)

#### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	Q1	Q2	UNIT
Collector-Base Voltage	$v_{CBO}$	15	10	V
Collector-Emitter Voltage	$v_{CEO}$	7	5	V
Emitter-Base Voltage	$V_{ m EBO}$	1.5	2	V
Collector Current	$I_{\mathbf{C}}$	40	40	mA
Base Current	$I_{ m B}$	20	10	mA
Collector Power Dissipation	P <sub>C</sub> (Note 1)	100		mW
Junction Temperature	$T_{ m j}$	125		$^{\circ}\mathrm{C}$
Storage Temperature Range	$\mathrm{T_{stg}}$	-55~125		$^{\circ}\mathrm{C}$

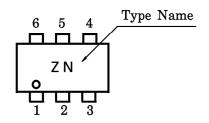
1.0±0.05 0.5 1.2±0.05 1. COLLECTOR 1 BASE 2 2. EMITTER 1 5. **EMITTER 2 COLLECTOR 2** BASE 1 **JEDEC EIAJ TOSHIBA** 2-2N1C

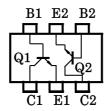
Unit in mm

(Note 1): Total power dissipation of Q1 and Q2.

## **MARKING**

## PIN ASSIGNMENT (TOP VIEW)





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# ELECTRICAL CHARACTERISTICS Q1 (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	ICBO	$V_{CB} = 10 \text{ V}, I_{E} = 0$	_	_	1	$\mu$ <b>A</b>
Emitter Cut-off Current	$I_{EBO}$	$V_{EB} = 1 V, I_{C} = 0$	_	_	1	$\mu$ <b>A</b>
DC Current Gain	$_{ m h_{FE}}$	$V_{CE} = 5 V$ , $I_{C} = 20 mA$	50	_	160	
Transition Frequency	${ m f_T}$	$V_{CE} = 5 V$ , $I_{C} = 20 mA$	10	12	_	GHz
Insertion Gain	$ S_{21e} ^2$	$V_{CE} = 5 \text{ V}, I_{C} = 20 \text{ mA}, $ f = 2000 MHz	5	7.8	_	dB
Noise Figure	NF	$V_{CE} = 5 \text{ V}, I_{C} = 5 \text{ mA}, $ f = 2000 MHz	_	1.5	3	dB
Reverse Transfer Capacitance	$\mathrm{C_{re}}$	$V_{CB} = 5 \text{ V}, I_{E} = 0,$ f = 1 MHz (Note 2)	_	0.5	0.95	pF

# ELECTRICAL CHARACTERISTICS Q2 (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	$I_{CBO}$	$V_{CB} = 5 \text{ V}, I_{E} = 0$	_	_	0.1	$\mu$ A
Emitter Cut-off Current	$I_{EBO}$	$V_{EB} = 1 V, I_{C} = 0$	_	_	1	$\mu$ A
DC Current Gain	${ m h_{FE}}$	$V_{CE} = 1 V$ , $I_{C} = 5 mA$	80	_	160	_
Transition Frequency	f <sub>T</sub> (1)	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}$	2	4.5	_	GHz
	f <sub>T</sub> (2)	$V_{CE} = 3 V$ , $I_{C} = 7 mA$	5	7	_	GHz
Insertion Gain	$ S_{21e} ^2$ (1)	$V_{\text{CE}} = 1 \text{ V}, \text{ I}_{\text{C}} = 5 \text{ mA},$ $f = 1 \text{ GHz}$	_	8.5	_	dB
	$ S_{21e} ^2$ (2)	$V_{CE} = 3 \text{ V}, I_{C} = 20 \text{ mA},$ f = 1  GHz	7.5	11	_	dB
Noise Figure	NF (1)	$V_{ ext{CE}} = 1 \text{ V}, \text{ I}_{ ext{C}} = 5 \text{ mA},$ $f = 1 \text{ GHz}$	_	1.3	2.2	dB
	NF (2)	$V_{CE} = 3 \text{ V}, I_{C} = 7 \text{ mA},$ f = 1  GHz	_	1.2	2	dB
Reverse Transfer Capacitance	$\mathrm{C_{re}}$	$V_{CB} = 1 V, I_{E} = 0,$ f = 1 MHz (Note 2)	_	0.9	1.25	pF

(Note 2):  $C_{re}$  is measured by 3 terminal method with capacitance bridge.

#### HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.