■ MN101C08C

Туре	MN101C08C			
ROM (×8-bit)	48 K (External memory can be expanded)			
RAM (×8-bit)	1.5 K (External memory can be expanded) QFP084-P-1818E *Lead-free 0.10 μs (at 4.5 V to 5.5 V, 20 MHz) 0.238 μs (at 2.7 V to 5.5 V, 8.39 MHz) 1.00 μs (at 2.0 V to 5.5 V, 2 MHz)* 125 μs (at 2.0 V to 5.5 V, 32.768 kHz)* * The lower limit for operation guarantee for EPROM built-in type is 2.7 V.			
Package				
Minimum Instruction Execution Time				
Interrupts	• RESET • Watchdog • External 0 • External 1 • External 2 • External 3 • External 4 • Timer 0 • Timer 1 • Timer 2 • Timer 3 • Timer 4 • Timer 5 • Time base • Serial 0 • Serial 1 • Automatic transfer finish • A/D conversion finish			
Timer Counter	Timer counter 0: 8-bit × 1 (square-wave/8-bit PWM output, event count, generation of remote control carrier) Clock source			
	Timer counter 1: 8-bit × 1 (square-wave output, event count, synchronous output event) Clock source			
	Interrupt source coincidence with compare register 1			
	Timer counter 0, 1 can be cascade-connected.			
	Timer counter 2: 8-bit × 1 (square-wave/8-bit PWM output, event count, synchronous output event) Clock source			
	Interrupt source coincidence with compare register 2			
	Timer counter 3: 8-bit × 1 (square-wave output, event count, generation of remote control carrier, serial 0 baud rate timer) Clock source			
	Interrupt source ······ coincidence with compare register 3			
	Timer counter 2, 3 can be cascade-connected.			
	Timer counter 4: 16-bit × 1 (square-wave/16-bit PWM output, event count, synchronous output event, input capture) Clock source			
	Interrupt source coincidence with compare register 4			
	Time base timer (one-minute count setting, independently operable 8-bit timer counter 5) Clock source			
	Interrupt source coincidence with compare register 5; 1/8192 prescaler overflow			
	Watchdog timer Interrupt source			
Serial Interface	Serial 0 : synchronous type/simple UART (half-duplex) × 1 Clock source ························1/2, 1/4, 1/16 of system clock frequency; output of timer counter 3			
	Serial 1 : synchronous type × 1 Clock source ··················· 1/2, 1/8, 1/64 of system clock frequency; output of timer counter 3			

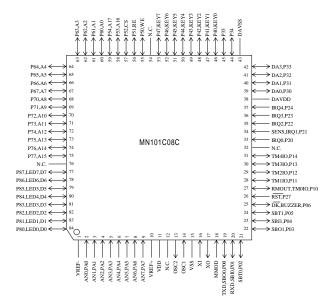
	I/O Pins I/O 5			Common use • Specified pull-up resistor available • Input/output selectable (bit unit)		
Input 13 • Common use • Specified pull-up resistor available		Common use • Specified pull-up resistor available				
	A/D Inputs			10-bit × 8-ch. (with S/H)		
	D/A Outputs			8 -bit \times 4-ch.		
	Special Ports		Buzzer output, remote control carrier signal output, high-current drive port			

Electrical Characteristics

Supply current

Parameter	Symbol	Condition		Limit		
rarameter				typ	max	Unit
Operating supply current	IDD1	fosc = 20 MHz, VDD = 5 V		25	60	mA
Operating supply current	IDD2	fx = 32.768 kHz, VDD = 3 V		30	100	μА
Supply current at HALT	IDD3	fx = 32.768 kHz, VDD = 3 V, Ta = 25°C		4	8	μА
Supply current at HALI		$fx = 32.768 \text{ kHz}, VDD = 3 \text{ V}, Ta = 85^{\circ}\text{C}$			20	μА
Supply current at STOP	IDD4	VDD = 5 V, Ta = 25°C			1	μА
Supply current at STOP		$VDD = 5 \text{ V}, \text{ Ta} = -40^{\circ}\text{C to } +85^{\circ}\text{C}$			30	μА

Pin Assignment



QFP084-P-1818E *Lead-free

Support Tool

In-circuit Emulator	PX-ICE101C / D + PX-PRB101C08-QFP084-P-1818E			
EPROM Built-in Type	Туре	MN101CP08CBF		
	ROM (× 8-bit) 48 K			
	RAM (× 8-bit)	1.5 K		
	Minimum instruction execution time	0.10 μs (at 4.5 V to 5.5 V, 20 MHz)		
		$0.238~\mu s$ (at $2.7~V$ to $5.5~V,8.39~MHz)$		
	Package	QFP084-P-1818E *Lead-free		

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