



MICROCIRCUIT DATA SHEET

MDLF156-X REV 0AL

Original Creation Date: 06/20/95
Last Update Date: 06/20/95
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USED IN LH0101K

Industry Part Number

LF156

NS Part Numbers

LF156-X/883

Prime Die

LF156

Controlling Document

DESC

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

MIL-STD-883, Method 5005

Subgrp Description Temp (°C)

1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

Electrical Characteristics

DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V_s = \pm 15V$, $R_s = 0 \text{ Ohms}$, $V_{cm} = 0V$

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
Psi	Power Supply Current					10	mA	1, 2
						12	mA	3
Vop+	Output Voltage Swing	$R_l = 2K$			10		V	1, 2, 3
Vop-	Output Voltage Swing	$R_l = 2K$				-10	V	1, 2, 3
-Ios	Short Circuit Current					50	mA	1, 2, 3
+Ios	Short Circuit Current				-50		mA	1, 2, 3
Vio	Input Offset Voltage				-5	5	mV	1
					-7	7	mV	2, 3
Iib	Input Bias Current				-.25	.25	nA	1
					-10	100	nA	2
Iio	Input Offset Current				-.05	.05	nA	1
					-50	50	nA	2
Avs	Large Signal Voltage Gain	$R_l = 2K$, $V_{out} = -10V$ to $+10V$	1		55		K	1
			1		30		K	2, 3
CMRR	Common Mode Rejection Ratio	$V_{cm} = \pm 10V$			86		dB	1, 2, 3
PSRR	Power Supply Rejection Ratio	$+V_s = +15V$ to $+5V$, $-V_s = -15V$ to $-5V$			86		dB	1, 2, 3

Note 1: Datalog in K = V/mV.