Advance Information TRIACS Silicon Bidirectional Thyristors

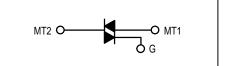
Designed primarily for full wave ac control applications, such as motor controls, heating controls or dimmers; or where ever full-wave, silicon gate-controlled devices are needed.

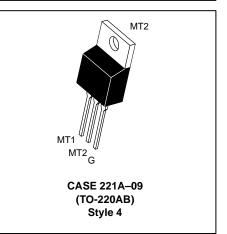
- High Commutating di/dt and High Immunity to dv/dt @ 125°C
- Minimizes Snubber Networks for Protection
- Blocking Voltage to 800 Volts
- On-State Current Rating of 16 Amperes RMS
- High Surge Current Capability 150 Amperes
- Industry Standard TO-220AB Package for Ease of Design
- Glass Passivated Junctions for Reliability and Uniformity



Motorola preferred devices







MAXIMUM RATINGS (T_J = 25° C unless otherwise noted)

Parameter		Symbol	Value	Unit	
Peak Repetitive Off-State Voltage (1) Peak Repetitive Reverse Voltage $(T_J = -40 \text{ to } 125^{\circ}\text{C})$	MAC16CD MAC16CM MAC16CN	Vdrm Vrrm	400 600 800	Volts	
On-State RMS Current (All Conduction Angles; T _C = 80°C)		^I T(RMS)	16	A	
Peak Non-Repetitive Surge Current (One Full Cycle, 60 Hz, T _J = 125°C)		ITSM	150	A	
Circuit Fusing Consideration (t = 8.33 ms)		l ² t	93	A ² sec	
Peak Gate Power (Pulse Width \leq 1.0 µs, T _C = 80°C)		PGM	20	Watts	
Average Gate Power (t = 8.3 ms, $T_C = 80^{\circ}C$)		PG(AV)	0.5	Watts	
Operating Junction Temperature Range		Тj	-40 to +125	°C	
Storage Temperature Range		T _{stg}	-40 to +150	°C	
THERMAL CHARACTERISTICS				•	
Thermal Resistance — Junction to Case — Junction to Ambient		R _{θJC} R _{θJA}	2.2 62.5	°C/W	

 VDRM and VRRM for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

Preferred devices are Motorola recommended choices for future use and best overall value.

Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 Seconds



°C

260

TL

MAC16C Series

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted)

Symbol	Characteristic	Min	Тур	Max	Unit		
OFF CHARACTERISTICS							
IDRM	Peak Repetitive Blocking Current $(V_D = Rated V_{DRM}, Gate Open)$ $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$			0.01 2.0	mA		

ON CHARACTERISTICS

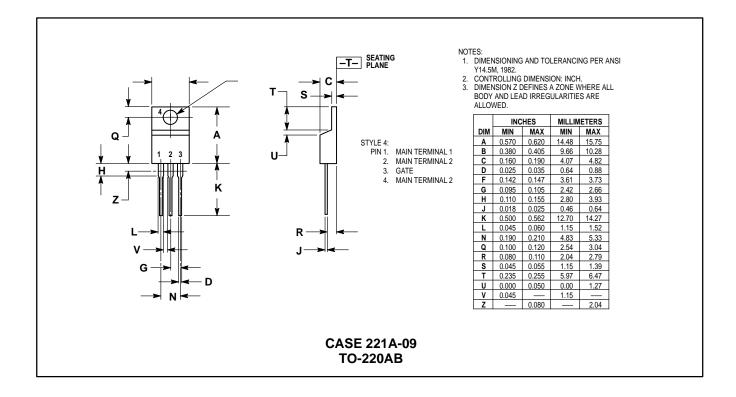
VTM	Peak On-State Voltage ¹ (I _{TM} = ±21 A Peak)	_	_	1.6	V
l _{GT}	Continuous Gate Trigger Current (V _D = 12 V, R _L = 100 Ω) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)	8.0 8.0 8.0		35 35 35	mA
ΙΗ	Holding Current $(V_D = 12 \text{ V}, \text{ Gate Open, Initiating Current} = \pm 150 \text{ mA})$	_	_	40	mA
ΙL	Latching Current ($V_D = 12 V$, $I_G = 50 mA$) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)			60 60 60	mA
V _{GT}	Continuous Gate Trigger Voltage ($V_D = 12 V, R_L = 100 \Omega$) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)	0.5 0.5 0.5		1.5 1.5 1.5	V

DYNAMIC CHARACTERISTICS

(di/dt) _C	Rate of Change of Commutating Current ¹ (V _D = 400 V, I _{TM} = 6.0 A, Commutating dv/dt = 20 V/ μ s, Gate Open, T _J = 125°C, f = 250 Hz, C _L = 10 μ F, L _L = 40 mH, with Snubber)	15	_	_	A/ms
dv/dt	Critical Rate of Rise of Off-State Voltage (V_D = Rated V_{DRM} , Exponential Waveform, Gate Open, T_J = 125°C)	600	_	_	V/µs
di/dt	Repetitive Critical Rate of Rise of On-State Voltage	—	—	20	A/μs

1. Pulse Test: Pulse Width \leq 2.0 ms, Duty Cycle \leq 2%.

PACKAGE DIMENSIONS



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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 1–303–675–2140 or 1–800–441–2447

Customer Focus Center: 1-800-521-6274

 Mfax™: RMFAX0@email.sps.mot.com
 - TOUCHTONE 1–602–244–6609

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ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298

4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan. 81-3-5487-8488

JAPAN: Nippon Motorola Ltd.; SPD, Strategic Planning Office, 141,

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