

HD74LVC1G57

Configurable Multiple-Function Gate

REJ03D0011-0300Z Rev.3.00 Jun. 29, 2004

Description

The HD74LVC1G57 has configurable multiple—function gate in a 6-pin package. The Output state is determined by eight patterns of 3-bit input. The user can choose the logic functions AND, NAND, NOR, EX-NOR. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

• The basic gate function is lined up as renesas uni logic series.

Supply voltage range: 1.65 to 5.5 V
 Operating temperature range: -40 to +85°C

• All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)

All outputs V_0 (Max.) = 5.5 V (@V_{CC} = 0 V)

• Output current: $\pm 4 \text{ mA} (@V_{CC} = 1.65 \text{ V})$

 $\pm 8 \text{ mA} (@V_{CC} = 2.3 \text{ V})$

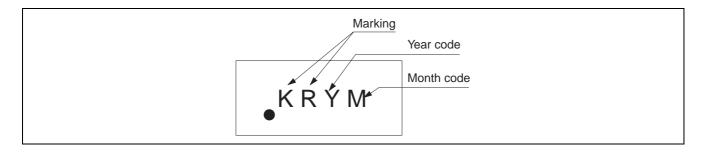
 $\pm 24 \text{ mA } (@V_{CC} = 3.0 \text{ V})$

 $\pm 32 \text{ mA } (@V_{CC} = 4.5 \text{ V})$

- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

| Part Name | Package Type | Package Code | Package Abbreviation | Taping Abbreviation (Quantity) |
|----------------|--------------|--------------|-------------------------|--------------------------------|
| HD74LVC1G57CPE | WCSP-6 pin | TBS-6V | СР | E (3,000 pcs/reel) |
| HD74LVC1G57CLE | | TBS-6AV | CL | |

Article Indication



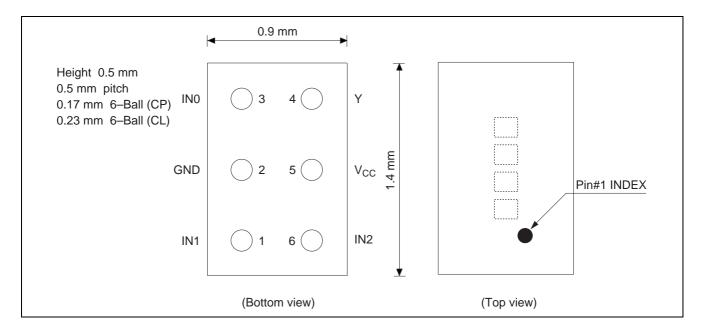
Function Table

| | Inputs | | Output |
|-----|--------|-----|--------|
| IN2 | IN1 | IN0 | Υ |
| L | L | L | Н |
| L | L | Н | L |
| L | Н | L | Н |
| L | Н | Н | L |
| Н | L | L | L |
| Н | L | Н | L |
| Н | Н | L | Н |
| Н | Н | Н | Н |

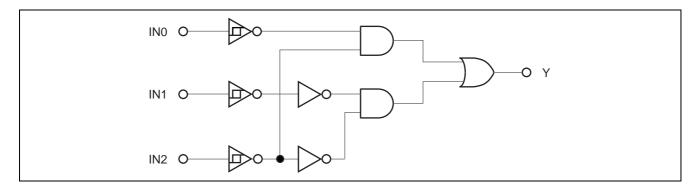
H: High level

L: Low level

Pin Arrangement



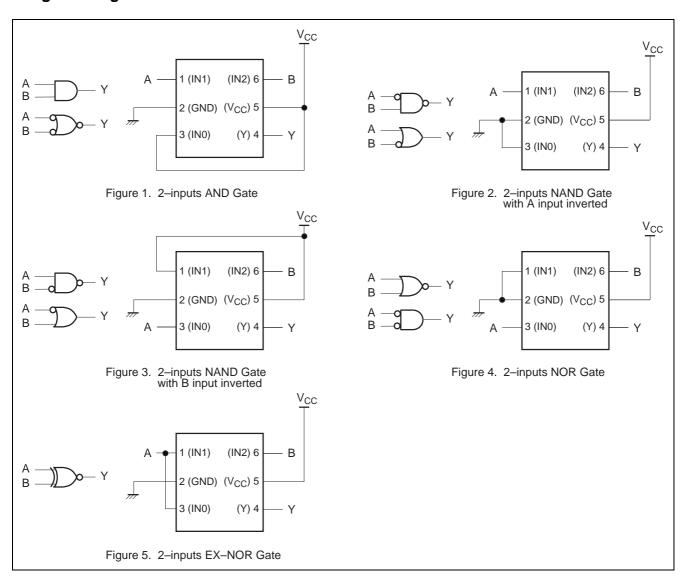
Logic Diagram



Function Selection Table

| Logic Function | Figure No. |
|---------------------------------------|------------|
| 2-input AND | 1 |
| 2-input AND with both inputs inverted | 4 |
| 2-input NAND with one input inverted | 2, 3 |
| 2-input OR with one input inverted | 2, 3 |
| 2-input NOR | 4 |
| 2-input NOR with both inputs inverted | 1 |
| 2-input EX-NOR | 5 |

Logic Configurations



Absolute Maximum Ratings

| Item | Symbol | Ratings | Unit | Test Conditions |
|---|-------------------------------------|-------------------------------|------|-----------------------|
| Supply voltage range | V _{CC} | -0.5 to 6.5 | V | |
| Input voltage range *1 | Vı | -0.5 to 6.5 | V | |
| Output voltage range *1, 2 | Vo | -0.5 to V _{CC} + 0.5 | V | Output : H or L |
| | | -0.5 to 6.5 | | V _{CC} : OFF |
| Input clamp current | I _{IK} | -50 | mA | V _I < 0 |
| Output clamp current | I _{OK} | -50 | mA | V _O < 0 |
| Continuous output current | Io | ±50 | mA | $V_O = 0$ to V_{CC} |
| Continuous current through V _{CC} or GND | I _{CC} or I _{GND} | ±100 | mA | |
| Package Thermal impedance | θ_{ja} | 143 | °C/W | СР |
| | | 123 | | CL |
| Storage temperature | Tstg | -65 to 150 | °C | |

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.

Recommended Operating Conditions

| Item | Symbol | Min | Max | Unit | Conditions |
|------------------------------------|-----------------|------|-----------------|--------|---|
| Supply voltage range | V _{CC} | 1.65 | 5.5 | V | |
| Input voltage range | Vı | 0 | 5.5 | V | |
| Output voltage range | Vo | 0 | V _{CC} | V | |
| Output current | I _{OL} | _ | 4 | mA | V _{CC} = 1.65 V |
| | | _ | 8 | | V _{CC} = 2.3 V |
| | | _ | 16 | | V _{CC} = 3.0 V |
| | | _ | 24 | | |
| | | _ | 32 | | V _{CC} = 4.5 V |
| | I _{OH} | _ | -4 | | V _{CC} = 1.65 V |
| | | _ | -8 | | V _{CC} = 2.3 V |
| | | _ | -16 | | V _{CC} = 3.0 V |
| | | _ | -24 | | |
| | | _ | -32 | | V _{CC} = 4.5 V |
| Input transition rise or fall rate | Δt / Δν | 0 | 20 | ns / V | V _{CC} = 1.65 to 1.95 V, 2.3 to 2.7 V |
| | | 0 | 10 | _ | |
| | | U | | | $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ |
| | | 0 | 5 | | $V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$ |
| Operating free-air temperature | Ta | -40 | 85 | °C | |

Note: Unused or floating inputs must be held high or low.

Electrical Characteristics

Ta = -40 to $85^{\circ}C$

| Item | Symbol | V _{cc} (V) | Min | Тур | Max | Unit | Test condition |
|--------------------------|------------------|---------------------|----------------------|-----|------|------|--|
| Threshold voltage | V_T^+ | 1.8 | 0.8 | _ | 1.4 | V | |
| | | 2.5 | 1.2 | _ | 1.7 | | |
| | | 3.3 | 1.6 | _ | 2.3 | | |
| | | 5.0 | 2.3 | _ | 3.0 | | |
| | V _T | 1.8 | 0.4 | _ | 0.7 | | |
| | | 2.5 | 0.6 | _ | 1.0 | | |
| | | 3.3 | 0.9 | _ | 1.4 | | |
| | | 5.0 | 1.5 | _ | 2.0 | | |
| | ΔV_{T} | 1.8 | 0.4 | _ | 0.7 | | |
| | | 2.5 | 0.4 | _ | 0.8 | | |
| | | 3.3 | 0.4 | _ | 0.9 | | |
| | | 5.0 | 0.4 | _ | 1.0 | | |
| Output voltage | V _{OH} | 1.65 to 5.5 | V _{CC} -0.1 | _ | _ | V | I _{OH} = -100 μA |
| | | 1.65 | 1.2 | _ | _ | | $I_{OH} = -4 \text{ mA}$ |
| | | 2.3 | 1.9 | _ | _ | | I _{OH} = -8 mA |
| | | 3.0 | 2.4 | _ | _ | | $I_{OH} = -16 \text{ mA}$ |
| | | | 2.3 | _ | _ | | I _{OH} = -24 mA |
| | | 4.5 | 3.8 | _ | _ | | $I_{OH} = -32 \text{ mA}$ |
| | V _{OL} | 1.65 to 5.5 | _ | _ | 0.1 | | I _{OL} = 100 μA |
| | | 1.65 | | _ | 0.45 | | I _{OL} = 4 mA |
| | | 2.3 | _ | _ | 0.3 | | I _{OL} = 8 mA |
| | | 3.0 | _ | _ | 0.4 | | I _{OL} = 16 mA |
| | | | | | 0.55 | | I _{OL} = 24 mA |
| | | 4.5 | _ | _ | 0.55 | | I _{OL} = 32 mA |
| Input current | I _{IN} | 0 to 5.5 | _ | _ | ±5 | μΑ | $V_{IN} = 5.5 \text{ V or GND}$ |
| Quiescent supply current | I _{CC} | 5.5 | | _ | 10 | μΑ | $V_{IN} = V_{CC}$ or GND, $I_O = 0$ |
| | ΔΙ _{CC} | 3 to 5.5 | _ | | 500 | | One input at V _{CC} -0.6 V, Other input at V _{CC} or GND |
| Output leakage current | I _{OFF} | 0 | _ | | ±10 | μΑ | V_{IN} or $V_O = 0$ to 5.5 V |
| Input capacitance | C _{IN} | 3.3 | | 3.5 | | pF | $V_{IN} = V_{CC}$ or GND |

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

 $V_{CC}=1.8\pm0.15~V$

| | | Ta = -40 to 85°C | | | | FROM | то |
|------------------------|--------------------------------------|------------------|------|------|---|---------|----------|
| Item | Symbol | Min | Max | Unit | Test Conditions | (Input) | (Output) |
| Propagation delay time | t _{PLH} t _{PHL} | 3.2 | 14.4 | | $C_L = 30 \text{ pF},$ $R_L = 1.0 \text{ k}\Omega$ | IN | Y |

 $V_{CC}=2.5\pm0.2~V$

| | | Ta = -40 |) to 85°C | | | FROM | то |
|-------|--------------------------------------|----------|-----------|------|--|---------|----------|
| Item | Symbol | Min | Max | Unit | Test Conditions | (Input) | (Output) |
| ' ' ' | t _{PLH} t _{PHL} | 2.0 | 8.3 | | $C_L = 30 \text{ pF},$ $R_L = 500 \Omega$ | IN | Y |

 $V_{CC}=3.3\pm0.3~V$

| | | Ta = -40 |) to 85°C | | | FROM | ТО |
|------|--------------------------------------|----------|-----------|------|--|---------|----------|
| Item | Symbol | Min | Max | Unit | Test Conditions | (Input) | (Output) |
| | t _{PLH} t _{PHL} | 1.5 | 6.3 | | $C_L = 50 \text{ pF},$ $R_L = 500 \Omega$ | IN | Y |

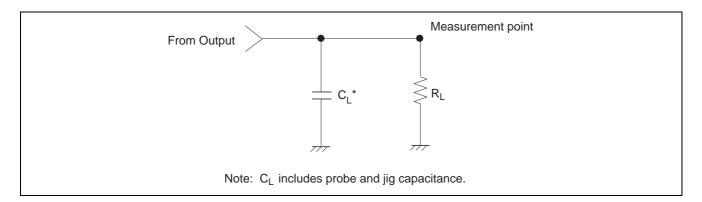
 $V_{CC}=5.0\pm0.5~V$

| | | Ta = -40 to 85°C | | | | FROM | ТО |
|------------------------|--------------------------------------|------------------|-----|------|--|---------|----------|
| Item | Symbol | Min | Max | Unit | Test Conditions | (Input) | (Output) |
| Propagation delay time | t _{PLH} t _{PHL} | 1.1 | 5.1 | | $C_L = 50 \text{ pF},$ $R_L = 500 \Omega$ | IN | Υ |

Operating Characteristics

| | | | | Ta = 25°C | | | |
|-------------------------------|-----------------|---------------------|-----|-----------|-----|------|-----------------|
| Item | Symbol | V _{cc} (V) | Min | Тур | Max | Unit | Test Conditions |
| Power dissipation capacitance | C _{PD} | 1.8 | _ | 20 | _ | pF | f = 10 MHz |
| | | 2.5 | _ | 20 | _ | | |
| | | 3.3 | _ | 21 | _ | | |
| | | 5.0 | _ | 22 | _ | | |

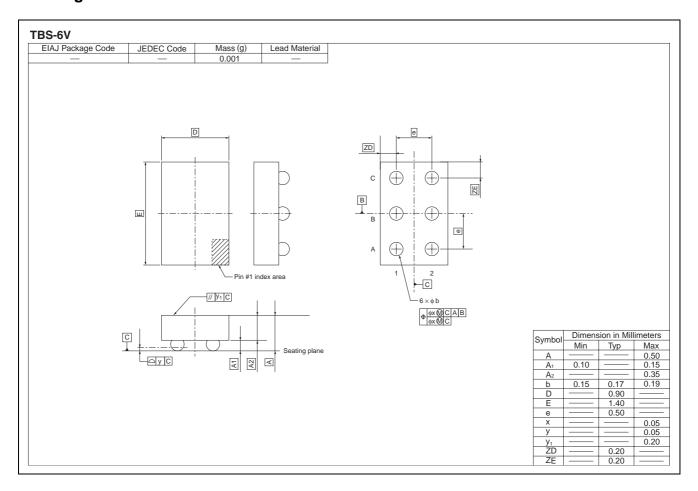
Test Circuit

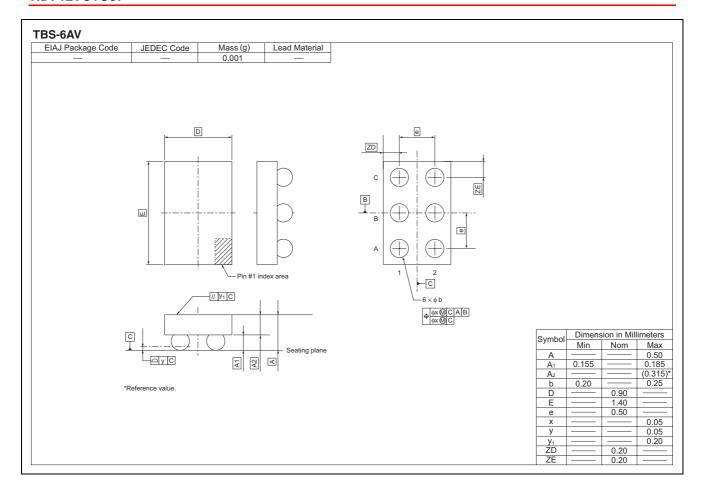


• Waveforms V_{I} 90% 90% Vref Vref Input 10% 10% **GND** t_{PLH} t_{PHL} V_{OH} In phase output Vref Vref V_{OL} V_{OH} Vref Vref Out of phase output V_{OL} t_{PHL} t_{PLH} **INPUTS** $V_{CC}(V)$ Vref R_L C_L V_{I} t_r / t_f ≤ 2 ns 30 pF $1.0~\text{k}\Omega$ V_{CC} / 2 V_{CC} 1.8±0.15 2.5±0.2 ≤ 2 ns V_{CC} / 2 30 pF $500\;\Omega$ V_{CC} 3.3 ± 0.3 1.5 V 3 V 50 pF 500 Ω ≤ 2.5 ns $V_{CC} \mid \leq 2.5 \text{ ns}$ 5.0 ± 0.5 V_{CC} / 2 50 pF $500\;\Omega$ Notes: 1. Input waveform : PRR \leq 10 MHz, Zo = 50 Ω .

2. The output are measured one at a time with one transition per measurement.

Package Dimensions





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