



SAW Components

Data Sheet B4067





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B4067

Low-Loss Filter

810,0 MHz

Data Sheet

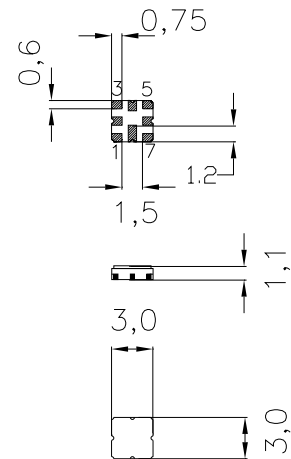
SMD ceramic package **QCC8D**

Features

- Low loss IF filter for HiperLAN
- Balanced to balanced operation
- Package for **Surface Mounted Technology (SMT)**

Terminals

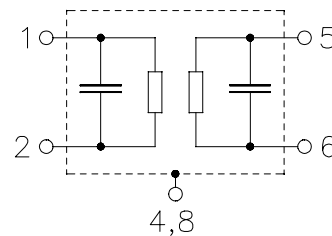
- Ni, gold-plated



Dimensions in mm, approx. weight 0,037 g

Pin configuration

- 1 Input
- 2 Input
- 5 Output
- 6 Output
- 3, 7 To be grounded
- 4, 8 Case - ground



Type	Ordering code	Marking and Package according to	Packing according to
B4067	B39811-B4067-U810	C61157-A7-A72	F61074-V8101-Z000

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T	- 40/+ 85	°C	
Storage temperature range	T_{stg}	- 40/+ 85	°C	
DC voltage	V_{DC}	0	V	
Source power	P_s	0	dBm	source impedance 200 Ω



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Characteristics

Operating temperature range: $T_A = 0 \dots +70 \text{ }^\circ\text{C}$
 Terminating source impedance: $Z_S = 200 \text{ } \Omega$
 Terminating load impedance: $Z_L = 200 \text{ } \Omega$

		min.	typ.	max.	
Nominal frequency	f_N	—	810,0	—	MHz
Minimum insertion attenuation	α_{\min}	—	1,7	4,0	dB
Amplitude ripple in passband (p-p)	$\Delta\alpha$				
	$f_N \pm 8,0 \text{ MHz}$	—	0,6	1,0	dB
	$f_N \pm 8,5 \text{ MHz}$	—	0,7	1,2	dB
Group delay ripple (p-p)	$\Delta\tau$				
	$f_N \pm 8,5 \text{ MHz}$	—	25	75	ns
Relative attenuation (relative to α_{\min})	α_{rel}				
	$f_N - 20,0 \text{ MHz}$	15,5	36	—	dB
	$f_N + 20,0 \text{ MHz}$	15,5	24	—	dB
	$f_N - 40,0 \text{ MHz}$	23	54	—	dB
	$f_N + 40,0 \text{ MHz}$	23	48	—	dB
	$f_N - 500 \text{ MHz} \dots f_N - 50,0 \text{ MHz}$	45	54	—	dB
	$f_N + 50,0 \text{ MHz} \dots f_N + 500 \text{ MHz}$	45	58	—	dB
Reflected wave signal suppression					
	450 ns after main pulse	46,0	48,0	—	dB



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Characteristics (2 filters cascaded)

Operating temperature range: $T_A = 0 \dots +70 \text{ }^\circ\text{C}$
 Terminating source impedance: $Z_S = 200 \text{ } \Omega$
 Terminating load impedance: $Z_L = 200 \text{ } \Omega$

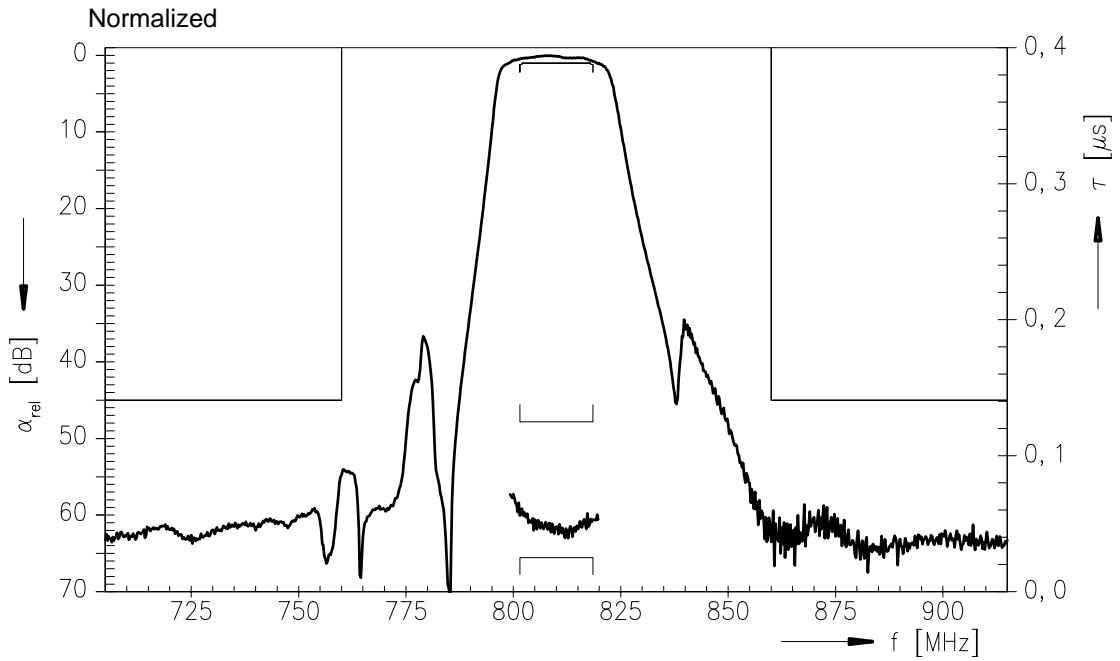
		min.	typ.	max.	
Nominal frequency	f_N	—	810,0	—	MHz
Minimum insertion attenuation	α_{\min}	—	3,4	8,0	dB
Amplitude ripple in passband (p-p)	$\Delta\alpha$				
	$f_N \pm 8,0 \text{ MHz}$	—	1,2	2,0	dB
	$f_N \pm 8,5 \text{ MHz}$	—	1,8	2,4	dB
Group delay ripple (p-p)	$\Delta\tau$				
	$f_N \pm 8,5 \text{ MHz}$	—	50	150	ns
Relative attenuation (relative to α_{\min})	α_{rel}				
	$f_N - 20,0 \text{ MHz}$	31	60	—	dB
	$f_N + 20,0 \text{ MHz}$	31	48	—	dB
	$f_N - 40,0 \text{ MHz}$	46	108 *)	—	dB
	$f_N + 40,0 \text{ MHz}$	46	96 *)	—	dB
	$f_N - 500 \text{ MHz} \dots f_N - 50,0 \text{ MHz}$	90	108 *)	—	dB
	$f_N + 50,0 \text{ MHz} \dots f_N + 500 \text{ MHz}$	90	116 *)	—	dB
Reflected wave signal suppression					
	900 ns after main pulse	46,0	48,0	—	dB

*) value depends on pcb layout

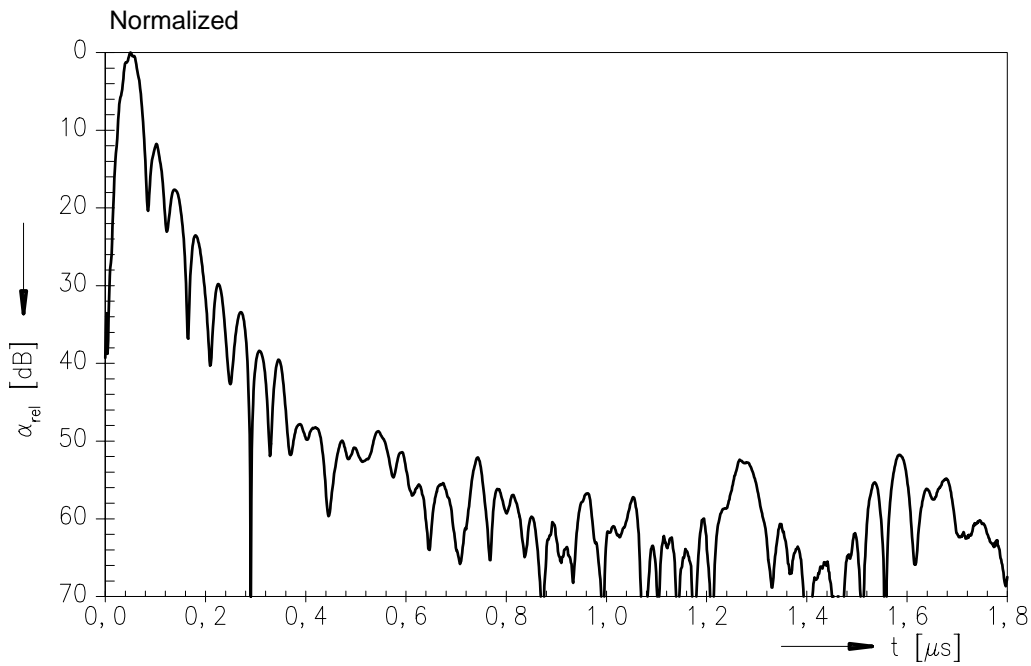


Data Sheet

Transfer function (single filter)



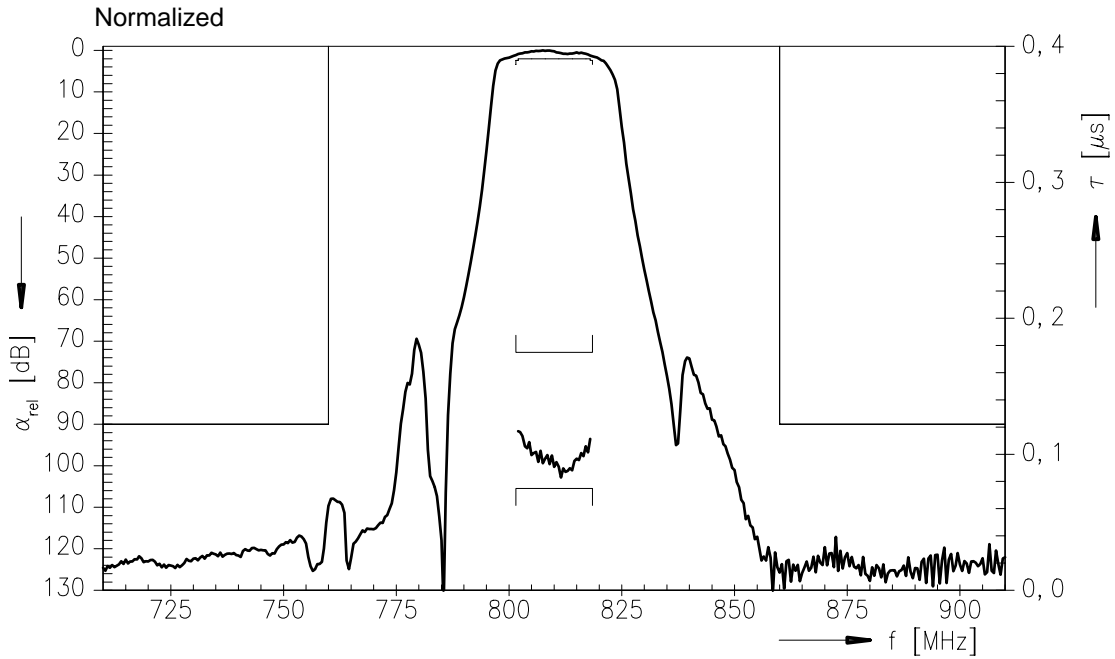
Impulse response (single filter)



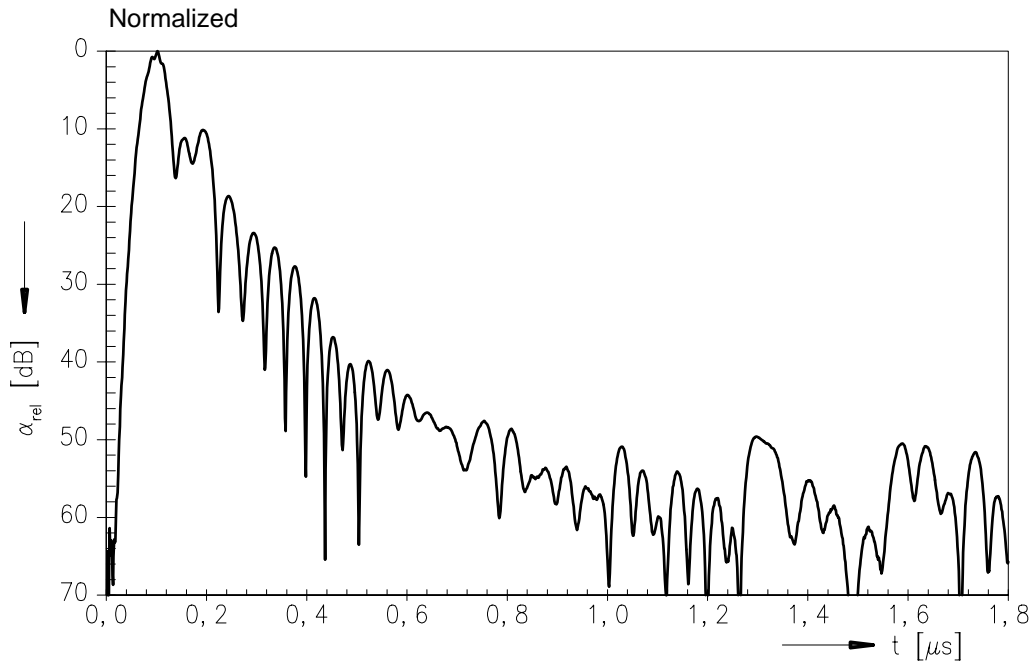


Data Sheet

Transfer function (2 cascaded filters)



Impulse response (2 cascaded filters)





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Published by EPCOS AG

Surface Acoustic Wave Components Division, SAW MC WT

P.O. Box 80 17 09, D-81617 München

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