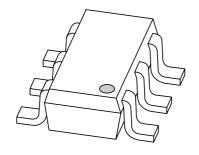
DISCRETE SEMICONDUCTORS

DATA SHEET



BAS21VDHigh-voltage switching diode array

Product specification

2003 Jul 02





High-voltage switching diode array

BAS21VD

FEATURES

- Small plastic SMD package
- Switching speed: max. 50 ns
- Continuous reverse voltage: max. 200 V
- Repetitive peak reverse voltage: max. 250 V
- Repetitive peak forward current: max. 1 A.

APPLICATIONS

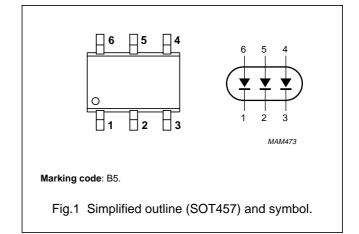
- High-voltage switching in surface mounted circuits
- Automotive
- · Communication.

DESCRIPTION

The BAS21VD is a high-voltage diode array fabricated in planar technology and encapsulated in a small SOT457 plastic SMD package.

PINNING

| PIN | DESCRIPTION |
|-----|--------------|
| 1 | cathode (k1) |
| 2 | cathode (k2) |
| 3 | cathode (k3) |
| 4 | anode (a3) |
| 5 | anode (a2) |
| 6 | anode (a1) |



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|------------------|-------------------------------------|---|------|------|------|
| Per diode | • | | • | | |
| V_{RRM} | repetitive peak reverse voltage | | _ | 250 | V |
| V _R | continuous reverse voltage | | _ | 200 | V |
| I _F | continuous forward current | note 1; see Fig.2 | _ | 200 | mA |
| I _{FRM} | repetitive peak forward current | $t = 1 \text{ ms}; \delta = 25\%$ | _ | 1 | А |
| I _{FSM} | non-repetitive peak forward current | square wave; T _j = 25 °C prior to surge; see Fig.4 | | | |
| | | t = 10 μs | _ | 16 | Α |
| | | t = 100 μs | _ | 8 | Α |
| | | t = 10 ms | _ | 2 | Α |
| P _{tot} | total power dissipation | T _{amb} = 25 °C; note 1 | _ | 250 | mW |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| T _j | junction temperature | | _ | 150 | °C |

Note

1. Pulse test: $t_p = 300 \,\mu s$; $\delta = 0.02$.

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ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | MAX. | TYP. | UNIT |
|-----------------|-----------------------|---|------|------|------|
| Per diode | | | | | |
| V _F | forward voltage | see Fig.3 | | | |
| | | I _F = 100 mA | _ | 1 | V |
| | | I _F = 200 mA | _ | 1.25 | V |
| I _R | reverse current | V _R = 200 V; note 1; see Fig.5 | 25 | 100 | nA |
| | | $V_R = 200 \text{ V}; T_j = 150 ^{\circ}\text{C}; \text{ note } 1$ | _ | 100 | μΑ |
| C _d | diode capacitance | f = 1 MHz; V _R = 0; see Fig.6 | 0.6 | 5 | pF |
| t _{rr} | reverse recovery time | when switched from $I_F = 30$ mA to $I_R = 30$ mA; $R_L = 100 \Omega$; measured at $I_R = 3$ mA; see Fig.8 | 16 | 50 | ns |

Note

1. Pulse test: t_p = 300 μ s; δ = 0.02.

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | CONDITIONS | VALUE | UNIT |
|---------------------|---|------------|-------|------|
| R _{th j-a} | thermal resistance from junction to ambient | note 1 | 208 | K/W |

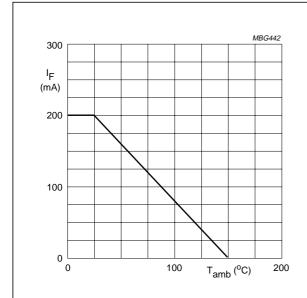
Note

1. Refer to SOT457 standard mounting conditions.

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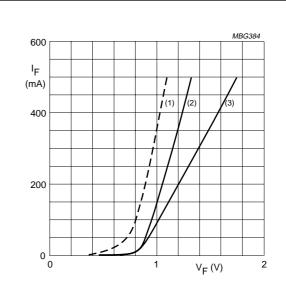
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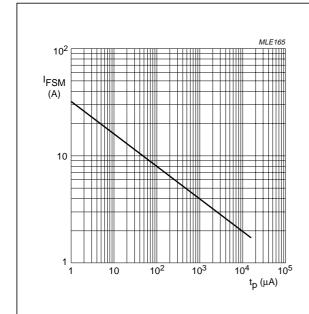
Device mounted on a FR4 printed-circuit board.

Fig.2 Maximum permissible continuous forward current as a function of ambient temperature.



- (1) $T_j = 150$ °C; typical values.
- (2) $T_i = 25$ °C; typical values.
- (3) $T_i = 25 \,^{\circ}C$; maximum values.

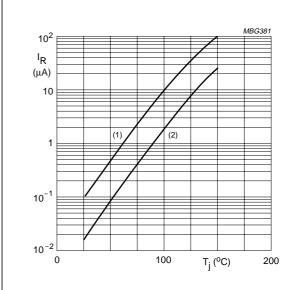
Fig.3 Forward current as a function of a forward voltage.



Based on square wave currents.

 $T_j = 25$ °C prior to surge.

Fig.4 Maximum permissible non-repetitive peak forward current as a function of pulse duration.



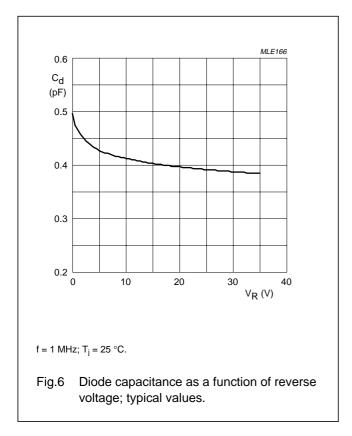
- (1) $V_R = V_{Rmax}$; maximum values.
- (2) $V_R = V_{Rmax}$; typical values.

Fig.5 Reverse current as a function of junction temperature.

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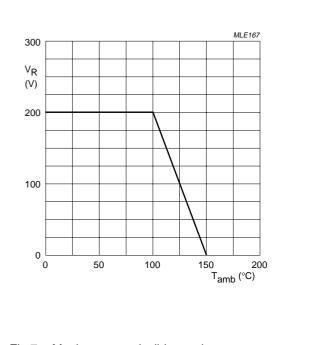
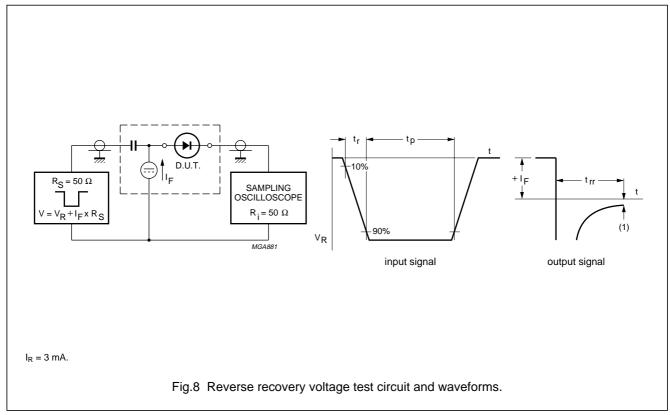


Fig.7 Maximum permissible continuous reverse voltage as a function of ambient temperature.



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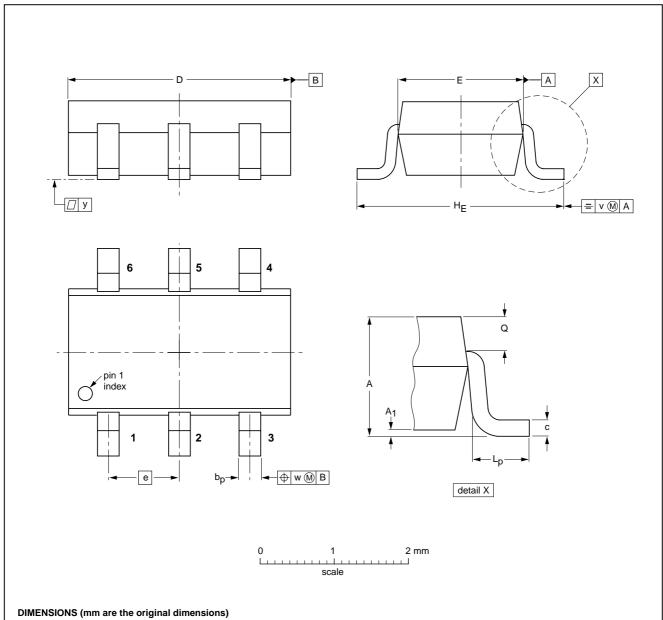
High-voltage switching diode array

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PACKAGE OUTLINE

Plastic surface mounted package; 6 leads

SOT457



| UNIT | Α | A ₁ | bp | С | D | E | е | HE | Lp | Q | v | w | у |
|------|------------|----------------|--------------|--------------|------------|------------|------|------------|------------|--------------|-----|-----|-----|
| mm | 1.1 0.9 | 0.1 0.013 | 0.40 0.25 | 0.26 0.10 | 3.1 2.7 | 1.7 1.3 | 0.95 | 3.0 2.5 | 0.6 0.2 | 0.33 0.23 | 0.2 | 0.2 | 0.1 |

| OUTLINE | | REFER | EUROPEAN | ISSUE DATE | | |
|---------|-----|-------|----------|------------|------------|---------------------------------|
| VERSION | IEC | JEDEC | EIAJ | | PROJECTION | ISSUE DATE |
| SOT457 | | | SC-74 | | | 97-02-28 01-05-04 |

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BAS21VD

DATA SHEET STATUS

| LEVEL | DATA SHEET STATUS ⁽¹⁾ | PRODUCT STATUS(2)(3) | DEFINITION |
|-------|-------------------------------------|-------------------------|--|
| I | Objective data | Development | This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice. |
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