

Design Idea DI-66

TOPSwitch[®]-GX 45 W, Universal Input, LCD Monitor Internal Supply



Application	Device	Power Output	Input Voltage	Output Voltage	Topology
LCD Monitor	TOP246Y/F	45 W	90-265 VAC	5 V / 12 V	Flyback

Design Highlights

- 66 kHz operation enables 0.45 W of standby output power delivery with 0.9 W of input power at 230 VAC
- Low no-load input power: <0.42 W at 230 VAC
- 82% efficient (min) at 90 VAC input and 45 W output
- Low component count: only 52 parts!
- Meets CISPR22 B EMI with > 10 dB of margin
- No TVS required for the primary snubber

Operation

Many of the built-in features of TOPSwitch-GX, such as line UV/OV, soft start, line feed-forward, accurate current limit, and frequency jitter have been used to reduce the component count, transformer size and overall system cost of this universal input, flyback power supply. This design is ideal for LCD monitor supplies that require low standby power consumption.

The switching frequency of U1 is set to 66 kHz by connecting its F pin to the CONTROL pin. Efficient 66 kHz operation results from the use of an EER3016S core to keep the number of primary turns low and to further reduce T1 leakage inductance. This reduces the losses due to both leakage inductance and winding capacitance. Low leakage inductance also allows the use of a low cost RCD snubber (C3, R2-R4 and D5) to clamp the U1 DRAIN voltage, while keeping standby power consumption low. Use of a glass-passivated normal recovery diode (D5) recycles the stored leakage energy and increases the overall efficiency. Resistor R4 dampens drain node ringing, and is necessary when a normal recovery diode is used.

Resistor R5 sets nominal UV and OV limits to 84 V and 378 V, respectively. Under-voltage lockout protects the supply from overheating during brownout, and eliminates power-up and power-down glitches. Overvoltage shutdown protects the power supply from line surges.

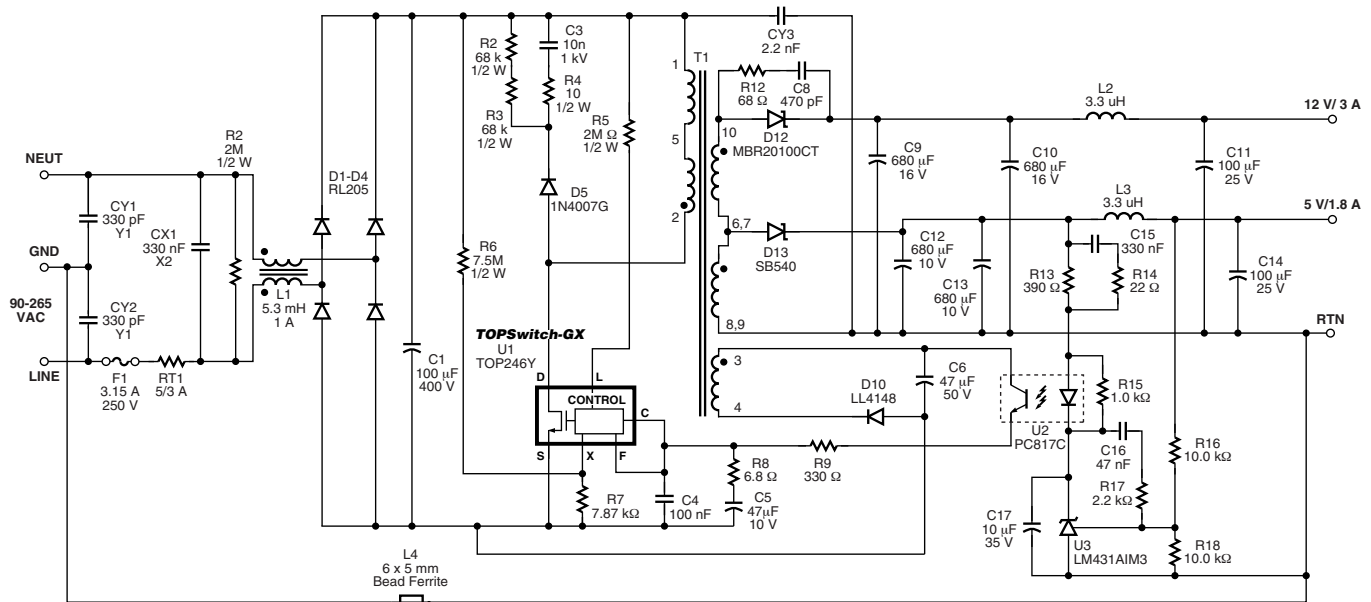


Figure 1. Circuit Diagram of a TOP246 Based LCD Monitor Power Supply.

PI-3743-120904

Effective EMI filtering is accomplished with only five parts (L1, CX1, CY1-CY3), due to the built-in frequency jitter function of *TOPSwitch-GX*.

Key Design Points

- Use 66 kHz operation to reduce standby power loss.
- To reduce the number of primary turns required for 66 kHz operation, the A_L of the T1 core must be large. Recommended cores are EER3016S and PQ2620.
- Split-primary transformer construction should be used to keep leakage inductance to a minimum.
- Use an RCD snubber with the normal recovery diode. Size resistors R2 and R3 for the highest value that ensures adequate drain voltage margin under overload conditions at high line.

TRANSFORMER PARAMETERS	
Core	Samwha EER3016S-PL-7 A_{LG} of 342 nH/T ²
Bobbin	10 Pin, EER3016S
Winding Details	1/2 Primary: 28 T, 26 AWG Bias: 7T, 2 x 26 AWG 12 V Secondary, 4T, 2 x 25 AWG Triple Insulated Wire 5 V Secondary: 3T, 3 x 25 AWG Triple Insulated Wire 1/2 Primary: 24T, 26 AWG
Winding Order (pin numbers)	1/2 Primary: 2-5, tape 1L Bias: 3-4, tape 2L 12 V Secondary: 10-6,7 5 V Secondary: 6,7-8,9, tape 2L 1/2 Primary: 5-1, tape 3L
Primary Inductance	919 μ H \pm 10%
Primary Resonant Frequency	1.1 MHz (Min)
Leakage Inductance	18 μ H (Max)

Table 1. Transformer Construction Information.

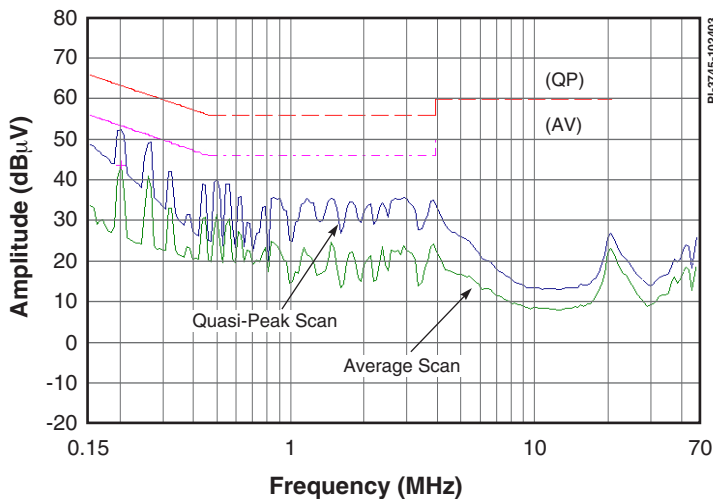


Figure 2. Conducted EMI, CISPR22B Limits, Maximum Load, Secondary Return Connected to Safety Ground, 230 VAC Input.

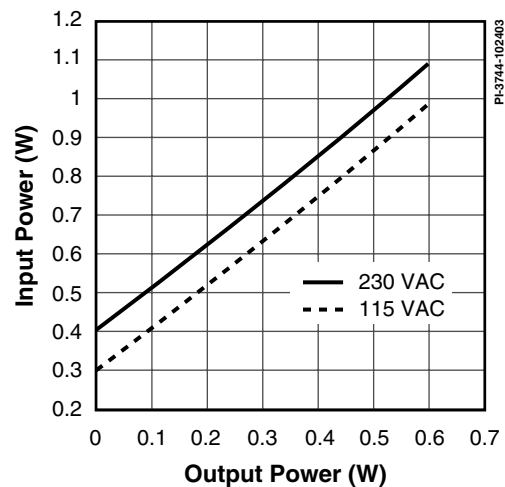


Figure 3. P_{in} vs. P_{out} , 5 V Output Loaded, No Load on 12 V Output.

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