

**IN74VHC373**

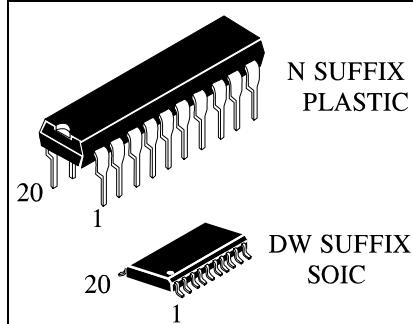
## Octal d-type latch with three state outputs

IN74VHC373 integrated circuits are designed for using in up-to-date high-performance computers, high-level electronic equipment for consumer application.

IN74VHC373 is identical in pinout to the IC series IN74HC373A, IN74HCT373A, IN74AC373A, IN74ACT373A.

Input voltage levels are compatible with standard C-MOS levels.

Output voltage levels are compatible with input levels of C-MOS, N-MOS and TTL ICs.

**ORDERING INFORMATION**

IN74VHC373N Plastic

IN74VHC373DW SOIC

T<sub>A</sub> = -40° to 85° C for all packages**Features:**

- Supply voltage 2.0 - 5.5 V.
- Low input current: 1.0 m $\mu$ A; 0.1 m $\mu$ A at T = 25 °C.
- Output current 8 mA.
- Latchup current not less than 300 mA at T = 85 °C.
- Tolerable value of static potential not less than 2000V as per human body model (HBM) and not less than 200V as per machine model (MM).

## IN74VHC373 truth table

Input		Output
$\bar{G}$	A	Y
L	L	L
L	H	H
H	X	Z

### *Pins description in IN74VHC373*

<i>Pinout</i>		Pin No.	Symbol	Description
OE	01	01	OE	Input OUTPUT ENABLE
Q0	02	02	Q0	Data output
D0	03	03	D0	Data input
D1	04	04	D1	Data input
Q1	05	05	Q1	Data output
Q2	06	06	Q2	Data output
D2	07	07	D2	Data input
D3	08	08	D3	Data input
Q3	09	09	Q3	Data output
GND	10	10	GND	Common output
		11	LE	Recording enable input
		12	Q4	Data output
		13	D4	Data input
		14	D5	Data input
		15	Q5	Data output
		16	Q6	Data output
		17	D6	Data input
		18	D7	Data input
		19	Q7	Data output
		20	Vcc	Supply output from voltage source

**Absolute maximum conditions\***

Parameter, unit	Symbol	Value	
		min	max
Supply voltage, V	V <sub>CC</sub>	-0.5	7.0
Input voltage, V	V <sub>in</sub>	-0.5	7.0
Output voltage, V	V <sub>out</sub>	-0.5	V <sub>CC</sub> +0.5V
Input diode current, mA	I <sub>ik</sub>	—	-20
Current of common output and supply output, mA	I <sub>cc</sub>	—	± 75
Output current, mA	I <sub>out</sub>	—	± 25
Output diode current, mA	I <sub>ok</sub>	—	± 20
Dissipated power, mW	P <sub>d</sub>	—	180

\*Under absolute maximum conditions operation of microcircuit is not guaranteed. Operation is guaranteed under maximum conditions

**Maximum conditions**

Parameter, unit	Symbol	Value	
		min	max
Supply voltage, V	V <sub>CC</sub>	2.0	5.5
Input voltage, V	V <sub>in</sub>	0	V <sub>CC</sub>
Output voltage, V	V <sub>out</sub>	0	V <sub>CC</sub>
Output current, mA	I <sub>out</sub>	—	± 8.0
Input rise and fall time, ns/V at V <sub>CC</sub> = (3.3 ± 0.3) V at V <sub>CC</sub> = (5.0 ± 0.5) V	t <sub>LH</sub> , t <sub>HL</sub>	0	100
		0	20

**DC electrical characteristics**

Symbol	Parameter	Test conditions	V <sub>CC</sub> , V	Value				Unit	
				25 °C		-40 to 85 °C			
				min	max	min	max		
V <sub>IH</sub>	High input voltage	-	2.0	1.5	-	1.5	-	V	
			3.0-5.5	0.7V <sub>CC</sub>	-	0.7V <sub>CC</sub>	-		
V <sub>IL</sub>	Low input voltage	-	2.0	-	0.5	-	0.5		
			3.0-5.5	-	0.3V <sub>CC</sub>	-	0.3V <sub>CC</sub>		
V <sub>OH</sub>	High output voltage  V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>O</sub> = -50 μA	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>O</sub> = -50 μA	2.0	1.92	-	1.9	-		
			3.0	2.92	-	2.9	-		
			4.5	4.42	-	4.4	-		
			5.5	5.52	-	5.4	-		
		V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>O</sub> = -4 mA	3.0	2.58	-	2.48	-		
		V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>O</sub> = -8 mA	4.5	3.94	-	3.80	-		
		V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>O</sub> = 50 μA	2.0	-	0.09	-	0.1		
			3.0	-	0.09	-	0.1		
			4.5	-	0.09	-	0.1		
			5.5	-	0.09	-	0.1		
		V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>O</sub> = 4 mA	3.0	-	0.36	-	0.44		
		V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> I <sub>O</sub> = 8 mA	4.5	-	0.36	-	0.44		
I <sub>OZ</sub>	Output current in "off" state	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub> V <sub>O</sub> = V <sub>CC</sub> or 0V	5.5	-	±0.25	-	±2.5	uA	
I <sub>I</sub>	Input current	V <sub>I</sub> = 5.5V or 0V	0 - 5.5	-	±0.1	-	±1.0		
I <sub>CC</sub>	Consumption current	V <sub>I</sub> = V <sub>CC</sub> or 0V	5.5	-	4.0	-	40.0		

**Noise characteristics (C<sub>L</sub> = 50 pF)**

Symbol	Parameter	V <sub>CC</sub> , V	Value		Unit
			min	max	
V <sub>OLP</sub>	Positive noise of low output voltage	5.0	-	0.9	V
V <sub>OLV</sub>	Negative noise of low output voltage	5.0	-0.9	-	
V <sub>IHD</sub>	Input dynamic high voltage	5.0	3.5	-	
V <sub>ILD</sub>	Input dynamic low voltage	5.0	-	1.5	

**AC electrical characteristics ( $t_{LH} = t_{HL} = 3.0$  ns,  $R_L = 1$  kOhm)**

Symbol	Parameter	Test conditions	$V_{CC}, V$	$C_L, pF$	Value				Unit	
					25°C		-40°C to 85°C			
					min	max	min	max		
$t_{PHL}, t_{PLH}$	Popagation delay time when switching «on», «off» from input LE to output Q	Fig. 1	$3.3 \pm 0.3$	15	—	11.0	—	13.0	ns	
				50	—	14.5	—	16.5		
			$5.0 \pm 0.5$	15	—	7.2	—	8.5		
				50	—	9.2	—	10.5		
	From input D to output Q		$3.3 \pm 0.3$	15	—	11.4	—	13.5		
				50	—	14.9	—	17.0		
			$5.0 \pm 0.5$	15	—	7.2	—	8.5		
				50	—	9.2	—	10.5		
$t_{PHZ}, t_{PLZ}$	Propagation delay time under transition from high, low level into «off» state	Fig.2	$3.3 \pm 0.3$	50	—	13.2	—	15.0		
			$5.0 \pm 0.5$	50	—	9.2	—	10.5		
$t_{PZH}, t_{PZL}$	Propagation delay time under transition from «off» state into high, low level	Fig.2	$3.3 \pm 0.3$	15	—	11.4	—	13.5		
				50	—	14.9	—	17.0		
			$5.0 \pm 0.5$	15	—	8.1	—	9.5		
				50	—	10.1	—	11.5		
$t_{SU}$	Time of setting signal D relativey to LE	Fig.3	$3.3 \pm 0.3$	15	4.0	—	4.0	—		
				50	4.0	—	4.0	—		
			$5.0 \pm 0.5$	15	4.0	—	4.0	—		
				50	4.0	—	4.0	—		
$t_H$	Retention time, D signal to LE	Fig.3	$3.3 \pm 0.3$	15	1.0	—	1.0	—		
				50	1.0	—	1.0	—		
			$5.0 \pm 0.5$	15	1.0	—	1.0	—		
				50	1.0	—	1.0	—		
$t_W$	Pulse duration of LE signal	Fig.3	$3.3 \pm 0.3$	15	5.0	—	5.0	—		
				50	5.0	—	5.0	—		
			$5.0 \pm 0.5$	15	5.0	—	5.0	—		
				50	5.0	—	5.0	—		
$t_{OSLH}, t_{OSHJ}$	Propagation delays difference between outputs	—	$3.3 \pm 0.3$	50	—	1.5	—	1.5		
			$5.5 \pm 0.5$	50	—	1.0	—	1.0		

**Capacitance characteristics**

Symbol	Parameter	Test conditions	$V_{CC}, V$	Value		Unit	
				25 °C			
				min	max		
$C_1$	Input capacity	-	5.0		10	pF	
$C_O$	Output capacity	-	5.0		12	pF	
$C_{PD}$	Dynamic capacity	$V_I = 0$ V or $V_{CC}$	5.0		54	pF	

*- Time diagram of input and output pulses*

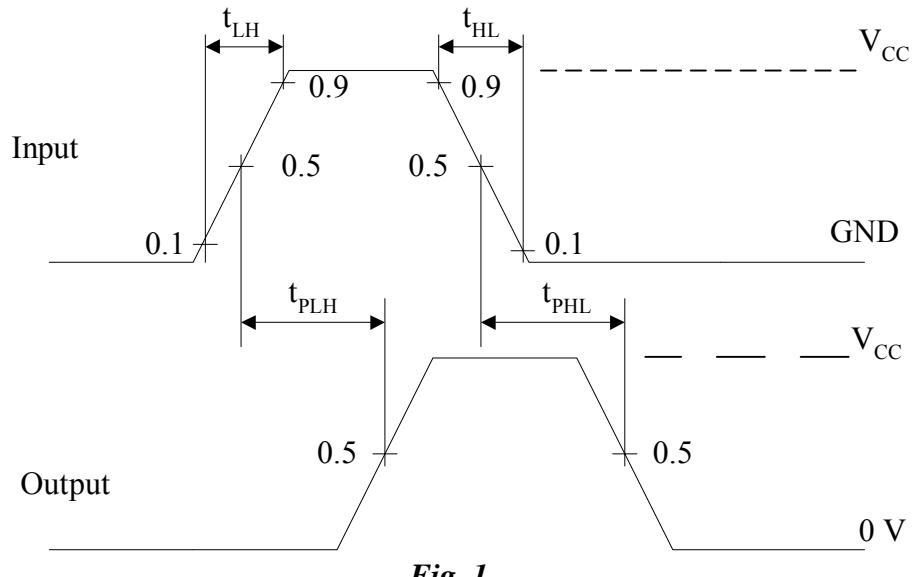


Fig. 1

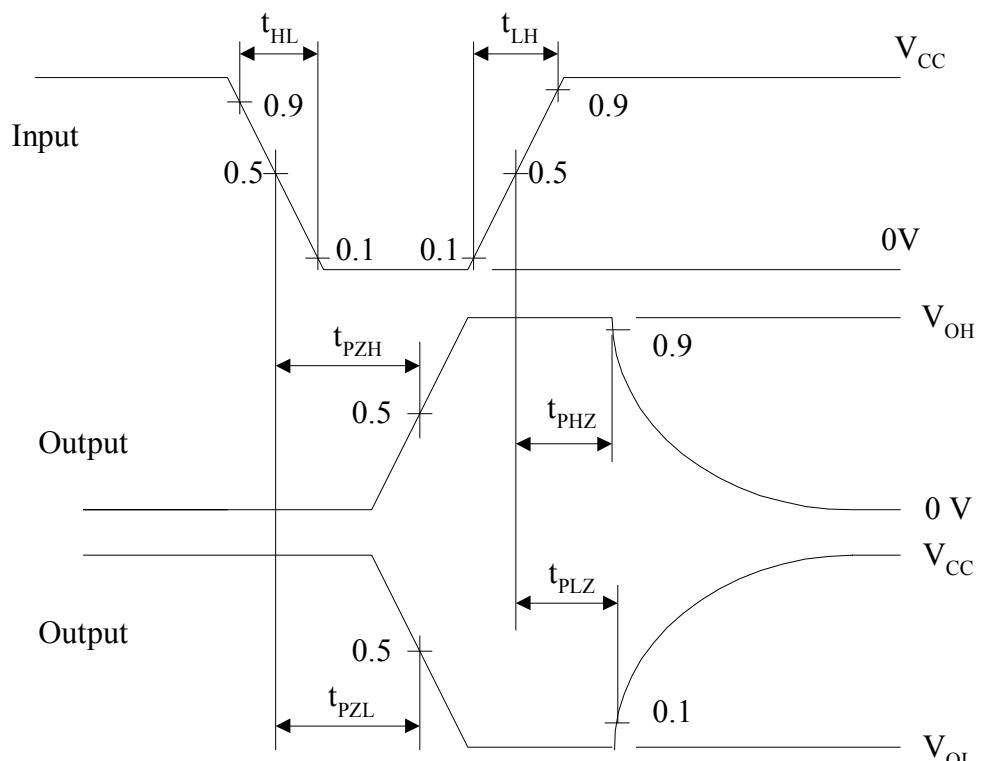


Fig. 2

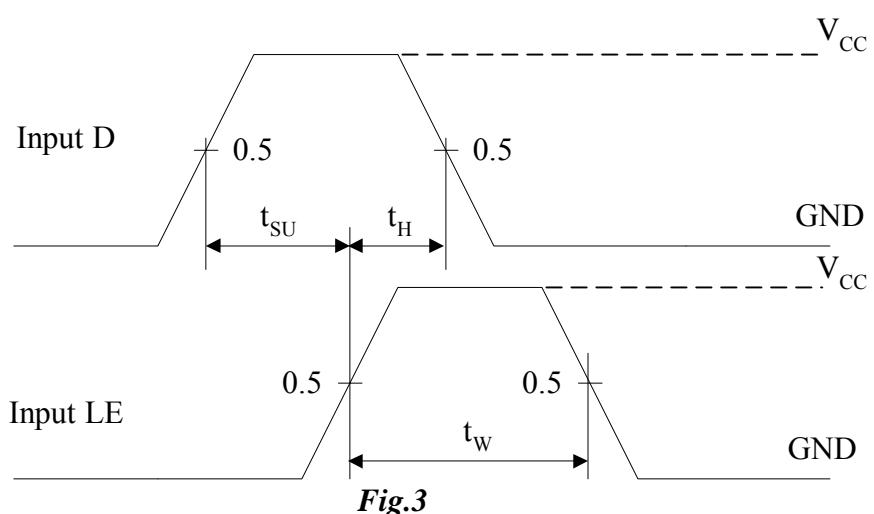
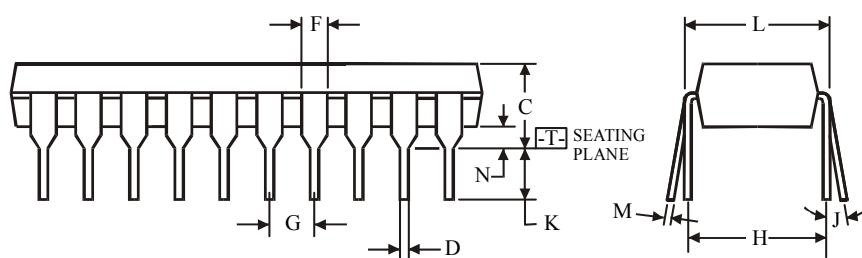
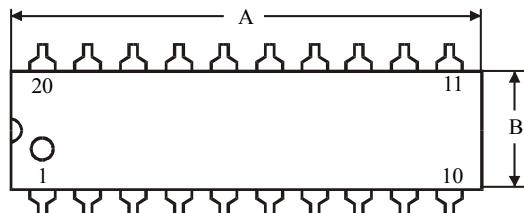
*Time diagram of input pulses*

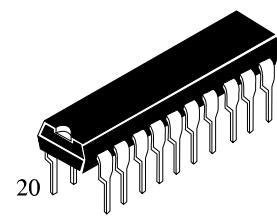
Fig.3

**N SUFFIX PLASTIC DIP  
(MS - 001AD)**
**NOTES:**

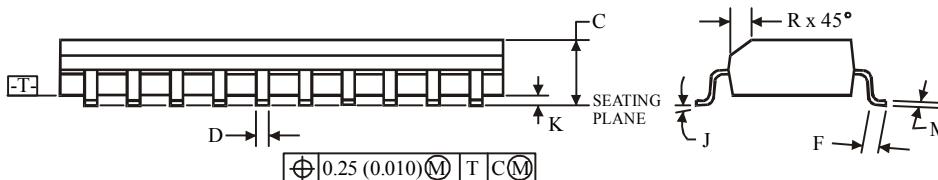
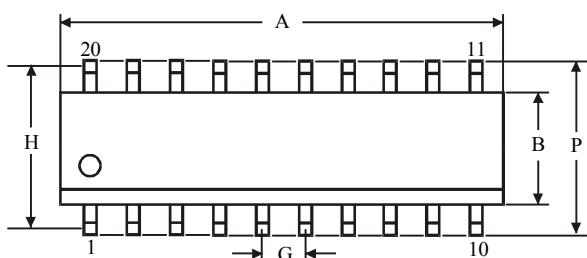
$$\oplus 0.25 (0.010) \text{ M T}$$

1. Dimensions "A", "B" do not include mold flash or protrusions.

Maximum mold flash or protrusions 0.25 mm (0.010) per side.



	Dimension, mm	
Symbol	MIN	MAX
A	24.89	26.92
B	6.1	7.11
C		5.33
D	0.36	0.56
F	1.14	1.78
G		2.54
H		7.62
J	0°	10°
K	2.92	3.81
L	7.62	8.26
M	0.2	0.36
N	0.38	

**D SUFFIX SOIC  
(MS - 013AC)**
**NOTES:**

1. Dimensions A and B do not include mold flash or protrusion.
2. Maximum mold flash or protrusion 0.15 mm (0.006) per side for A; for B - 0.25 mm (0.010) per side.



	Dimension, mm	
Symbol	MIN	MAX
A	12.6	13
B	7.4	7.6
C	2.35	2.65
D	0.33	0.51
F	0.4	1.27
G		1.27
H		9.53
J	0°	8°
K	0.1	0.3
M	0.23	0.32
P	10	10.65
R	0.25	0.75