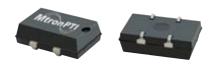
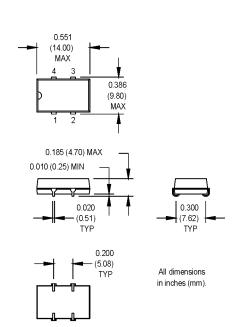
M3R Series

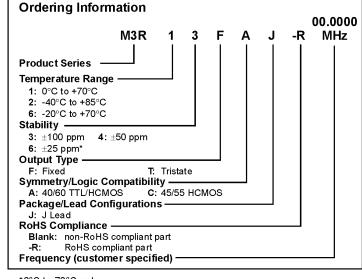
9x14 mm, 3.3 Volt, HCMOS/TTL, Clock Oscillator











^{*0°}C to 70°C only

Pin Connections

PIN	FUNCTION					
1	N/C or Tristate					
2	Gro und					
3	Output					
4	+Vdd					

0.200 (5.08) 0.050 (1.27) 0.346 (8.80)

0.118 (3.00)

SUGGESTED SOLDER PAD LAYOUT

NOTE: A capacitor of value 0.01 μF or greater between Vdd and Ground is recommended.

	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes
tions	Frequency Range	F	1		80	MHz	
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C	
	Frequency Stability	∆ F/F	(See Ordering Information)				
	Aging						
	1st Year		-5		+5	ppm	
	Thereafter (per year)		-5		+5	ppm	
lica	Input Voltage	Vdd	3.0	3.3	3.6	V	
Electrical Specifications	Input Current	ldd			15	mA	1.000 to 27.000 MHz
					20	mA	27.001 to 50.000 Mhz
					40	mA	50.001 to 80.000 MHz
	Output Type						HCMOS/TTL
	Load				15	pF	See Note 1
	Symmetry (Duty Cycle)		(See Ordering Information)				50% Vdd Level
	Logic "1" Level	Voh	90% Vdd			V	HCMOS Load
	Logic "0" Level	Vol			10% Vdd	V	HCMOS Load
	Output Current				±4	mA	
	Rise/Fall Time	Tr/Tf			8	ns	See Note 2
	Tristate Function		Input Logic "1" or floating; output active Input Logic "0"; output disables to high-Z				
	Start up Time				10	ms	
	Random Jitter	Rj		5	12	ps RMS	1-Sigma
	1 Coo lood aircuit diagram	<u> </u>		•	•	•	

- 1. See load circuit diagram #2
- 2. Rise/Fall times are measured between 10% Vdd and 90% Vdd with HCMOS load
- 3. TTL output drive capability is 2 TTL (10 LS-TTL)

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.



MtronPTI Lead Free Solder Profile

