ELECTRONICS, INC.
44 FARRAND STREET
BLOOMFIELD, NJ 07003
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NTE1817
Integrated Circuit
Module, AF PO, 20W,
Dual Power Supply

Features:

- Muting Curcuit to Cut Off Pop Noise

Absolute Maximum Ratings: (T_A = +25°C unless otherwise specified)

Maximum Supply Voltage, V_{CC}max . . . . . ±34.5V
Junction Temperature, T_J . . . . . +150°C
Operating Case Temperature, T_C . . . . . +125°C
Storage Temperature Range, T_{stg} . . . . . –30° to +125°C
Thermal Resistance, Junction–to–Case, R_{thJC} . . . . . 2.6°C/W
Available Time for Load Shorted (V_{CC} = ±23V, R_L = 8Ω, f = 50Hz, P_O = 20W), t_s 2sec

Recommended Operating Conditions: (T_A = +25°C unless otherwise specified)

Recommended Supply Voltage, V_{CC} . . . . . ±23V
Load Resistance, R_L 8Ω

Electrical Characteristics: (T_A = +25°C, V_{CC} = ±23V, R_L = 8Ω (Non–Inductive Load), R_g = 600Ω, V_G = 40dB unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Quiescent Current	I_{CCO}	V_{CC} = ±28V	20	40	100	mA
Output Power	P_O	THD = 0.4%, f = 20Hz to 20kHz	20	–	–	W
		V_{CC} = ±20V, THD = 1%, R_L = 4Ω, f = 1kHz	20	–	–	W
Total Harmonic Distortion	THD	P_O = 1W, f = 1kHz	–	–	0.3	%
Frequency Response	f_L, f_H	P_O = 1W, –3dB	20 to 50k			Hz
Input Resistance	r_i	P_O = 1W, f = 1kHz	–	55	–	kΩ
Output Noise Voltage	V_{NO}	V_{CC} = ±28V, R_g = 10kΩ	–	–	1.2	mV_{rms}
Middle–Point Voltage	V_N	V_{CC} = ±28V	–70	0	+70	mV

Note 1. For power supply at the time of test, use a constant–voltage power supply unless otherwise specified.

Pin Connection Diagram
(Front View)