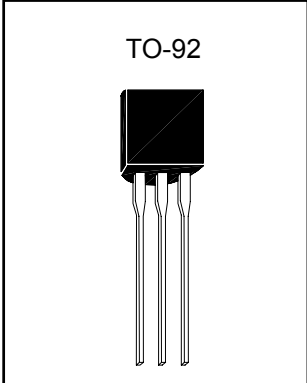


**Three Terminal Low Current Positive Voltage Regulators**

# PL78L12XA3

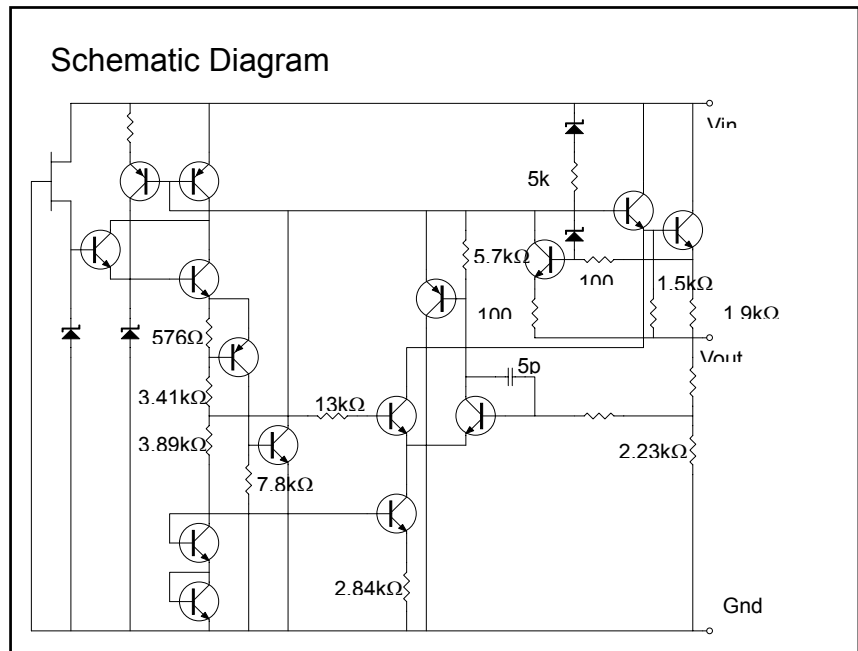
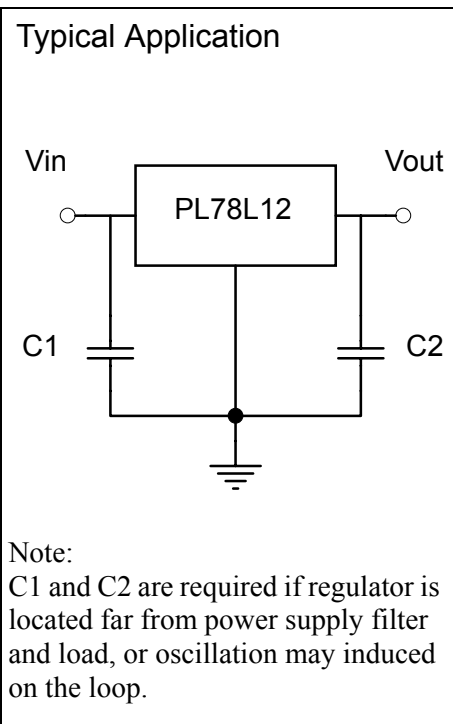


**Description**

These regulators employ internal current-limiting and thermal-shutdown, making them essentially indestructible. They can deliver up to 100mA output current, if the case temperature can keep in  $T_c=25^{\circ}\text{C}$ . They are intended as fixed voltage regulators in a wide range of applications including local (on-card) regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power pass elements to make high-current voltage regulators. The PL78L12 used as a Zener diode/resistor combination replacement, offers an effective output impedance improvement of typically two orders of magnitude, along with lower quiescent current and lower noise.

**Absolute Maximum Ratings** ( $T_a=25^{\circ}\text{C}$ )

- Input Voltage..... 35 V
- Total Power Dissipation..... Internally limited
- Operating Temperature Range.....  $0^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$
- Maximum Junction Temperature.....  $125^{\circ}\text{C}$
- Storage Temperature Range.....  $-55^{\circ}\text{C}$  to  $+150^{\circ}\text{C}$
- Lead Temperature (Soldering 10S).....  $260^{\circ}\text{C}$





## Ordering Information

Device	Output Voltage Tolerance
PL78L12AA3	3%
PL78L12BA3	5%

## Electrical Characteristics

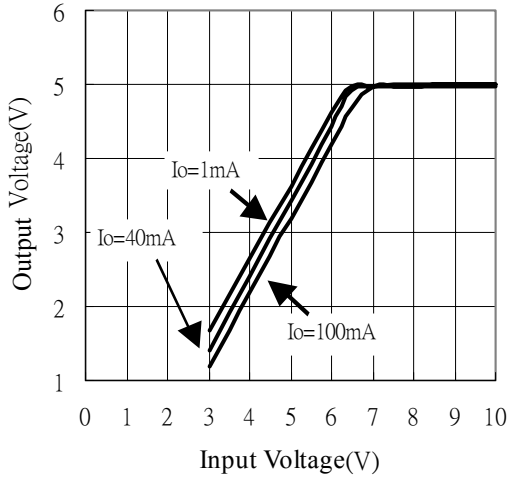
$V_{in}=19V$ ,  $I_{out}=40mA$ ,  $0^{\circ}C \leq T_j \leq 125^{\circ}C$ ,  $C_{in}=0.33\mu F$ ,  $C_{out}=0.1\mu F$  (unless otherwise noted)

Symbol	Parameter	Conditions	PL78L12A			Units
			Min	Typ	Max	
Vo	Output Voltage	Tj=25°C	11.64	12	12.36	V
		1mA ≤ Io ≤ 70mA	11.64	-	12.36	
		1mA ≤ Io ≤ 40mA and 14.5V ≤ Vin ≤ 27V	11.64	-	12.36	
ΔVo	Line Regulation	Tj=25°C, 16V ≤ Vin ≤ 27V	-	20	100	mV
		Tj=25°C, 14.5V ≤ Vin ≤ 27V	-	30	180	
ΔVo	Load Regulation	Tj=25°C, 1mA ≤ Io ≤ 40mA	-	10	50	mV
		Tj=25°C, 1mA ≤ Io ≤ 100mA	-	30	100	
IQ	Quiescent Current	Tj=25°C	-	3	5	mA
ΔIQ	Quiescent Current Change	1mA ≤ Io ≤ 40mA	-	-	0.1	mA
		16V ≤ Vin ≤ 27V	-	-	1	
Vn	Output Noise Voltage	Ta=25°C, 10Hz ≤ f ≤ 10KHz	-	80	-	μV
ΔVin / ΔVout	Ripple Rejection	f=120Hz	40	54	-	dB
VD	Dropout Voltage	Tj=25°C	-	1.7	-	V
	Input Voltage Require To Maintain Line Regulation	Tj=25°C	14.5	-	-	V

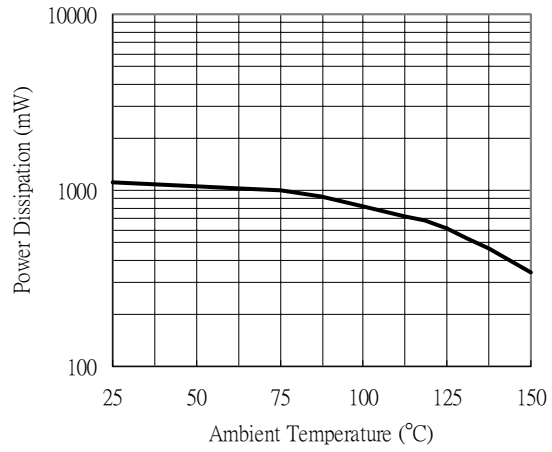
Symbol	Parameter	Conditions	PL78L12B			Units
			Min	Typ	Max	
Vo	Output Voltage	Tj=25°C	11.5	12	12.5	V
		1mA ≤ Io ≤ 70mA	11.4	-	12.6	
		1mA ≤ Io ≤ 40mA and 14.5V ≤ Vin ≤ 27V	11.4	-	12.6	
ΔVo	Line Regulation	Tj=25°C, 16V ≤ Vin ≤ 27V	-	20	100	mV
		Tj=25°C, 14.5V ≤ Vin ≤ 27V	-	30	180	
ΔVo	Load Regulation	Tj=25°C, 1mA ≤ Io ≤ 40mA	-	10	50	mV
		Tj=25°C, 1mA ≤ Io ≤ 100mA	-	30	100	
IQ	Quiescent Current	Tj=25°C	-	3	5	mA
ΔIQ	Quiescent Current Change	1mA ≤ Io ≤ 40mA	-	-	0.1	mA
		16V ≤ Vin ≤ 27V	-	-	1	
Vn	Output Noise Voltage	Ta=25°C, 10Hz ≤ f ≤ 10KHz	-	80	-	μV
ΔVin / ΔVout	Ripple Rejection	f=120Hz	40	54	-	dB
VD	Dropout Voltage	Tj=25°C	-	1.7	-	V
	Input Voltage Require To Maintain Line Regulation	Tj=25°C	14.5	-	-	V

**Characteristic Curves**

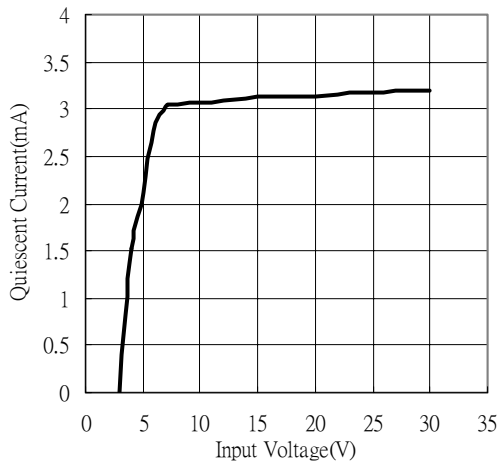
Dropout Characteristics



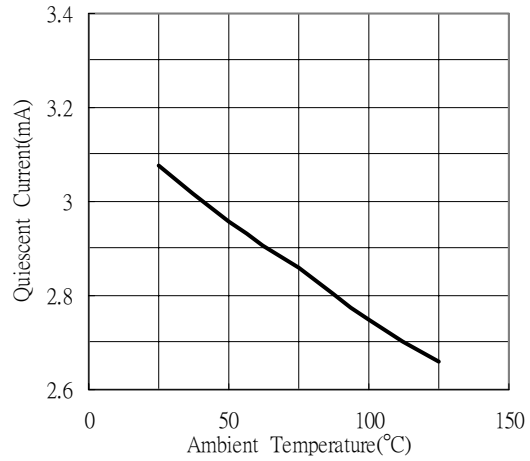
Maximum Average Power Dissipation



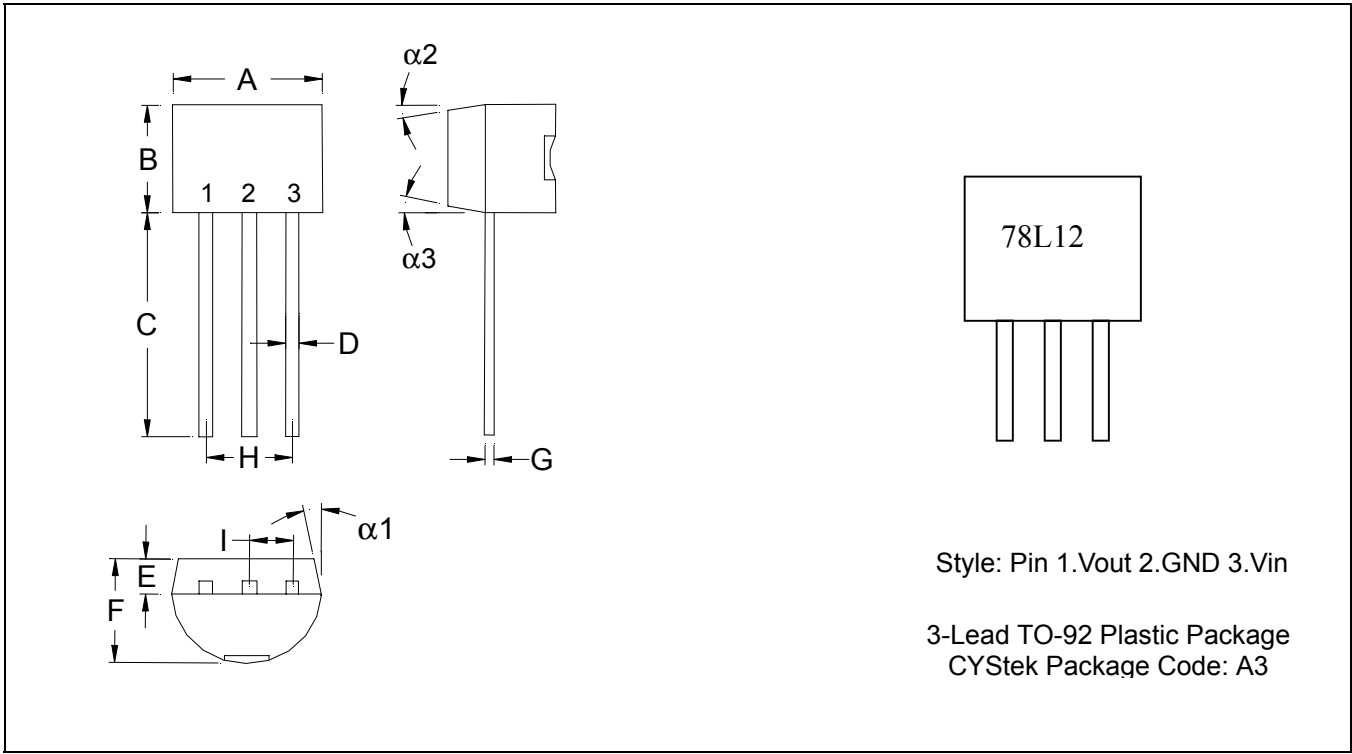
Quiescent Current



Quiescent Current



**TO-92 Dimension**



\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.1704	0.1902	4.33	4.83	G	0.0142	0.0220	0.36	0.56
B	0.1704	0.1902	4.33	4.83	H	-	*0.1000	-	*2.54
C	0.5000	-	12.70	-	I	-	*0.0500	-	*1.27
D	0.0142	0.0220	0.36	0.56	$\alpha 1$	-	*5°	-	*5°
E	-	*0.0500	-	*1.27	$\alpha 2$	-	*2°	-	*2°
F	0.1323	0.1480	3.36	3.76	$\alpha 3$	-	*2°	-	*2°

- Notes: 1.Controlling dimension: millimeters.  
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.  
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

**Material:**

- Lead: 42 Alloy ; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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