



# SPC6601

## N & P Pair Enhancement Mode MOSFET

### DESCRIPTION

The SPC6601 is the N- and P-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

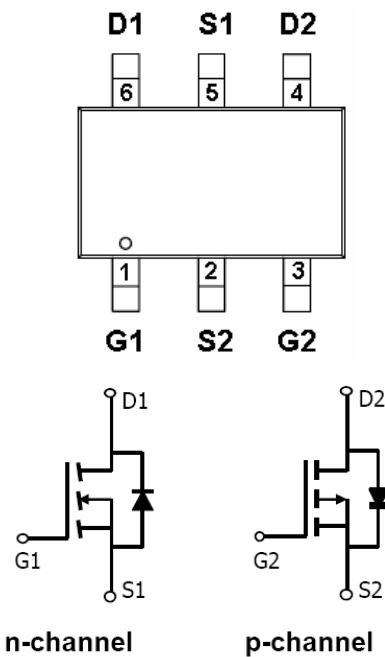
### FEATURES

- ◆ N-Channel
  - 30V/2.8A,  $R_{DS(ON)} = 68m\Omega @ V_{GS} = 10V$
  - 30V/2.3A,  $R_{DS(ON)} = 78m\Omega @ V_{GS} = 4.5V$
  - 30V/1.5A,  $R_{DS(ON)} = 108m\Omega @ V_{GS} = 2.5V$
- ◆ P-Channel
  - 30V/-2.8A,  $R_{DS(ON)} = 105m\Omega @ V_{GS} = -10V$
  - 30V/-2.5A,  $R_{DS(ON)} = 120m\Omega @ V_{GS} = -4.5V$
  - 30V/-1.5A,  $R_{DS(ON)} = 150m\Omega @ V_{GS} = -2.5V$
- ◆ Super high density cell design for extremely low  $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TSOP- 6P package design

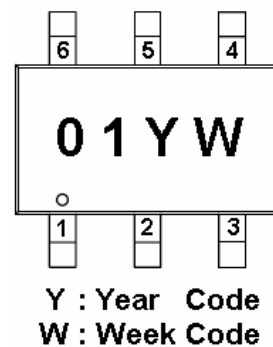
### APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

### PIN CONFIGURATION( TSOP- 6P )



### PART MARKING





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### PIN DESCRIPTION

Pin	Symbol	Description
1	G1	Gate 1
2	S2	Source 2
3	G2	Gate 2
4	D2	Drain 2
5	S1	Source 1
6	D1	Drain1

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPC6601ST6RG	TSOP- 6P	01YW

※ Week Code : A ~ Z ( 1 ~ 26 ) ; a ~ z ( 27 ~ 52 )

※ SPC6601ST6RG : Tape Reel ; Pb – Free

### ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical		Unit	
		N-Channel	P-Channel		
Drain-Source Voltage	V <sub>DSS</sub>	30	-30	V	
Gate –Source Voltage	V <sub>GSS</sub>	±12	±12	V	
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	TA=25°C	-2.8	A	
		TA=70°C	-2.1		
Pulsed Drain Current	I <sub>DM</sub>	10	-8	A	
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	1.25	-1.4	A	
Power Dissipation	P <sub>D</sub>	1.15		W	
		0.75			
Operating Junction Temperature	T <sub>J</sub>	-55/150		°C	
Storage Temperature Range	T <sub>STG</sub>	-55/150		°C	
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	T ≤ 10sec	50	52	°C/W
		Steady State	90	90	



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### ELECTRICAL CHARACTERISTICS

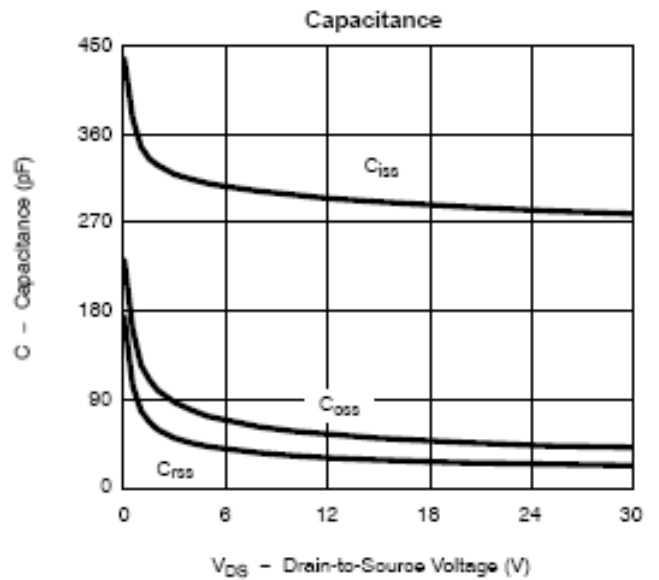
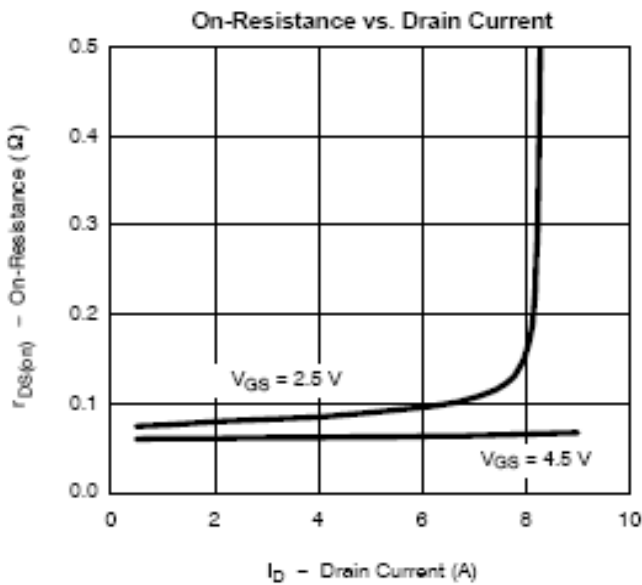
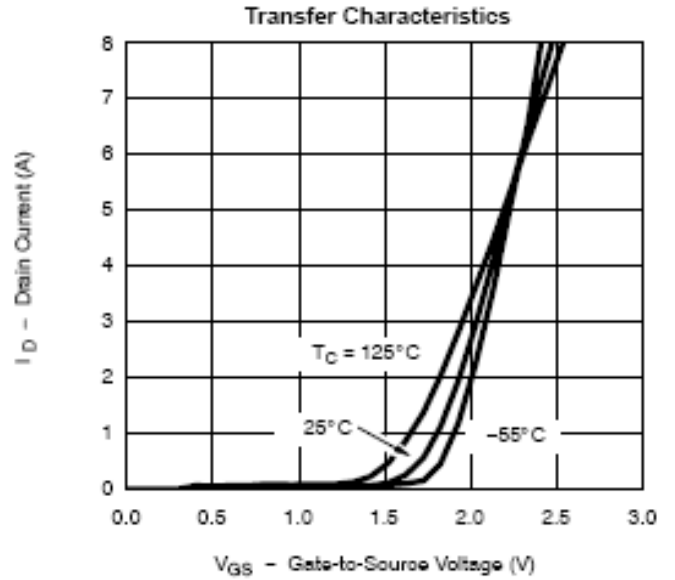
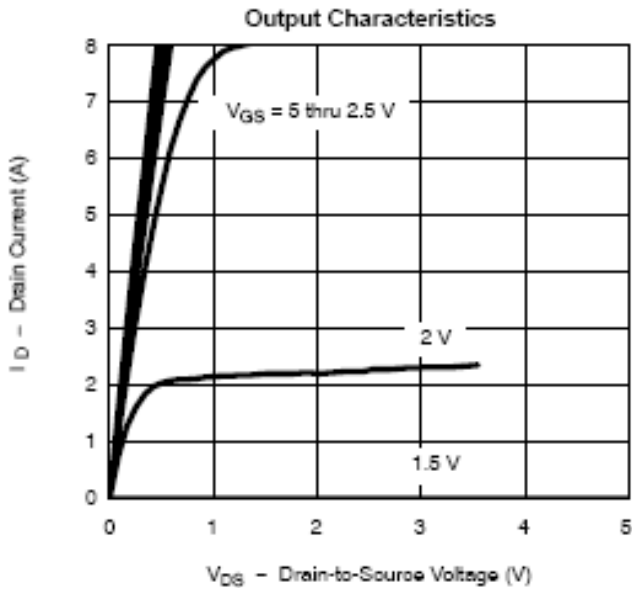
(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit	
<b>Static</b>							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> = 250uA	N-Ch	30		V	
		V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	P-Ch	-30			
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	N-Ch	0.8	1.6		
		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	P-Ch	-0.4	-1.0		
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V	N-Ch		±100	nA	
		V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V	P-Ch		±100		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24V, V <sub>GS</sub> =0V	N-Ch		1	uA	
		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V	P-Ch		-1		
		V <sub>DS</sub> = 24V, V <sub>GS</sub> =0V T <sub>J</sub> =55°C	N-Ch		10		
		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V T <sub>J</sub> =55°C	P-Ch		-10		
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5V, V <sub>GS</sub> = 10V	N-Ch	6		A	
		V <sub>DS</sub> ≤ -5V, V <sub>GS</sub> = -10V	P-Ch	-6			
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.8A	N-Ch		0.048	0.068	Ω
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-2.8A	P-Ch		0.077	0.105	
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2.3A	N-Ch		0.054	0.078	
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.5A	P-Ch		0.092	0.120	
		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 1.5A	N-Ch		0.079	0.108	
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1.5A	P-Ch		0.118	0.150	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =4.5V, I <sub>D</sub> =2.8A	N-Ch		4.6	S	
		V <sub>DS</sub> =-10V, I <sub>D</sub> =-2.8A	P-Ch		4		
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1.25A, V <sub>GS</sub> =0V	N-Ch		0.8	1.2	V
		I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V	P-Ch		-0.8	-1.2	
<b>Dynamic</b>							
Total Gate Charge	Q <sub>g</sub>	N-Channel V <sub>DS</sub> =15 , V <sub>GS</sub> =4.5V , I <sub>D</sub> =2.0A P-Channel V <sub>DS</sub> =-15V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-2.0A	N-Ch		4.2	6	nC
Gate-Source Charge	Q <sub>gs</sub>		P-Ch		5.8		
			N-Ch		0.6		
Gate-Drain Charge	Q <sub>gd</sub>		P-Ch		0.8		
			N-Ch		1.5		
Turn-On Time	td(on)		N-Ch		2.5		
	tr	P-Ch		6			
Turn-Off Time		td(off)	N-Ch		2.5		
	P-Ch			3.9			
	N-Ch			20			
	P-Ch			40			
	tf	N-Ch		4			
		P-Ch		15			



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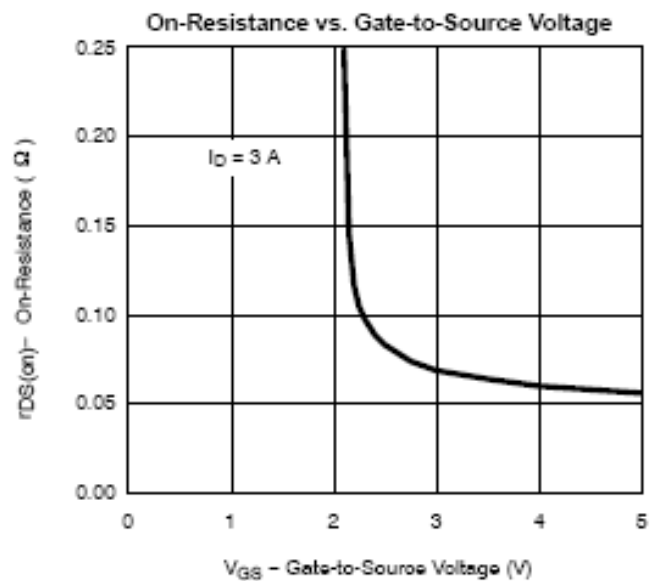
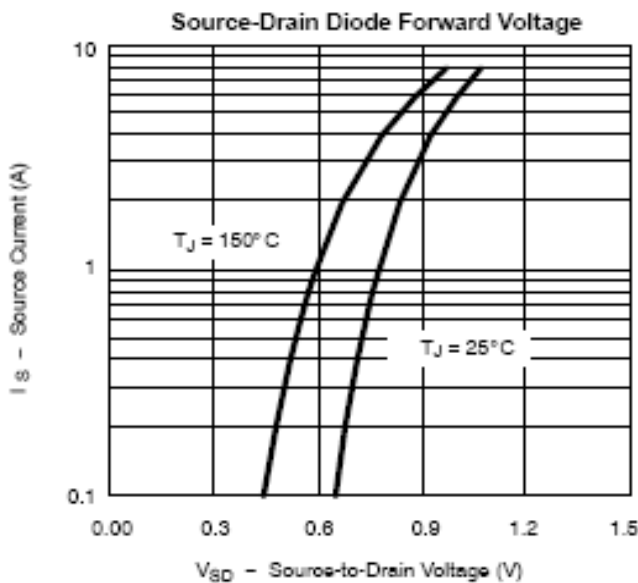
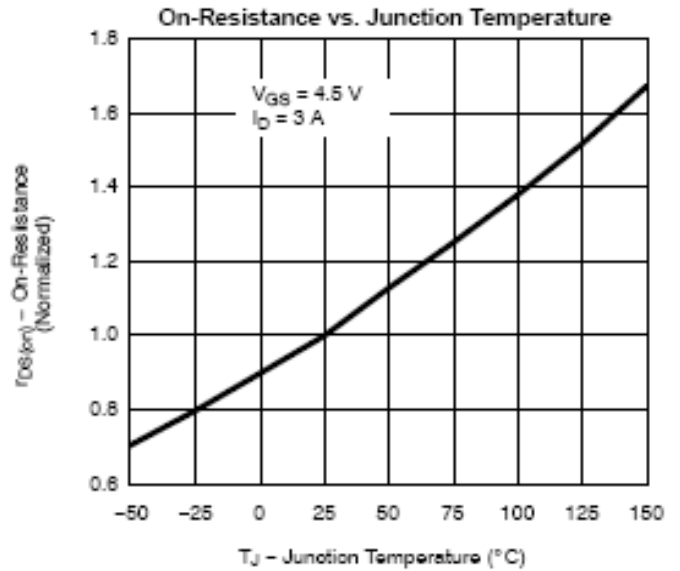
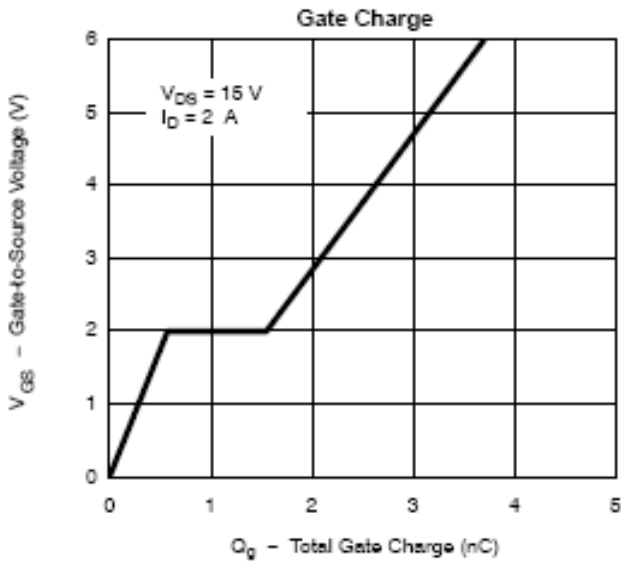
## TYPICAL CHARACTERISTICS ( N-Channel )





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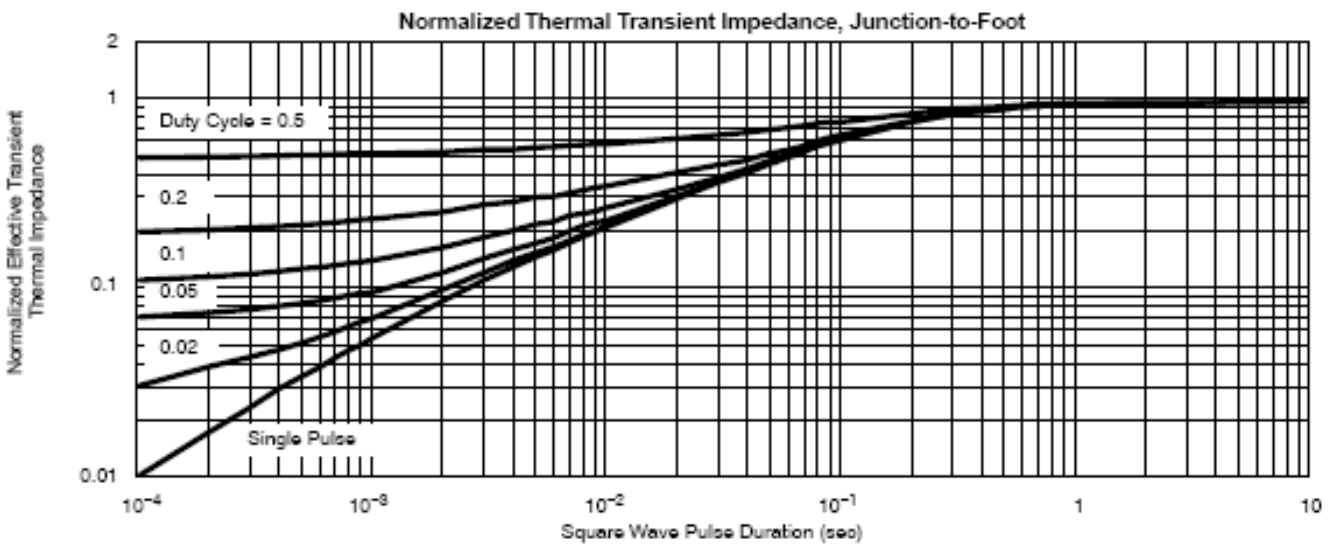
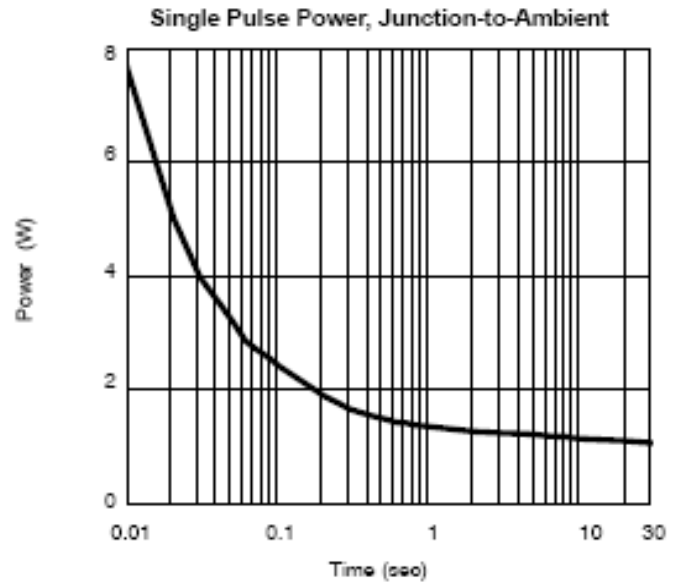
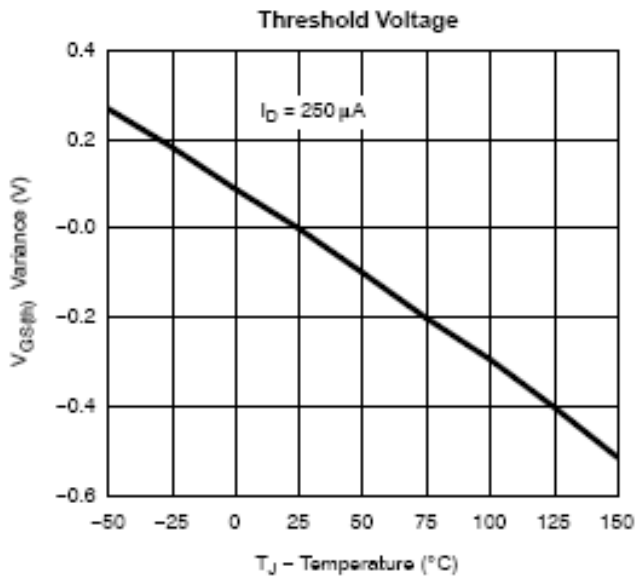
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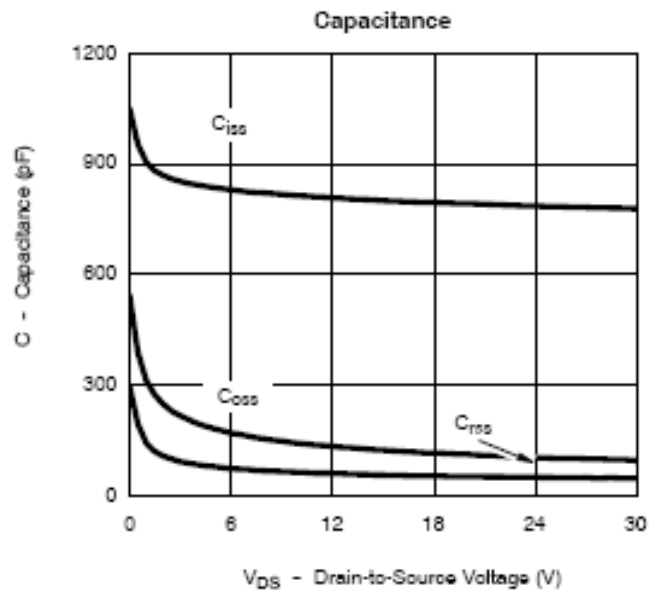
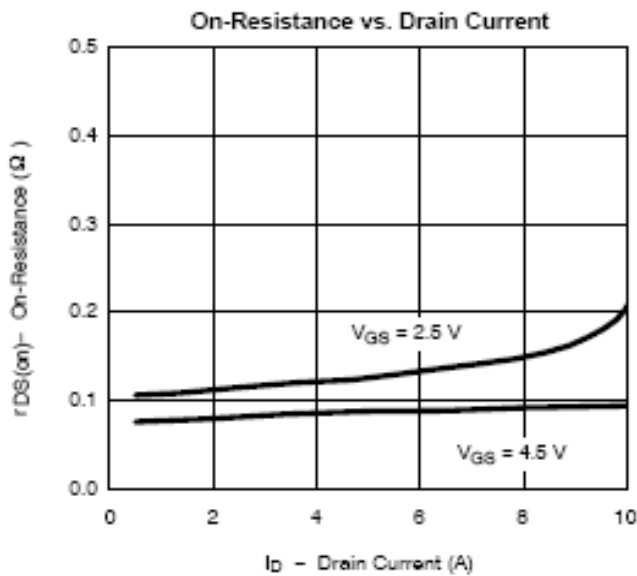
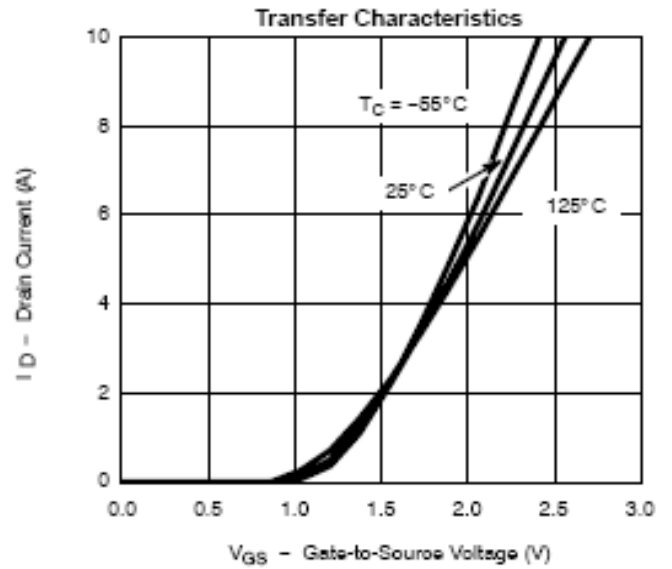
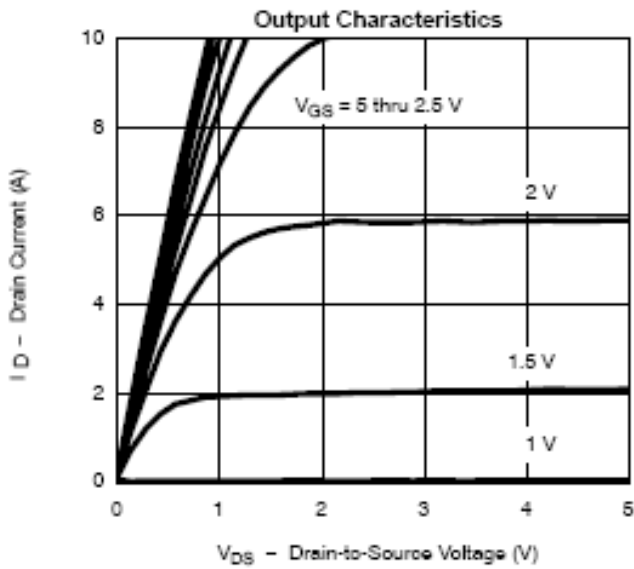
## TYPICAL CHARACTERISTICS ( N-Channel )





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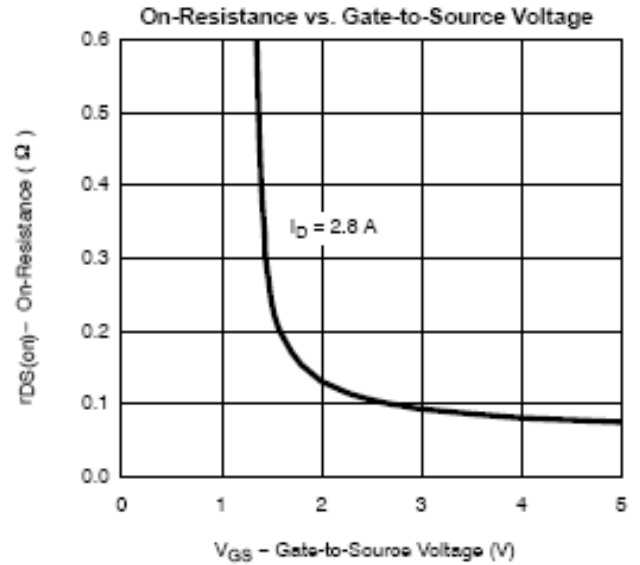
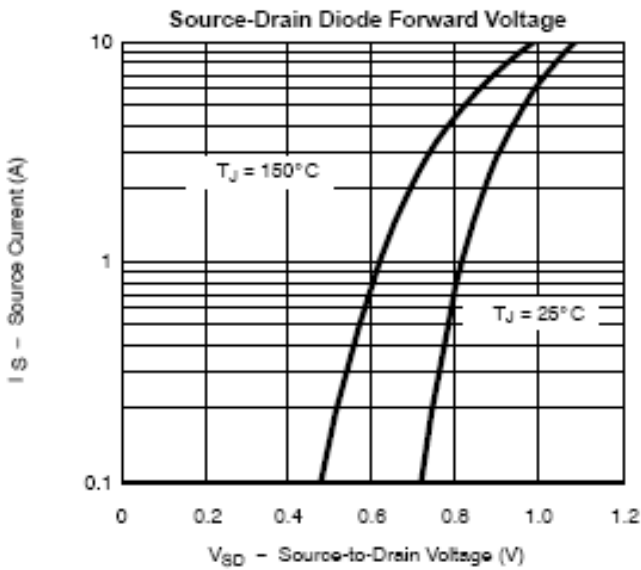
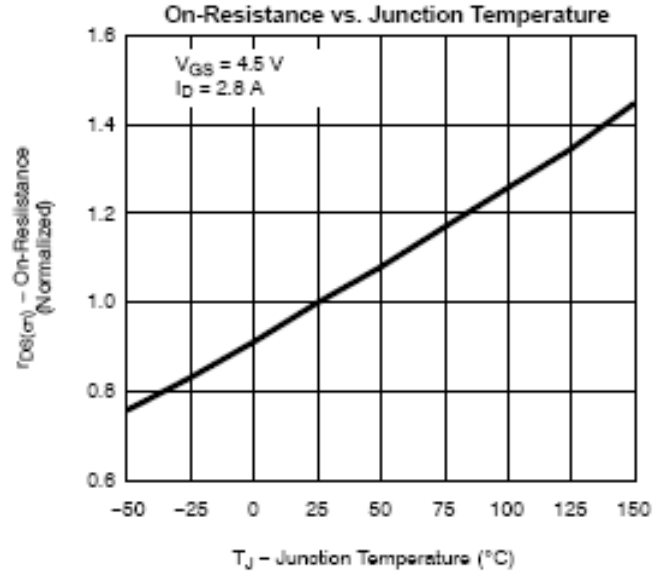
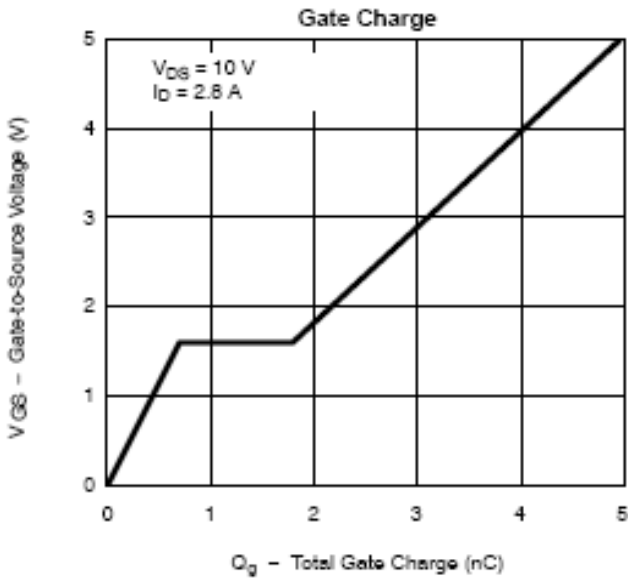
## TYPICAL CHARACTERISTICS ( P-Channel )





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## TYPICAL CHARACTERISTICS ( P-Channel )

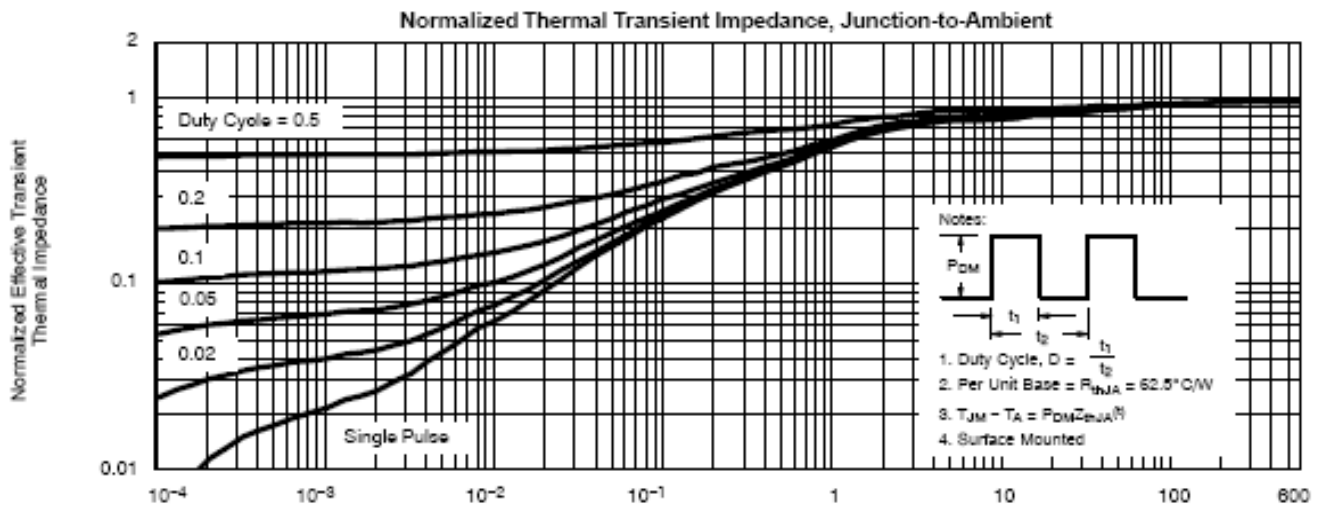
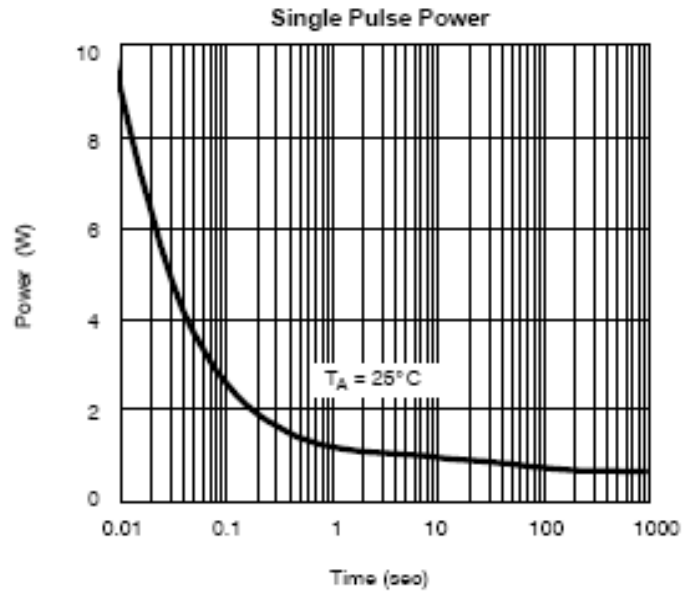
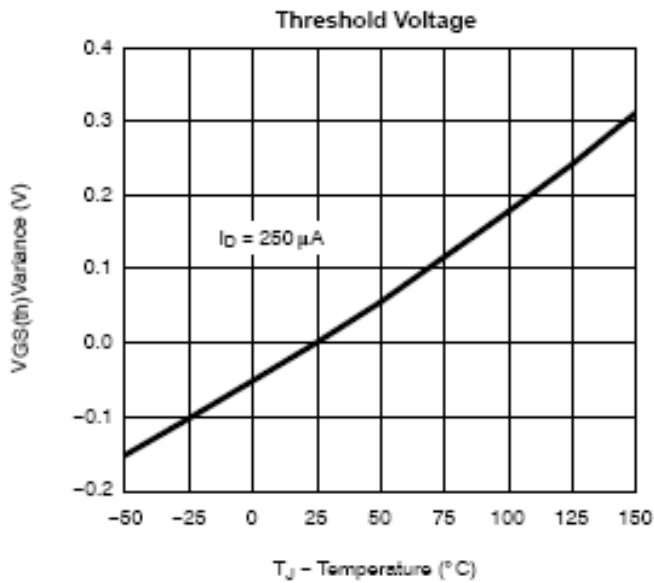






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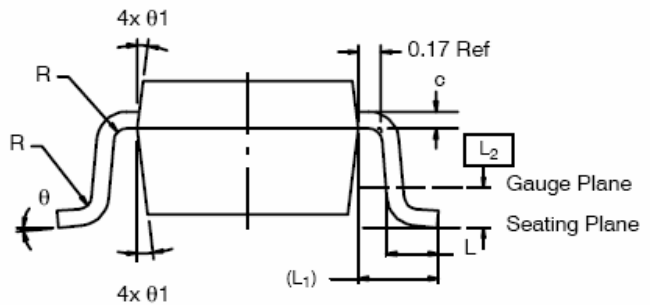
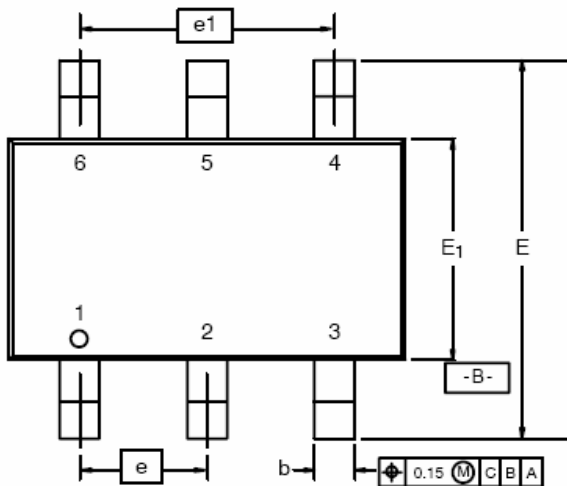
## TYPICAL CHARACTERISTICS ( P-Channel )





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## TSOP- 6P PACKAGE OUTLINE



Dim	MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max
<b>A</b>	0.91	-	1.10	0.036	-	0.043
<b>A<sub>1</sub></b>	0.01	-	0.10	0.0004	-	0.004
<b>A<sub>2</sub></b>	0.90	-	1.00	0.035	0.038	0.039
<b>b</b>	0.30	0.32	0.45	0.012	0.013	0.018
<b>c</b>	0.10	0.15	0.20	0.004	0.006	0.008
<b>D</b>	2.95	3.05	3.10	0.116	0.120	0.122
<b>E</b>	2.70	2.85	2.98	0.106	0.112	0.117
<b>E<sub>1</sub></b>	1.55	1.65	1.70	0.061	0.065	0.067
<b>e</b>	1.00 BSC			0.0394 BSC		
<b>e<sub>1</sub></b>	1.90	2.00	2.10	0.075	0.080	0.085
<b>L</b>	0.35	-	0.50	0.014	-	0.020
<b>L<sub>1</sub></b>	0.60 Ref			0.024 Ref		
<b>L<sub>2</sub></b>	0.25 BSC			0.010 BSC		
<b>R</b>	0.10	-	-	0.004	-	-
<b>θ</b>	0°	4°	8°	0°	4°	8°
<b>θ<sub>1</sub></b>	7° Nom			7° Nom		



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