



SPP2305

P-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPP2305 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

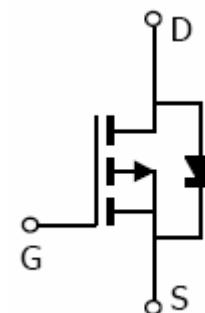
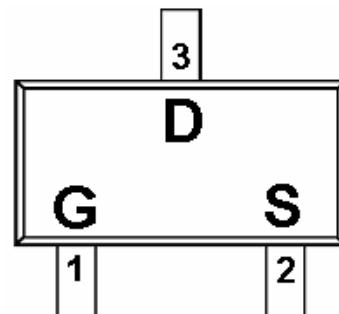
FEATURES

- ◆ -15V/-3.5A,R_{DS(ON)}= 70mΩ@V_{GS}=-4.5V
- ◆ -15V/-3.0A,R_{DS(ON)}= 85mΩ@V_{GS}=-2.5V
- ◆ -15V/-2.0A,R_{DS(ON)}=105mΩ@V_{GS}=-1.8V
- ◆ Super high density cell design for extremely low R_{DS (ON)}
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-23-3L package design

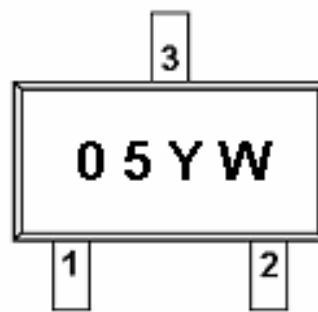
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

PIN CONFIGURATION(SOT-23-3L)



PART MARKING



Y : Year Code
W : Week Code



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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPP2305S23RG	SOT-23-3L	05YW

※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)

※ SPP2305S23RG : Tape Reel ; Pb – Free

ABSOLUT MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	-15	V
Gate –Source Voltage	V _{GSS}	±12	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	ID	-3.5
	T _A =70°C		-2.8
Pulsed Drain Current	I _{DM}	-10	A
Continuous Source Current(Diode Conduction)	I _S	-1.6	A
Power Dissipation	T _A =25°C	P _D	1.25
	T _A =70°C		0.8
Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	120	°C/W



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ELECTRICAL CHARACTERISTICS

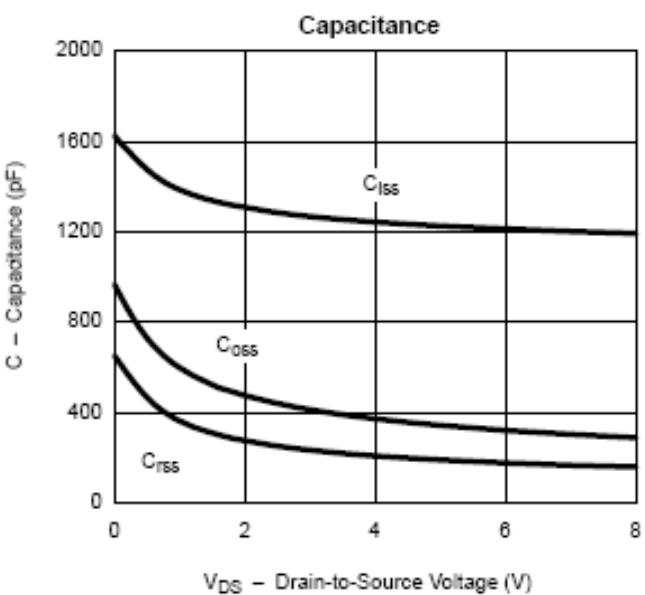
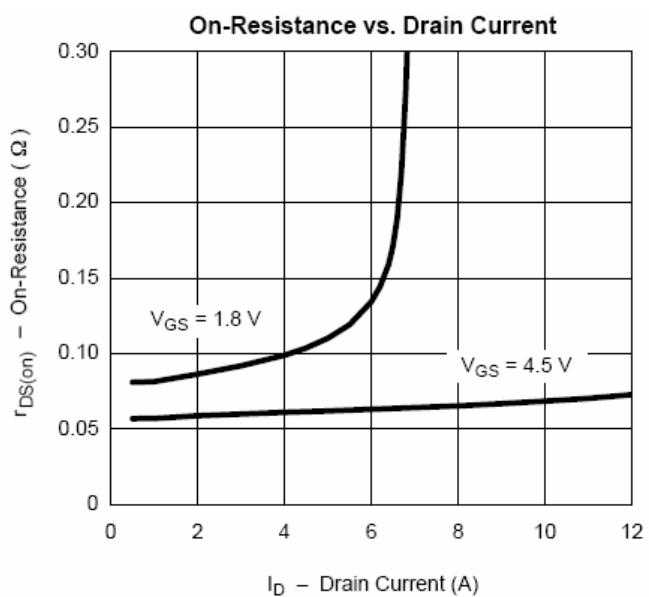
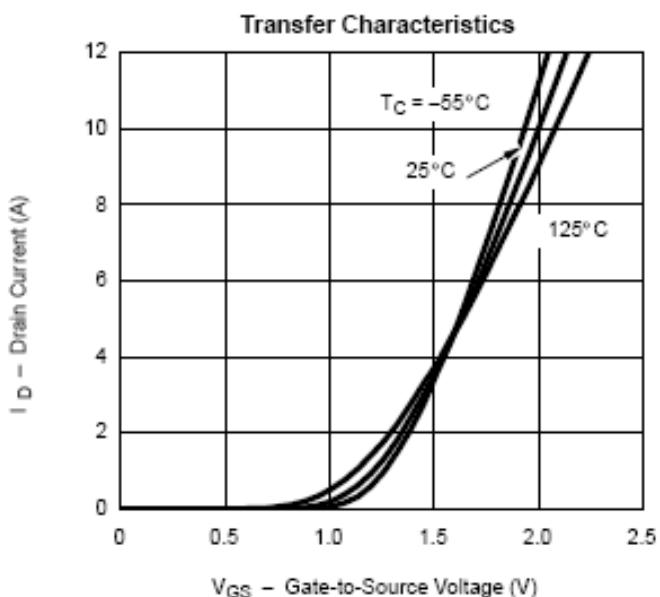
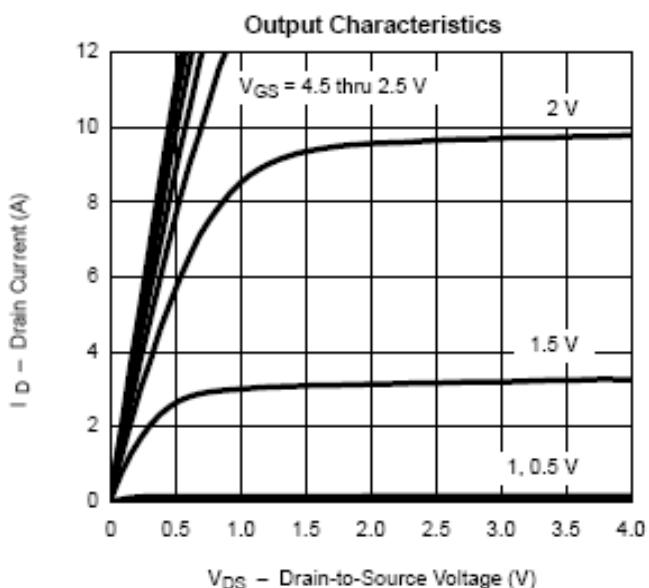
(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=-250uA	-15			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=-250uA	-0.35		-0.85	
Gate Leakage Current	IGSS	VDS=0V, VGS=±10V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=-12V, VGS=0V			-1	uA
		VDS=-12V, VGS=0V TJ=55°C			-10	
On-State Drain Current	ID(on)	VDS≤-5V, VGS=-4.5V	-4			A
		VDS≤-5V, VGS=-2.5V	-2			
Drain-Source On-Resistance	RDS(on)	VGS=-4.5V, ID=-3.5A		0.055	0.70	Ω
		VGS=-2.5V, ID=-3.0A		0.065	0.85	
		VGS=-1.8V, ID=-2.0A		0.085	0.105	
Forward Transconductance	gfs	VDS=-5V, ID=-3.5A		8.5		S
Diode Forward Voltage	VSD	Is=-1.5A, VGS=0V		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Qg	VDS=-6V, VGS=-4.5V ID=-2.8A		4.8	8	nC
Gate-Source Charge	Qgs			1.0		
Gate-Drain Charge	Qgd			1.0		
Input Capacitance	Ciss	VDS=-6V, VGS=0V f=1MHz		485		pF
Output Capacitance	Coss			85		
Reverse Transfer Capacitance	Crss			40		
Turn-On Time	td(on)	VDD=-6V, RL=6Ω ID=-1.0A, VGEN=-4.5V RG=6Ω		10	16	ns
	tr			13	23	
Turn-Off Time	td(off)			18	25	
	tf			15	20	



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TYPICAL CHARACTERISTICS

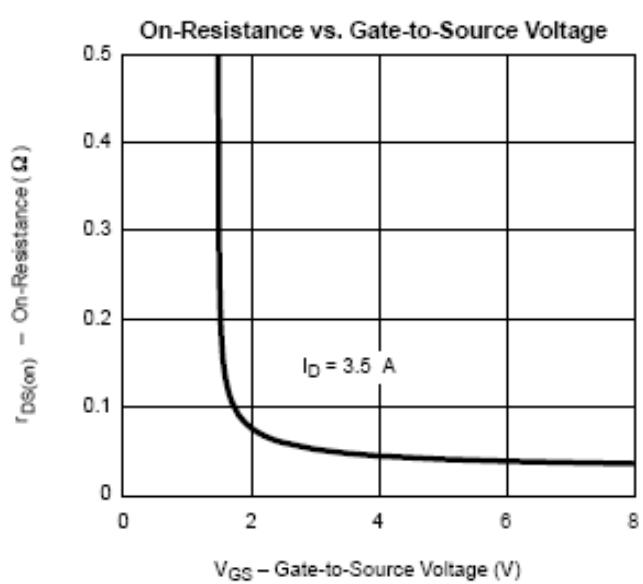
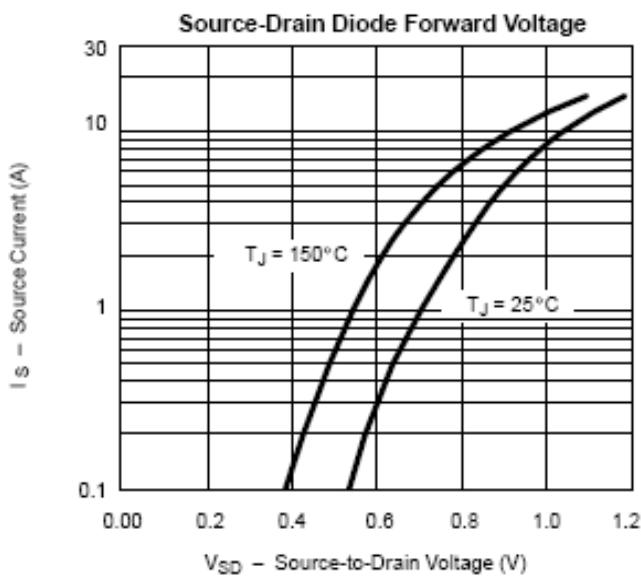
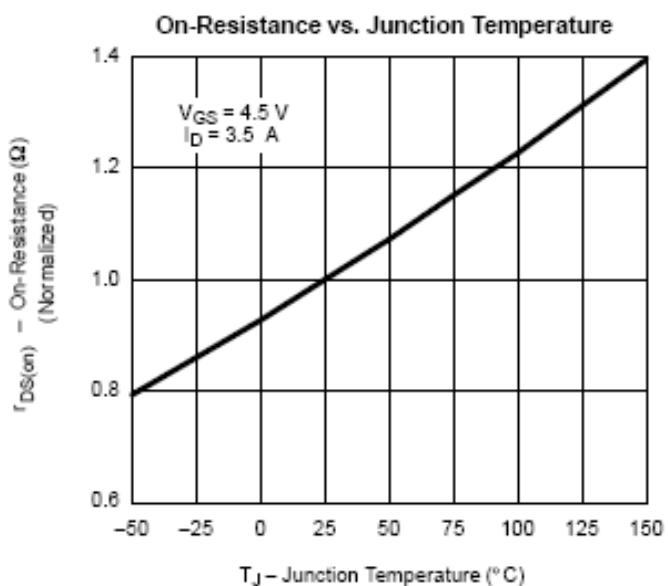
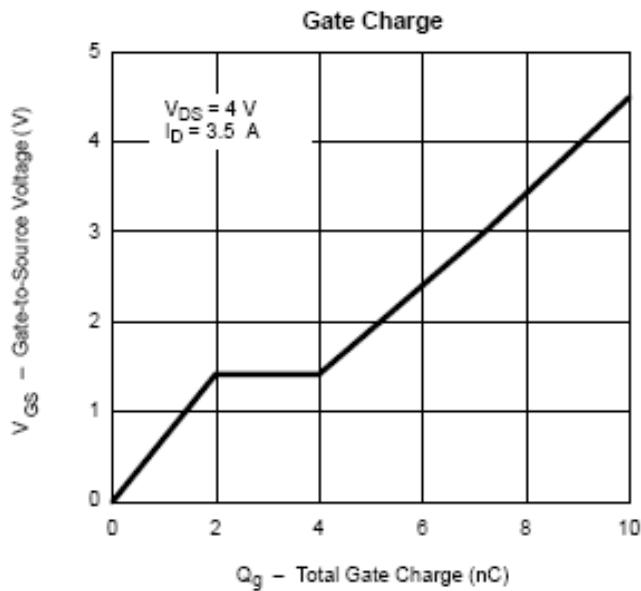




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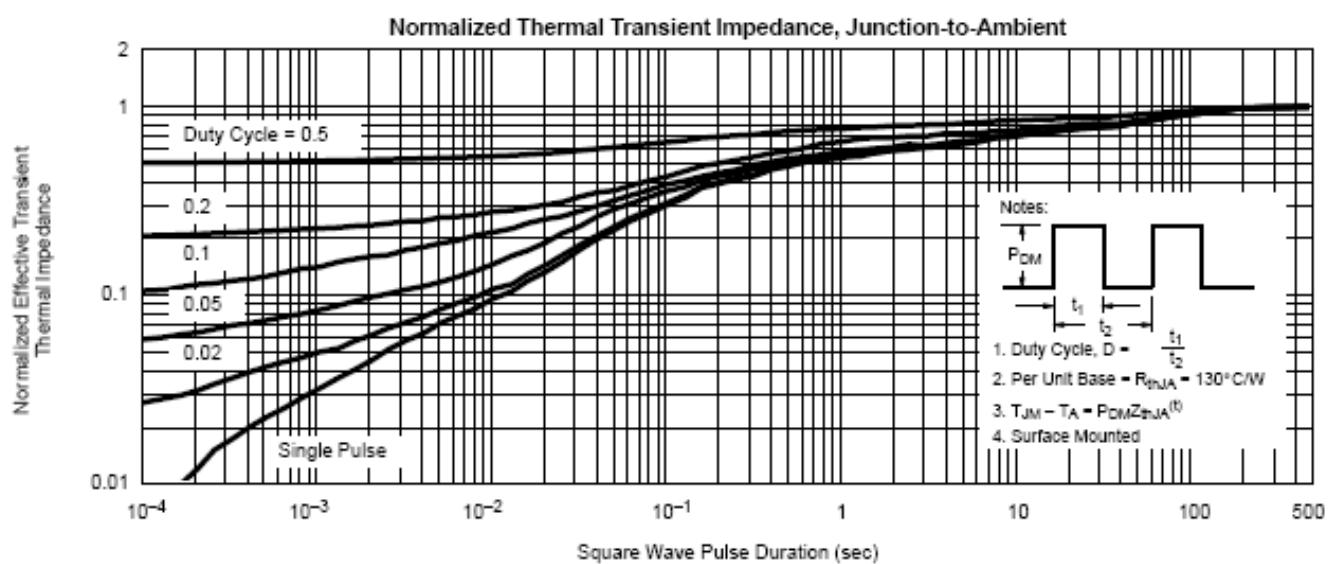
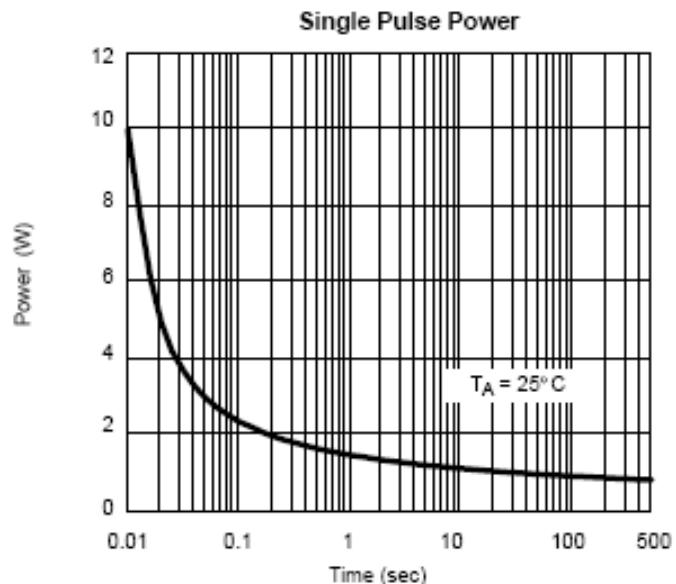
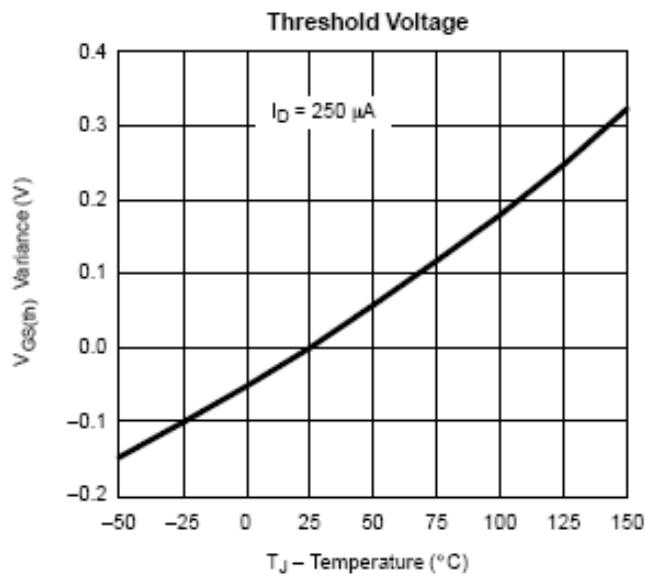




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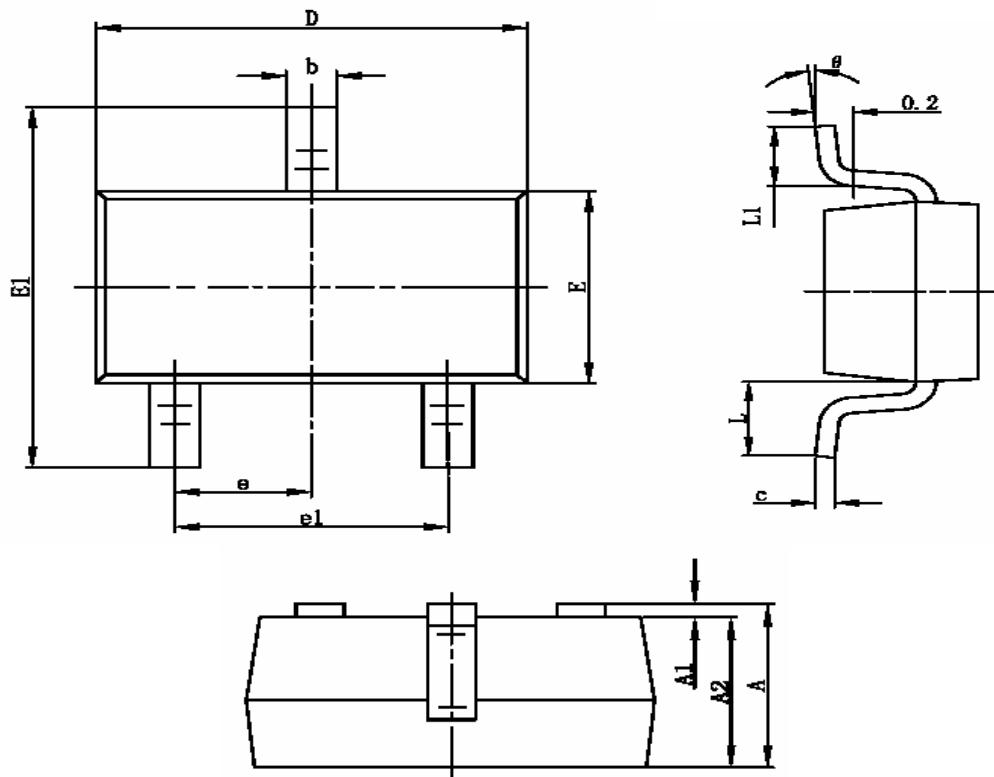




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SOT-23-3L PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.400	0.012	0.016
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.700REF		0.028REF	
L1	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



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