

# SRC1205K

**NPN Silicon Transistor** 

### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### Features

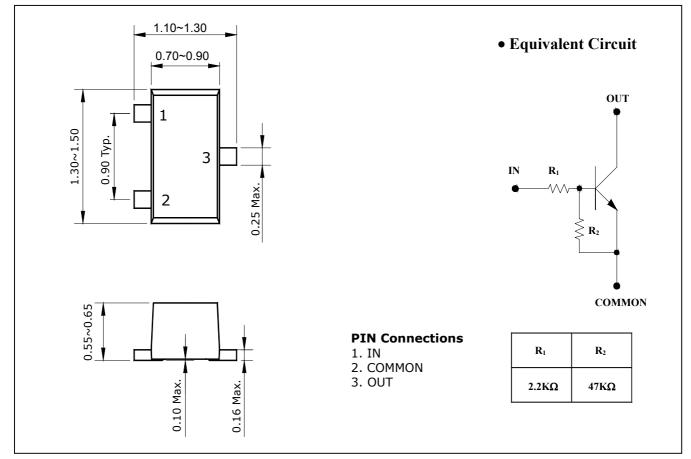
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

### **Ordering Information**

Type NO.	Marking	Package Code	
SRC1205K	R5	SOT-623F	

### **Outline Dimensions**

unit : mm



# **SRC1205K**

### Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C)	
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	50	V	
Input voltage	VI	15,-5	V	
Output current	I <sub>O</sub>	100	mA	
Power dissipation	P <sub>D</sub>	100	mW	
Junction temperature	Tյ	150	°C	
Storage temperature range	T <sub>stg</sub>	-55 ~ 150	°C	

### **Electrical Characteristics**

(Ta=25°C) Characteristic **Test Condition** Symbol Min. Typ. Max. Unit  $V_0 = 50V, V_I = 0$ Output cut-off current \_ 500 nA I<sub>O(OFF)</sub>  $G_{\rm I}$ DC current gain  $V_0 = 5V, I_0 = 10mA$ 80 200 \_ --V Output voltage  $I_0=10$ mA,  $I_I=0.5$ mA 0.1 0.3 V<sub>O(ON)</sub> V<sub>0</sub>=0.2V, I<sub>0</sub>=5mA 1.1 V Input voltage (ON)  $V_{I(ON)}$ \_ -V Input voltage (OFF) V<sub>I(OFF)</sub>  $V_0 = 5V, I_0 = 0.1mA$ 0.5 - $f_{T}^{*}$  $V_0=10V$ ,  $I_0=5mA$ , f=1MHz Transition frequency 200 MHz --Input current  $V_{I} = 5V, I_{O} = 0$ 3.6 mΑ  $I_{I}$ --1.54 2.86 KΩ Input resistor (Input to base)  $\mathsf{R}_1$ 2.2 -KΩ Input resistor (Base to common)  $R_2$ 33 47 61 -

\* : Characteristic of transistor only

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### **Electrical Characteristic Curves**

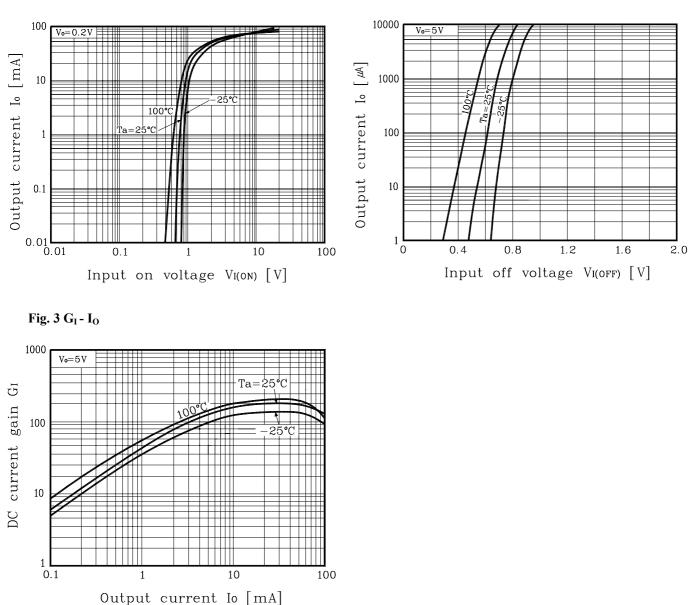


Fig. 1 Io - VI(ON)

Fig. 2 I<sub>O</sub> - V<sub>I(OFF)</sub>

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