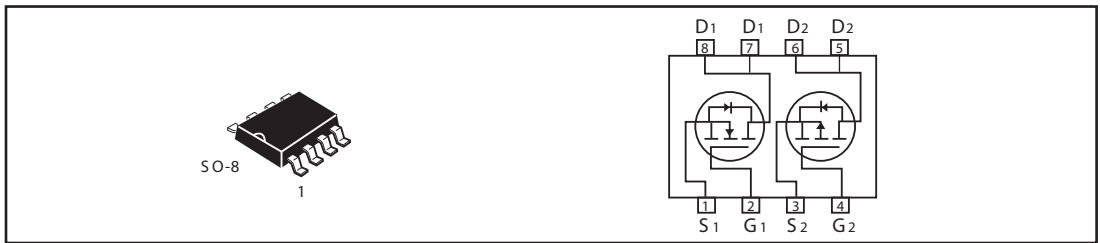




Dual Enhancement Mode Field Effect Transistor (N and P Channel)

PRODUCT SUMMARY (N-Channel)		
V _{DS}	I _D	R _{DS(ON)} (mΩ) Max
40V	5A	35 @ V _{GS} = 10V
		62 @ V _{GS} = 4.5V

PRODUCT SUMMARY (P-Channel)		
V _{DS}	I _D	R _{DS(ON)} (mΩ) Max
-40V	-4A	47 @ V _{GS} = -10V
		65 @ V _{GS} = -4.5V



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

Parameter		Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage		V _{DS}	40	-40	V
Gate-Source Voltage		V _{GS}	±20	±20	V
Drain Current-Continuous ^a @ T _a	25°C	I _D	5	-4	A
	70°C		4.2	-3.4	A
-Pulsed ^b		I _{DM}	20	-16	A
Drain-Source Diode Forward Current ^a		I _S	1.7	-1.7	A
Maximum Power Dissipation ^a	T _a =25°C	P _D	2		W
	T _a =70°C		1.44		
Operating Junction and Storage Temperature Range		T _J , T _{STG}	-55 to 150		°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R _{θJA}	62.5	°C/W
--	------------------	------	------

STM8450A

N-Channel ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	40			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=32V, V_{GS}=0V$			1	μA
Gate-Body Leakage	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
ON CHARACTERISTICS^b						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	2.0	3.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=5A$		25	35	m ohm
		$V_{GS}=4.5V, I_D=4A$		45	62	m ohm
On-State Drain Current	$I_{D(ON)}$	$V_{DS}=5V, V_{GS}=10V$	15			A
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=5A$		8.5		S
DYNAMIC CHARACTERISTICS^c						
Input Capacitance	C_{ISS}	$V_{DS}=10V, V_{GS}=0V$ $f=1.0MHz$		800	895	pF
Output Capacitance	C_{OSS}			150	168	pF
Reverse Transfer Capacitance	C_{RSS}			98	110	pF
Gate resistance	R_g	$V_{GS}=0V, V_{DS}=0V, f=1.0MHz$		3		ohm
SWITCHING CHARACTERISTICS^c						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD}=15V$ $I_D=3A$ $V_{GS}=10V$ $R_{GEN}=4.7\text{ ohm}$		6.8	8	ns
Rise Time	t_r			8.4	9	ns
Turn-Off Delay Time	$t_{D(OFF)}$			16.6	18	ns
Fall Time	t_f			10.5	11	ns
Total Gate Charge	Q_g	$V_{DS}=24V, I_D=5A, V_{GS}=10V$		15.3	18	nC
		$V_{DS}=24V, I_D=5A, V_{GS}=4.5V$		7.7	9	nC
Gate-Source Charge	Q_{gs}	$V_{DS}=24V, I_D=5A$		3	3.6	nC
Gate-Drain Charge	Q_{gd}	$V_{GS}=4.5V$		3.9	4.6	nC

STM8450A

P-Channel ELECTRICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = -250\mu A$	-40			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -32V, V_{GS} = 0V$			-1	μA
Gate-Body Leakage	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
ON CHARACTERISTICS^b						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.8	-1.5	-2.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS} = -10V, I_D = -4A$		37	47	m ohm
		$V_{GS} = -4.5V, I_D = -3A$		50	65	m ohm
On-State Drain Current	$I_{D(ON)}$	$V_{DS} = -5V, V_{GS} = -10V$	20			A
Forward Transconductance	g_{FS}	$V_{DS} = -5V, I_D = -4A$		9.5		S
DYNAMIC CHARACTERISTICS^c						
Input Capacitance	C_{ISS}	$V_{DS} = -10V, V_{GS} = 0V$ $f = 1.0MHz$		1090	1297	pF
Output Capacitance	C_{OSS}			205	240	pF
Reverse Transfer Capacitance	C_{RSS}			125	145	pF
Gate resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz$		3.5		ohm
SWITCHING CHARACTERISTICS^c						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD} = -15V$ $I_D = -2.2A$ $V_{GS} = -10V$ $R_{GEN} = 4.7\text{ ohm}$		8.7	10	ns
Rise Time	t_r			19.8	23	ns
Turn-Off Delay Time	$t_{D(OFF)}$			63.7	75	ns
Fall Time	t_f			26	30	ns
Total Gate Charge	Q_g	$V_{DS} = -24V, I_D = -4A, V_{GS} = -10V$		19.9	23	nC
		$V_{DS} = -24V, I_D = -4A, V_{GS} = -4.5V$		9.8	11	nC
Gate-Source Charge	Q_{gs}	$V_{DS} = -24V, I_D = -4A$		2.8	3.2	nC
Gate-Drain Charge	Q_{gd}	$V_{GS} = -4.5V$		4.2	4.9	nC

STM8450A

ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS^b						
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}, I_s = 1.7\text{A}$	N-Ch	0.8	1.2	V
		$V_{GS} = 0\text{V}, I_s = -1.7\text{A}$	P-Ch	-0.77	-1.2	

Notes

- a. Surface Mounted on FR4 Board, $t \leq 10\text{sec}$.
- b. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2\%$.
- c. Guaranteed by design, not subject to production testing.

N-Channel

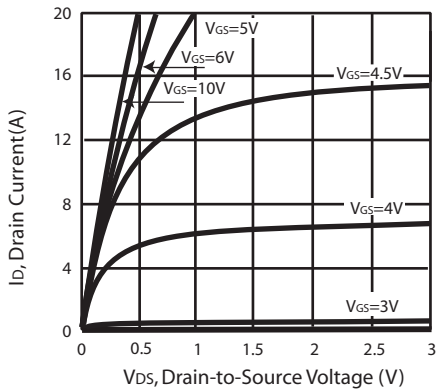


Figure 1. Output Characteristics

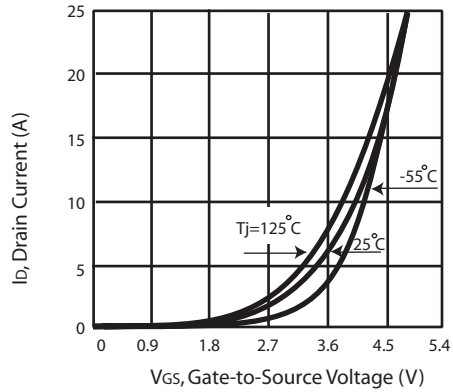


Figure 2. Transfer Characteristics

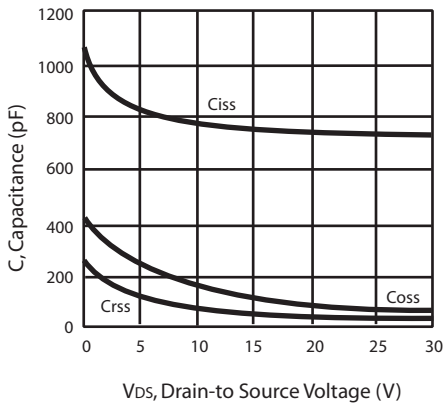


Figure 3. Capacitance

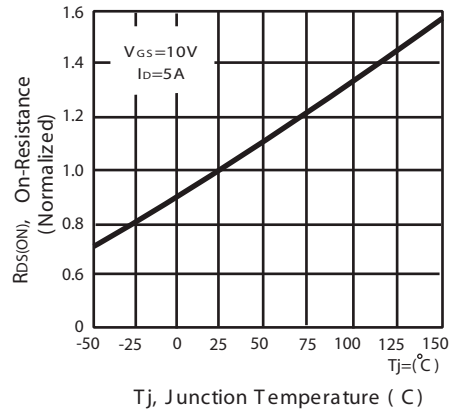


Figure 4. On-Resistance Variation with Drain Current and Temperature

STM8450A

N-Channel

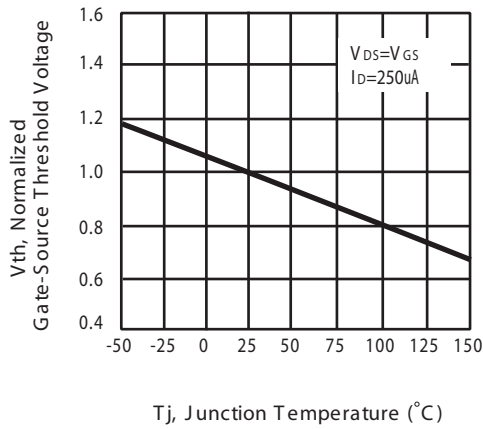


Figure 5. Gate Threshold Variation with Temperature

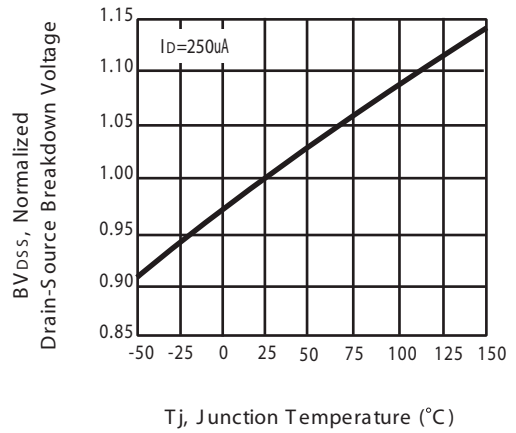


Figure 6. Breakdown Voltage Variation with Temperature

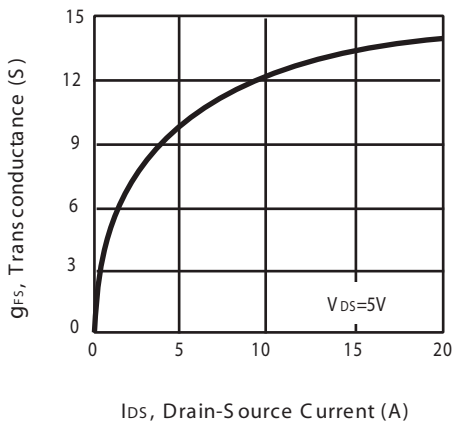


Figure 7. Transconductance Variation with Drain Current

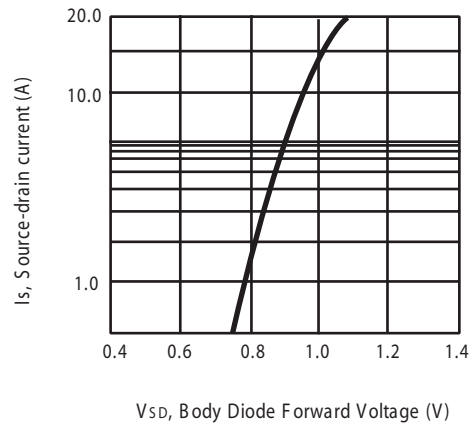


Figure 8. Body Diode Forward Voltage Variation with Source Current

STM8450A

P-Channel

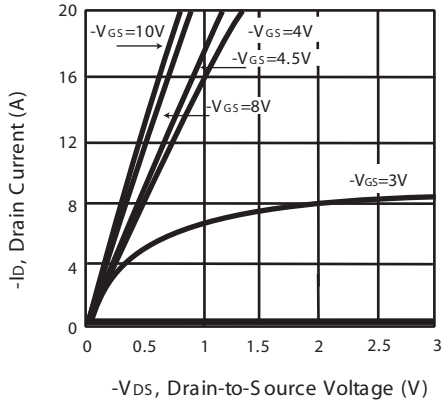


Figure 1. Output Characteristics

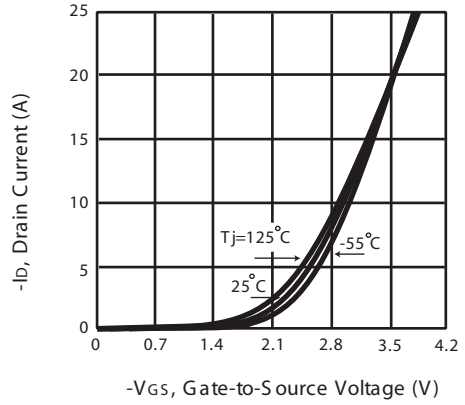


Figure 2. Transfer Characteristics

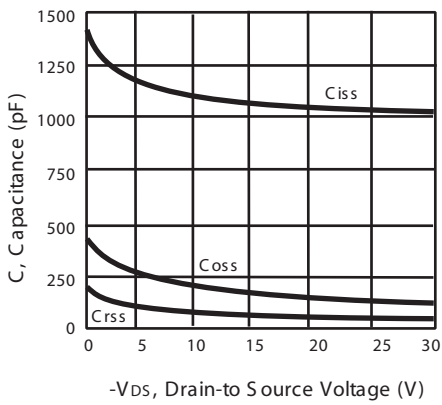


Figure 3. Capacitance

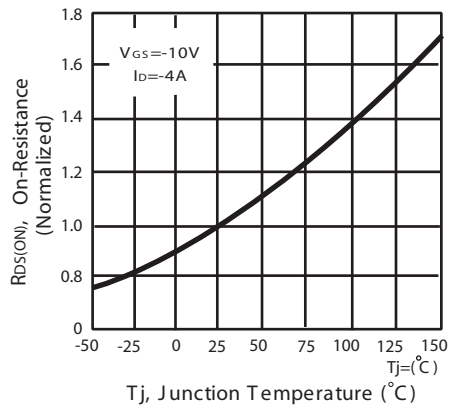


Figure 4. On-Resistance Variation with Temperature

STM8450A

P-Channel

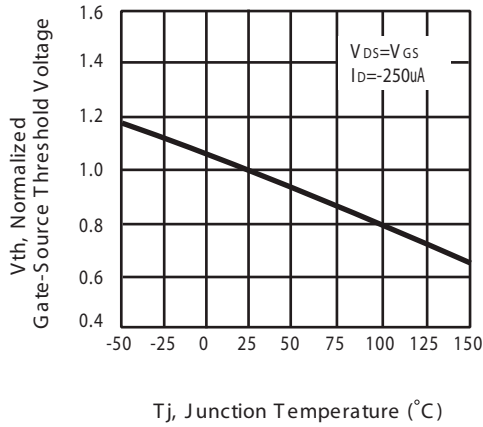


Figure 5. Gate Threshold Variation with Temperature

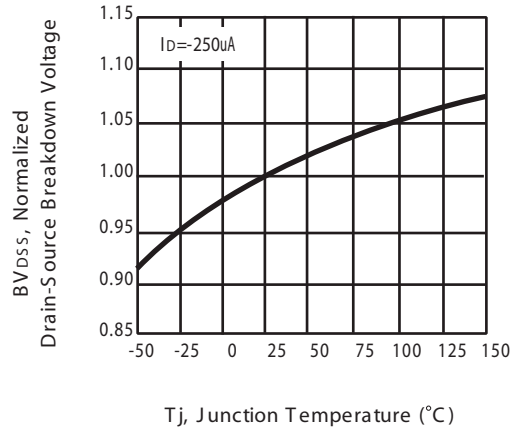


Figure 6. Breakdown Voltage Variation with Temperature

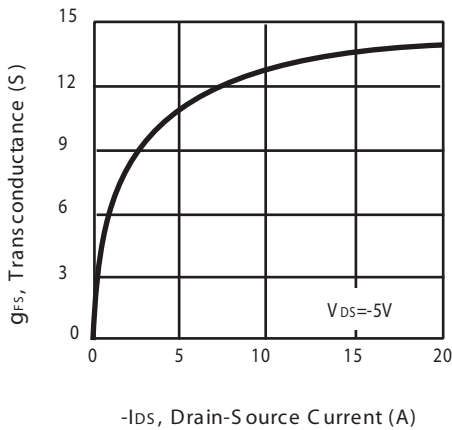


Figure 7. Transconductance Variation with Drain Current

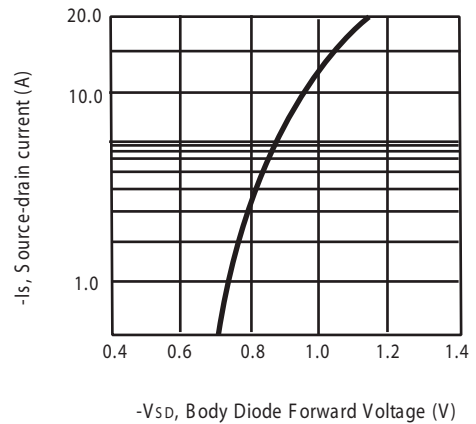
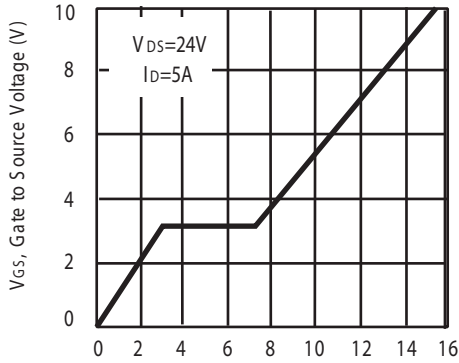


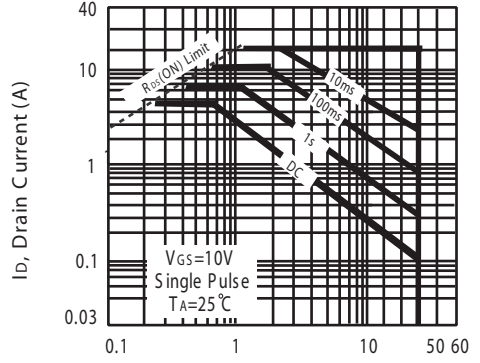
Figure 8. Body Diode Forward Voltage Variation with Source Current

STM8450A

N-Channel

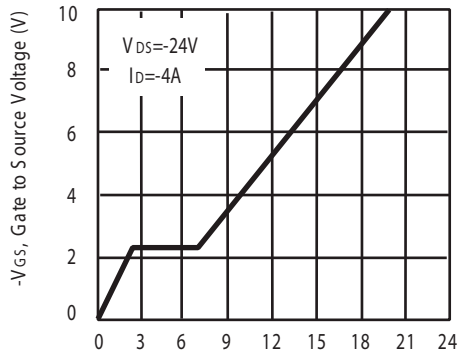


Qg, Total Gate Charge (nC)
Figure 9. Gate Charge

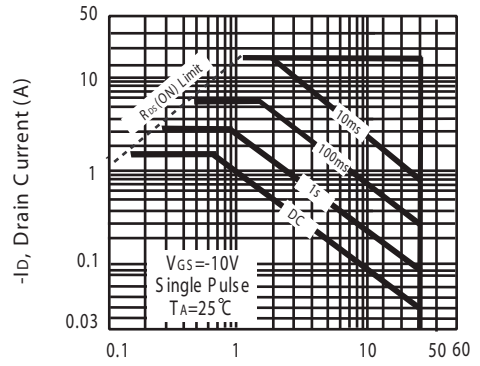


VDs, Drain-Source Voltage (V)
Figure 10. Maximum Safe Operating Area

P-Channel



Qg, Total Gate Charge (nC)
Figure 9. Gate Charge



-VDs, Body Diode Forward Voltage (V)
Figure 10. Maximum Safe Operating Area

STM8450A

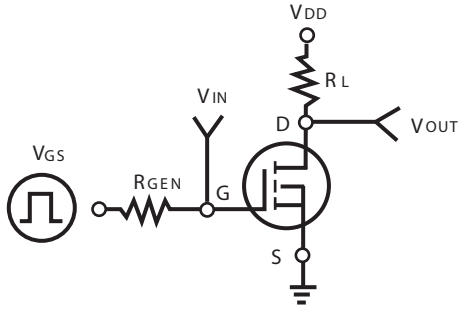


Figure 11. Switching Test Circuit

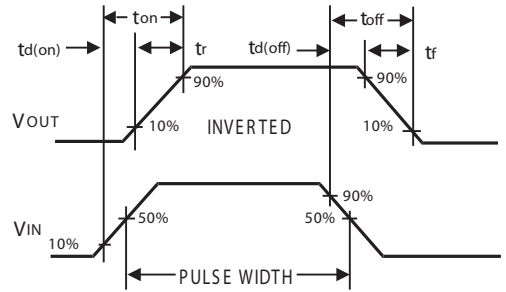
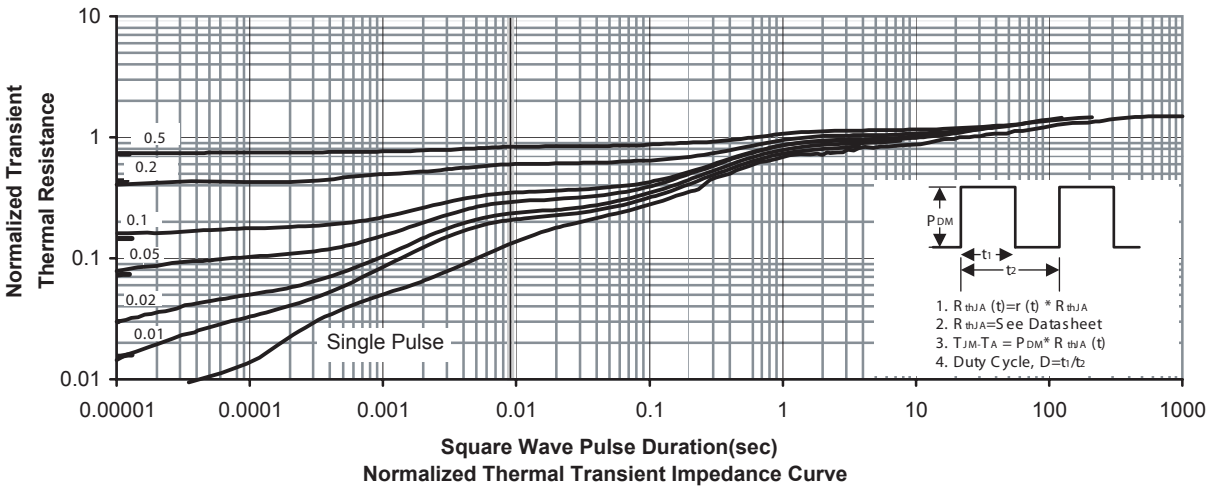
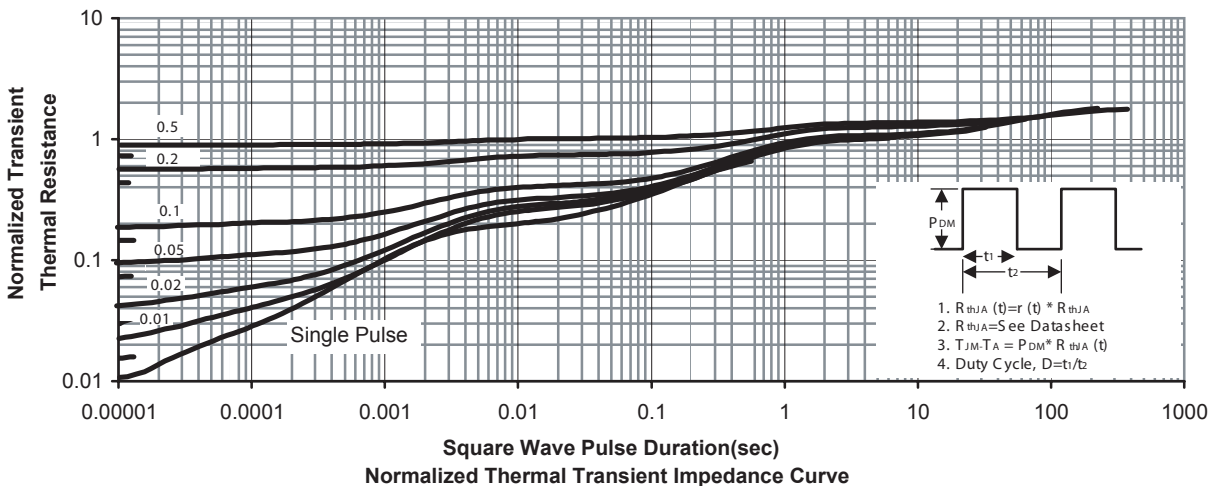


Figure 12. Switching Waveforms

N-Channel



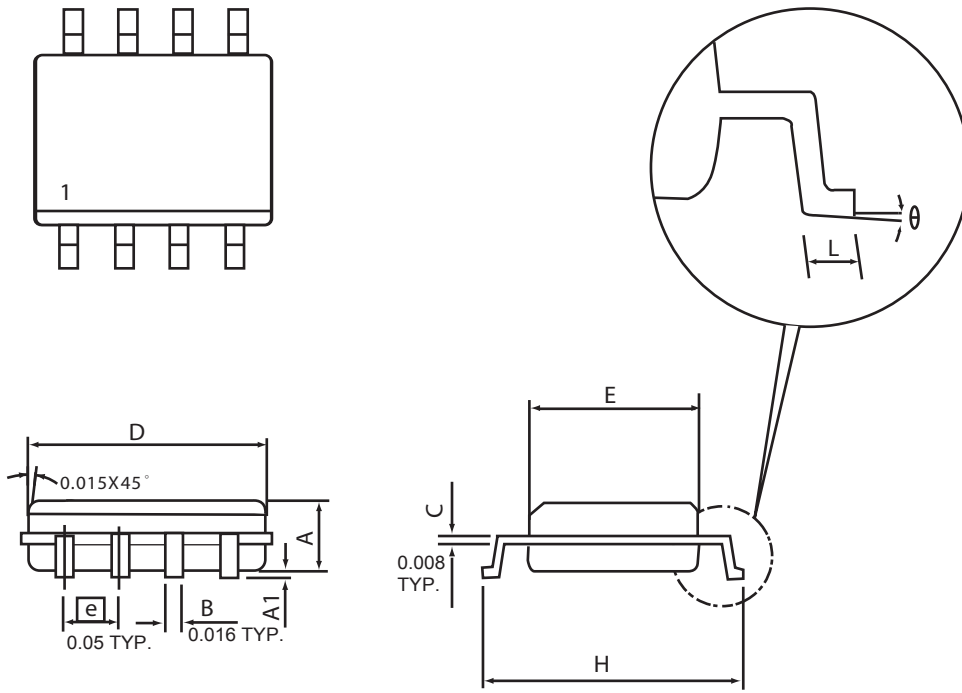
P-Channel



STM8450A

PACKAGE OUTLINE DIMENSIONS

SO-8

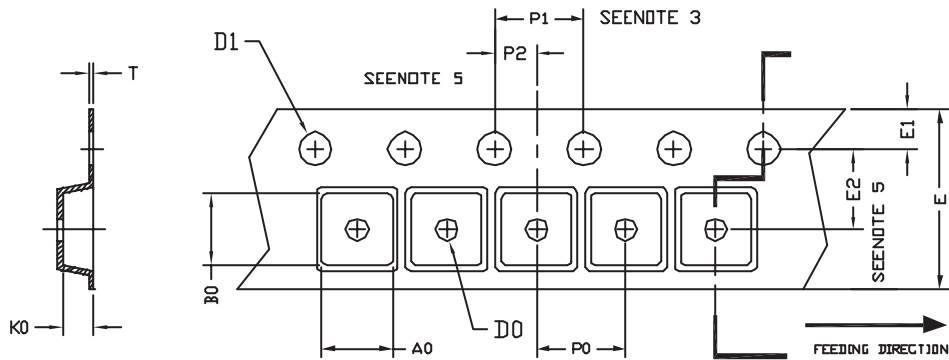


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°

STM8450A

SO-8 Tape and Reel Data

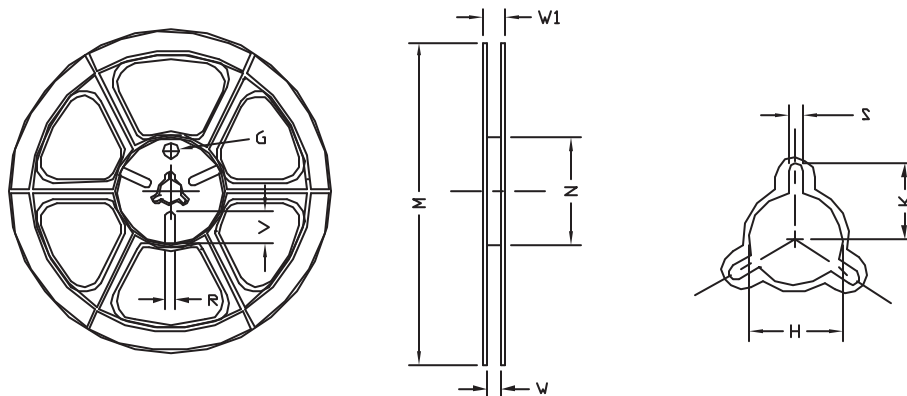
SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.40	5.20	2.10	ϕ 1.5 (MIN)	ϕ 1.5 + 0.1 - 0.0	12.0 \pm 0.3	1.75	5.5 \pm 0.05	8.0	4.0	2.0 \pm 0.05	0.3 \pm 0.05

SO-8 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	ϕ 330	330 \pm 1	62 \pm 1.5	12.4 + 0.2	16.8 - 0.4	ϕ 12.75 + 0.15	---	2.0 \pm 0.15	---	---	---