

## Silicon Controlled Rectifiers

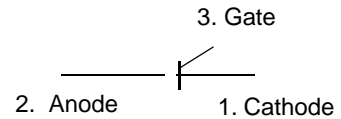
### Features

Repetitive Peak Off-State Voltage : 600V  
 R.M.S On-State Current (  $I_{T(RMS)} = 8\text{ A}$  )  
 Low On-State Voltage (1.4V(Typ.)@  $I_{TM}$ )  
 Non-isolated Type

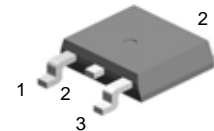
### General Description

Standard gate triggering SCR is suitable for the application where requiring high bidirectional blocking voltage capability and also suitable for over voltage protection ,motor control circuit in power tool, inrush current limit circuit and heating control system.

#### Symbol



#### D-PAK



### Absolute Maximum Ratings ( $T_J = 25^\circ\text{C}$ unless otherwise specified )

Symbol	Parameter	Condition	Ratings	Units
$V_{DRM}$	Repetitive Peak Off-State Voltage		600	V
$I_{T(AV)}$	Average On-State Current	Half Sine Wave : $T_C = 109^\circ\text{C}$	5	A
$I_{T(RMS)}$	R.M.S On-State Current	180° Conduction Angle	8	A
$I_{TSM}$	Surge On-State Current	1/2 Cycle, 60Hz, Sine Wave Non-Repetitive	73	A
$I^2t$	$I^2t$ for Fusing	$t = 8.3\text{ms}$	26.6	$\text{A}^2\text{s}$
di/dt	Critical rate of rise of on-state current		50	$\text{A}/\mu\text{s}$
$P_{GM}$	Forward Peak Gate Power Dissipation		5	W
$P_{G(AV)}$	Forward Average Gate Power Dissipation		1	W
$I_{FGM}$	Forward Peak Gate Current		2	A
$V_{RGM}$	Reverse Peak Gate Voltage		5.0	V
$T_J$	Operating Junction Temperature		- 40 ~ 125	$^\circ\text{C}$
$T_{STG}$	Storage Temperature		- 40 ~ 150	$^\circ\text{C}$

# TN815R

## Electrical Characteristics ( $T_C = 25\text{ }^\circ\text{C}$ unless otherwise noted )

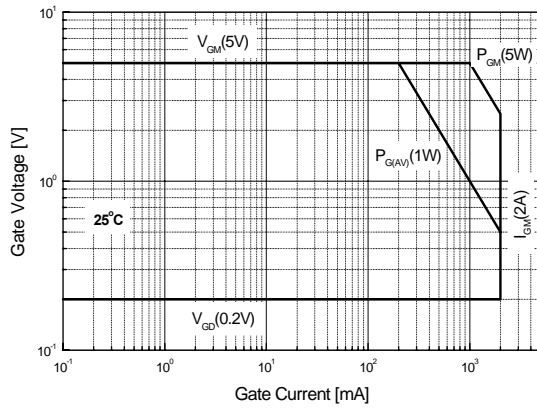
Symbol	Items	Conditions	Ratings			Unit
			Min.	Typ.	Max.	
$I_{\text{DRM}}$	Repetitive Peak Off-State Current	$V_{\text{AK}} = V_{\text{DRM}}$ $T_C = 25\text{ }^\circ\text{C}$ $T_C = 125\text{ }^\circ\text{C}$			10 200	$\mu\text{A}$
$V_{\text{TM}}$	Peak On-State Voltage (1)	$I_{\text{TM}} = 16\text{ A}$ $t_p = 380\text{ }\mu\text{s}$			1.6	V
$I_{\text{GT}}$	Gate Trigger Current (2)	$V_{\text{AK}} = 6\text{ V(DC)}$ , $R_L = 10$ $T_C = 25\text{ }^\circ\text{C}$			15	mA
$V_{\text{GT}}$	Gate Trigger Voltage (2)	$V_D = 6\text{ V(DC)}$ , $R_L = 10$ $T_C = 25\text{ }^\circ\text{C}$			1.5	V
$V_{\text{GD}}$	Non-Trigger Gate Voltage (1)	$V_{\text{AK}} = 12\text{ V}$ , $R_L = 100$ $T_C = 125\text{ }^\circ\text{C}$	0.2			V
dv/dt	Critical Rate of Rise Off-State Voltage	Linear slope up to $V_D = V_{\text{DRM}} 67\%$ , Gate open $T_J = 125\text{ }^\circ\text{C}$	200			V/ $\mu\text{s}$
$I_{\text{H}}$	Holding Current	$I_T = 100\text{ mA}$ , Gate Open $T_C = 25\text{ }^\circ\text{C}$			20	mA
$R_{\text{th(j-c)}}$	Thermal Impedance	Junction to case			2.0	$^\circ\text{C/W}$
$R_{\text{th(j-a)}}$	Thermal Impedance	Junction to Ambient			70	$^\circ\text{C/W}$

### Notes :

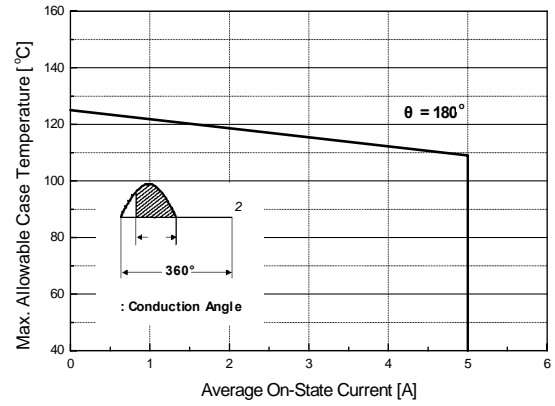
1. Pulse Width = 1.0 ms , Duty cycle 1%
2.  $R_{\text{GK}}$  Current not Included in measurement.



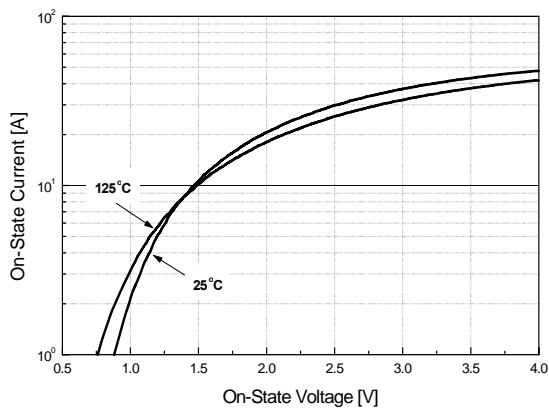
**Fig 1. Gate Characteristics**



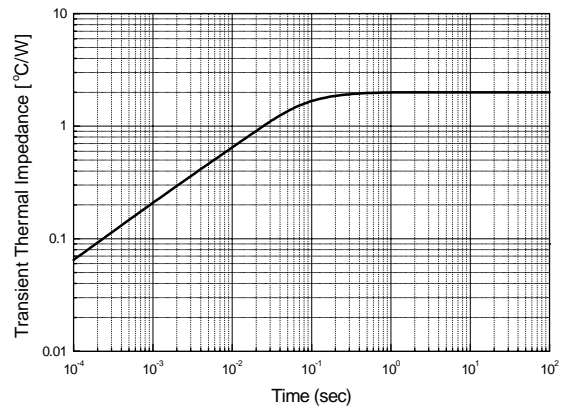
**Fig 2. Maximum Case Temperature**



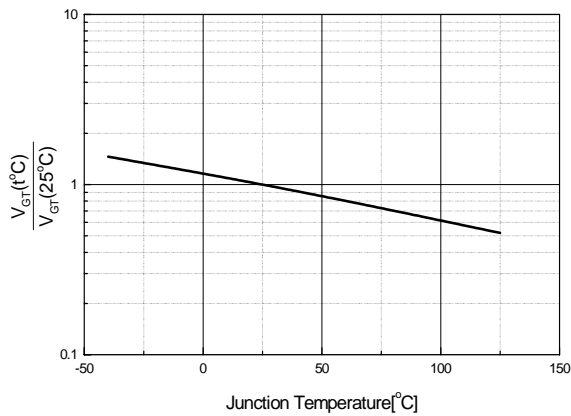
**Fig 3. Typical Forward Voltage**



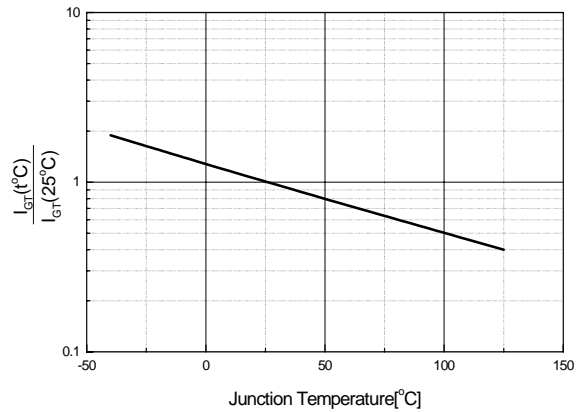
**Fig 4. Thermal Response**



**Fig 5. Typical Gate Trigger Voltage vs. Junction Temperature**



**Fig 6. Typical Gate Trigger Current vs. Junction Temperature**



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Fig 7. Typical Holding Current

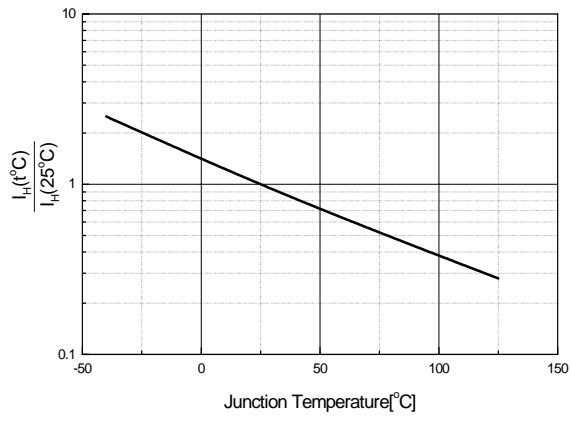
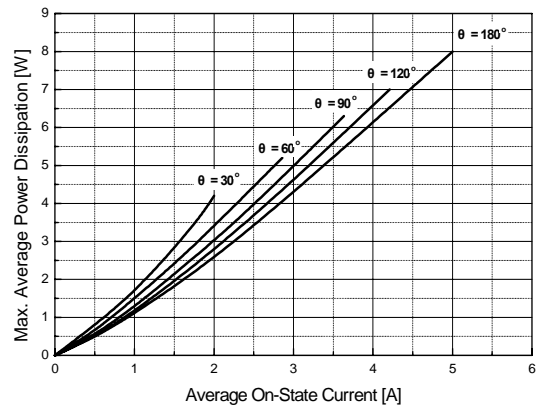


Fig 8. Power Dissipation



## TO-252(D-PAK) Package Dimension

Dim.	mm			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	6.48	6.604	6.73	0.255	0.26	0.265
B	5.0	5.08	5.21	0.197	0.2	0.205
C	7.42	7.8	8.18	0.292	0.307	0.322
D	2.184	2.286	2.388	0.086	0.09	0.094
E	0.762	0.813	0.864	0.03	0.032	0.034
F	1.016	1.067	1.118	0.04	0.042	0.044
G		2.286			0.09	
H		2.286			0.09	
I	0.534	0.61	0.686	0.021	0.024	0.027
J	1.016	1.067	1.118	0.04	0.042	0.044
K		0.508			0.02	
L		0.762			0.03	
		1.57			0.06	

