Unit in mm

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

2SC2290A

2~30MHz SSB LINEAR POWER AMPLIFIER APPLICATIONS (LOW SUPPLY VOLTAGE USE)

• Specified 12.5V, 28MHz Characteristics

Output Power : Po = 60Wpep (Min.)
 Power Gain : Gp = 11.8dB (Min.)
 Collector Efficiency : ηC = 35% (Min.)
 Intermodulation Distortion: IMD = -30dB (Max.)

ABSOLUTE MAXIMUM RATINGS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	V _{CBO}	45	V
Collector-Emitter Voltage	V _{CES}	45	V
Collector-Emitter Voltage	V _{CEO}	18	V
Emitter-Base Voltage	V _{EBO}	4	V
Collector Current	IC	20	Α
Collector Power Dissipation	PC	175	W
Junction Temperature	Tj	175	°C
Storage Temperature Range	T _{stg}	-65~175	°C

1. EMITTER
2. BASE
3. EMITTER
4. COLLECTOR

JEDEC —

EIAJ —

TOSHIBA 2-13B1A

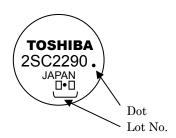
Weight: 5.2g

Note: Using continuously under heavy loads (e.g. the application of high

temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

MARKING

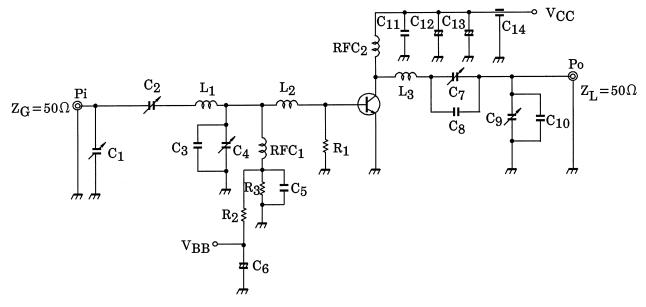


ELECTRICAL CHARACTERISTICS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector-Emitter Breakdown Voltage	V (BR) CEO	I _C = 100mA, I _B = 0	18	_	_	V
Collector-Emitter Breakdown Voltage	V (BR) CES	I _C = 100mA, V _{EB} = 0	45	_	_	V
Emitter-Base Breakdown Voltage	V (BR) EBO	I _E = 1mA, I _C = 0	4	_	_	V
DC Current Gain	h _{FE}	V _{CE} = 5V, I _C = 10A *	10	_	150	_
Collector Output Capacitance	C _{ob}	V _{CB} = 12.5V, I _E = 0 f = 1MHz	_	_	500	pF
Power Gain	Gp		11.8	13.8	_	dB
Input Power	Pi	V_{CC} = 12.5V, f_1 = 28.000MHz, f_2 = 28.001MHz	_	2.5	4	W _{PEP}
Collector Efficiency	ηc	I _{idle} = 50mA Po = 60W _{PEP} (Fig.)	35	_	_	%
Intermodulation Distortion	IMD		_	_	-30	dB
Series Equivalent Input Impedance	Z _{in}	V _{CC} = 12.5V, f ₁ = 28.000MHz, f ₂ = 28.001MHz	_	1.02 -j0.17	_	Ω
Series Equivalent Output Impedance	Z _{out}	Po = 60W _{PEP}	_	0.86 -j0.21		Ω

^{*} Pulse Test: Pulse Width ≤ 100μs, Duty Cycle ≤ 3%

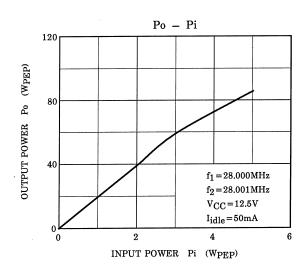
Fig. Pi TEST CIRCUIT

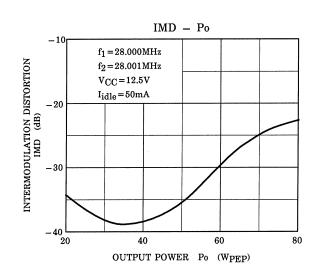


 $C_1, C_2, C_4, C_7 : 7 \sim 150 pF$: ϕ 0.8 ENAMEL COATED COPPER WIRE, 9ID, 6T L_1 : ϕ 1 SILVER PLATED COPPER WIRE, 9ID, 2T : 250pF L_2 : ϕ 1.5 ENAMEL COATED COPPER WIRE, 9ID, 5T : $0.4\mu F$ C_5 L_3 : $100 \mu F 10WV$ RFC₁: ϕ 0.8 ENAMEL COATED COPPER WIRE, 9ID, 20T C_6 RFC₂ : ϕ 1.5 ENAMEL COATED COPPER WIRE, 12ID, 15T C₈ : 150pF

 $\begin{array}{ccc} C_{12}^{--}, C_{13} & : & 22 \mu F \, 35 WV \\ C_{14} & : & 1000 pF \end{array}$

(FEED THROUGH)





CAUTION

These are only typical curves and devices are not necessarily guaranteed at these curves.

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RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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