

# FDMS8692

## N-Channel PowerTrench® MOSFET

30V, 28A, 9.0mΩ

### Features

- Max  $r_{DS(on)}$  = 9.0mΩ at  $V_{GS} = 10V$ ,  $I_D = 12A$
- Max  $r_{DS(on)}$  = 14.0mΩ at  $V_{GS} = 4.5V$ ,  $I_D = 10.5A$
- Advanced Package and Silicon combination for low  $r_{DS(on)}$  and high efficiency
- MSL1 robust package design
- RoHS Compliant

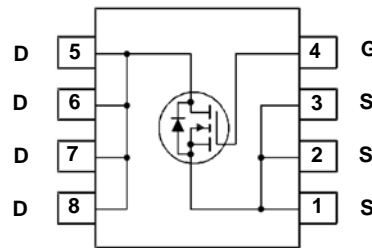
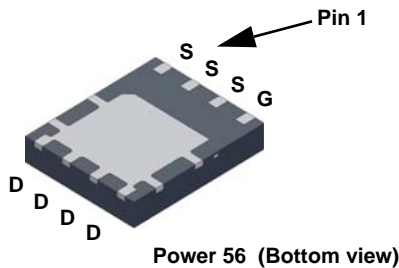


### General Description

The FDMS8692 has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been combined to offer the lowest  $r_{DS(on)}$  while maintaining excellent switching performance.

### Applications

- High Side for Synchronous Buck to Power Core Processor
- Secondary Side Synchronous Rectifier
- High Side Switch in POL DC/DC Converter
- Oring FET/ Load Switch



### MOSFET Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain to Source Voltage	30	V
$V_{GS}$	Gate to Source Voltage	$\pm 20$	V
$I_D$	Drain Current -Continuous (Package limited) $T_C = 25^\circ\text{C}$	28	A
	-Continuous (Silicon limited) $T_C = 25^\circ\text{C}$	48	
	-Continuous $T_A = 25^\circ\text{C}$ (Note 1a)	12	
	-Pulsed	120	
$E_{AS}$	Single Pulse Avalanche Energy (Note 3)	150	mJ
$P_D$	Power Dissipation $T_C = 25^\circ\text{C}$	41	W
	Power Dissipation $T_A = 25^\circ\text{C}$ (Note 1a)	2.5	
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ\text{C}$

### Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	3.0	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a)	50	

### Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS8692	FDMS8692	Power 56	13"	12mm	3000units

## Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
--------	-----------	-----------------	-----	-----	-----	-------

### Off Characteristics

$BV_{DSS}$	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$ , referenced to $25^\circ\text{C}$		20		mV/ $^\circ\text{C}$
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 24\text{V}, V_{GS} = 0\text{V}$			1	$\mu\text{A}$
$I_{GSS}$	Gate to Source Leakage Current	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$			$\pm 100$	nA

### On Characteristics

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 250\mu\text{A}$	1	1.8	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$ , referenced to $25^\circ\text{C}$		-5.4		mV/ $^\circ\text{C}$
$r_{DS(on)}$	Static Drain to Source On Resistance	$V_{GS} = 10\text{V}, I_D = 12\text{A}$		7.0	9.0	m $\Omega$
		$V_{GS} = 4.5, I_D = 10.5\text{A}$		10.5	14.0	
		$V_{GS} = 10\text{V}, I_D = 12\text{A}, T_J = 125^\circ\text{C}$		10.0	13.0	
$g_{FS}$	Forward Transconductance	$V_{DD} = 10\text{V}, I_D = 12\text{A}$		58		S

### Dynamic Characteristics

$C_{iss}$	Input Capacitance	$V_{DS} = 15\text{V}, V_{GS} = 0\text{V},$ $f = 1\text{MHz}$		950	1265	pF
$C_{oss}$	Output Capacitance			515	685	pF
$C_{riss}$	Reverse Transfer Capacitance			85	130	pF
$R_g$	Gate Resistance		$f = 1\text{MHz}$		1.0	4.0

### Switching Characteristics

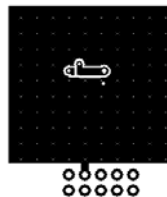
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 15\text{V}, I_D = 12\text{A},$ $V_{GS} = 10\text{V}, R_{GEN} = 6\Omega$		9	18	ns
$t_r$	Rise Time			3	10	ns
$t_{d(off)}$	Turn-Off Delay Time			19	34	ns
$t_f$	Fall Time			2	10	ns
$Q_g$	Total Gate Charge		$V_{GS} = 0\text{V to } 10\text{V}$	$V_{DD} = 15\text{V},$ $I_D = 12\text{A}$	15	21
$Q_g$	Total Gate Charge	$V_{GS} = 0\text{V to } 5\text{V}$	8		11	nC
$Q_{gs}$	Gate to Source Charge		2.7			nC
$Q_{gd}$	Gate to Drain "Miller" Charge		2.1			nC

### Drain-Source Diode Characteristics

$V_{SD}$	Source to Drain Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 2.1\text{A}$ (Note 2)		0.7	1.2	V
		$V_{GS} = 0\text{V}, I_S = 12\text{A}$		0.8	1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F = 12\text{A}, di/dt = 100\text{A}/\mu\text{s}$		29	47	ns
$Q_{rr}$	Reverse Recovery Charge			14	25	nC

#### NOTES:

1.  $R_{\theta JA}$  is determined with the device mounted on a  $1\text{in}^2$  pad 2 oz copper pad on a  $1.5 \times 1.5\text{in.}$  board of FR-4 material.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.



a.  $50^\circ\text{C}/\text{W}$  when mounted on a  $1\text{in}^2$  pad of 2 oz copper.



b.  $125^\circ\text{C}/\text{W}$  when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width <  $300\mu\text{s}$ , Duty cycle < 2.0%.  
3. Starting  $T_J = 25^\circ\text{C}$ ,  $L = 3\text{mH}$ ,  $I_{AS} = 10\text{A}$ ,  $V_{DD} = 30\text{V}$ ,  $V_{GS} = 10\text{V}$ .

**Typical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted

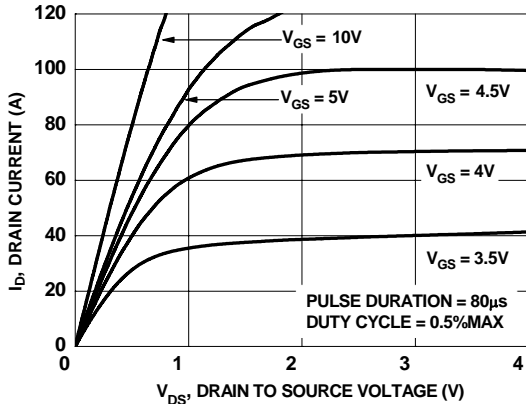


Figure 1. On-Region Characteristics

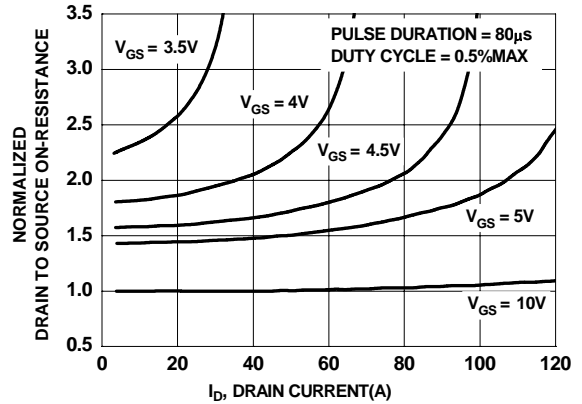


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

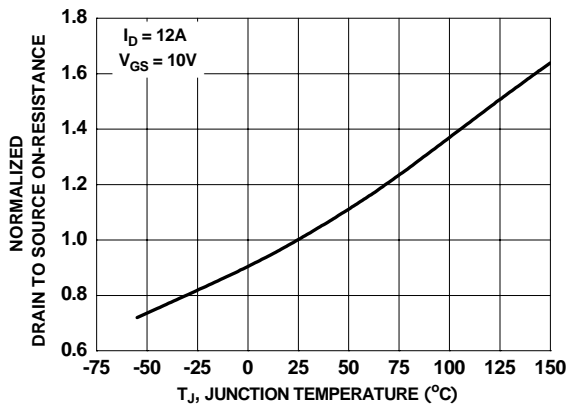


Figure 3. Normalized On-Resistance vs Junction Temperature

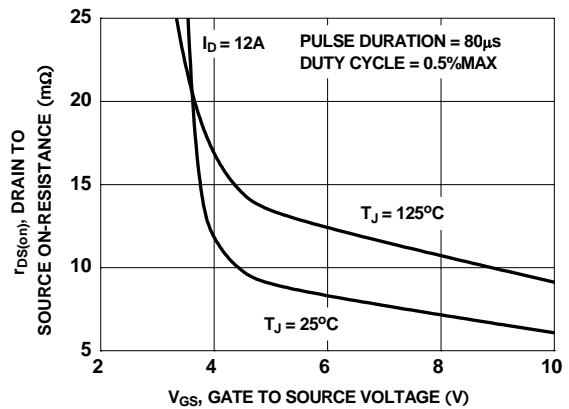


Figure 4. On-Resistance vs Gate to Source Voltage

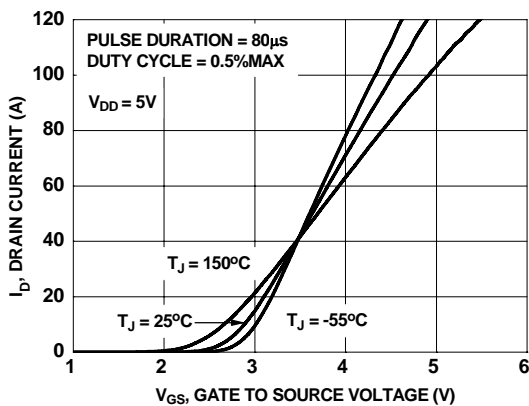


Figure 5. Transfer Characteristics

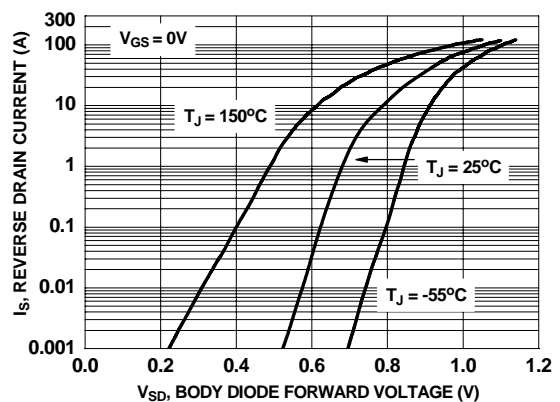
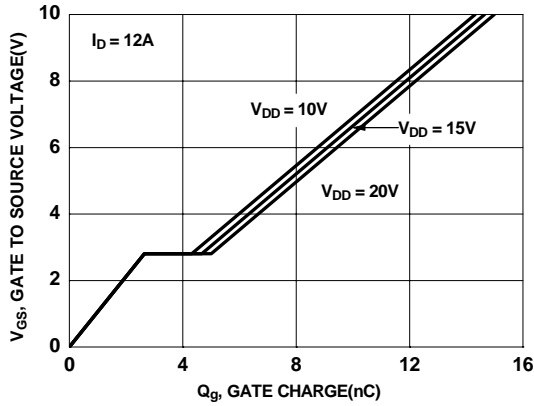
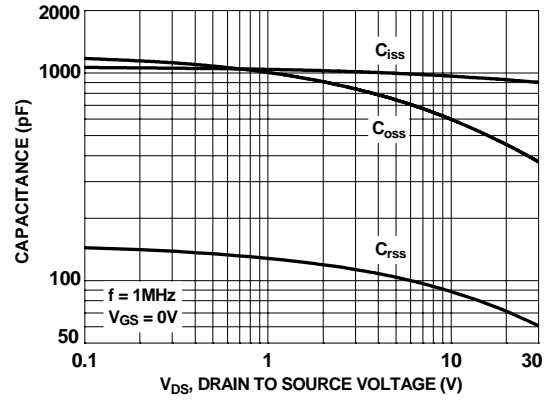


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

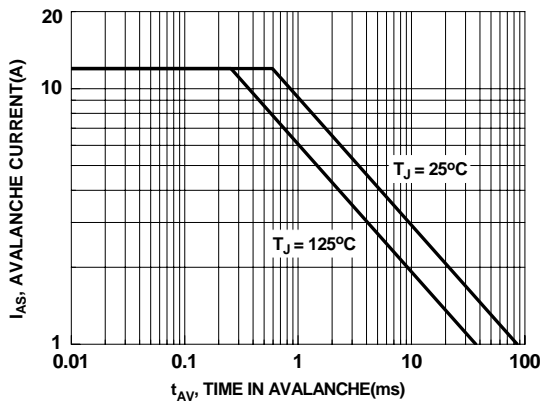
**Typical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted



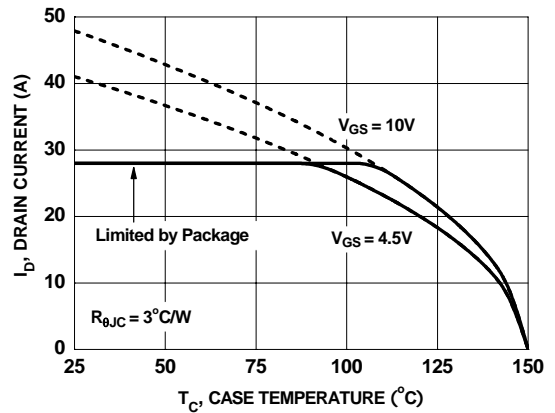
**Figure 7. Gate Charge Characteristics**



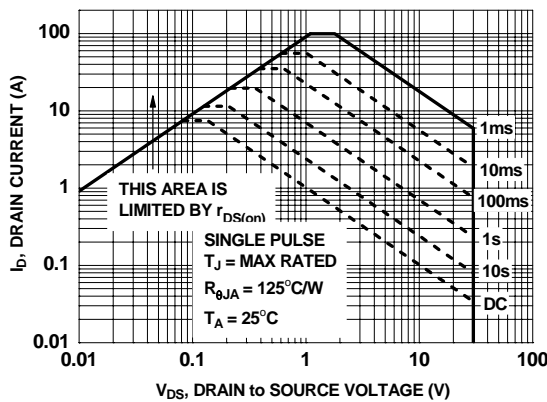
**Figure 8. Capacitance vs Drain to Source Voltage**



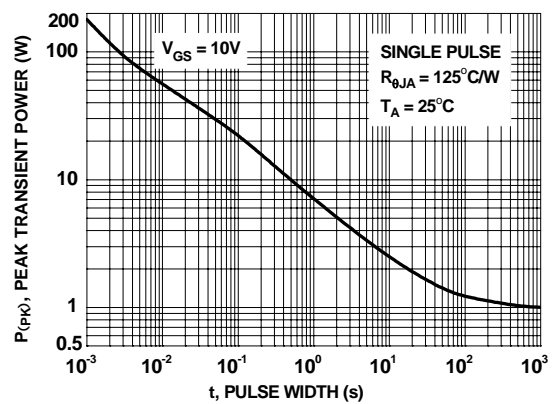
**Figure 9. Unclamped Inductive Switching Capability**



**Figure 10. Maximum Continuous Drain Current vs Case Temperature**

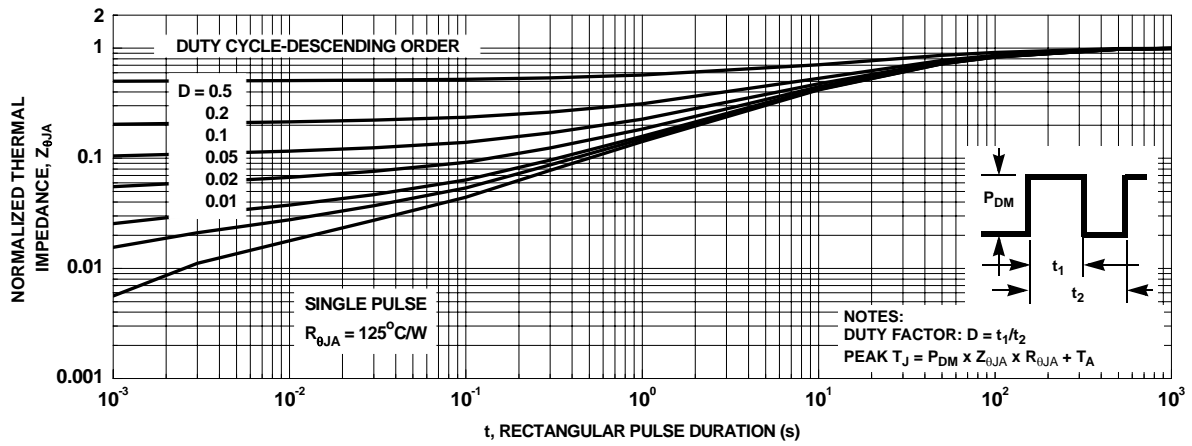


**Figure 11. Forward Bias Safe Operating Area**



**Figure 12. Single Pulse Maximum Power Dissipation**

**Typical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted




**Figure 13. Transient Thermal Response Curve**



## TRADEMARKS

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx <sup>®</sup>	Green FPS <sup>™</sup>	Power247 <sup>®</sup>	SuperSOT <sup>™</sup> -8
Build it Now <sup>™</sup>	Green FPS <sup>™</sup> e-Series <sup>™</sup>	POWEREDGE <sup>®</sup>	SyncFET <sup>™</sup>
CorePLUS <sup>™</sup>	GTO <sup>™</sup>	Power-SPM <sup>™</sup>	The Power Franchise <sup>®</sup>
CROSSVOLT <sup>™</sup>	<i>i-Lo</i> <sup>™</sup>	PowerTrench <sup>®</sup>	<b>power</b> franchise
CTL <sup>™</sup>	IntelliMAX <sup>™</sup>	Programmable Active Droop <sup>™</sup>	TinyBoost <sup>™</sup>
Current Transfer Logic <sup>™</sup>	ISOPLANAR <sup>™</sup>	QFET <sup>®</sup>	TinyBuck <sup>™</sup>
EcoSPARK <sup>®</sup>	MegaBuck <sup>™</sup>	QS <sup>™</sup>	TinyLogic <sup>®</sup>
<b>F</b> <sup>®</sup>	MICROCOUPLER <sup>™</sup>	QT Optoelectronics <sup>™</sup>	TINYOPTO <sup>™</sup>
Fairchild <sup>®</sup>	MicroFET <sup>™</sup>	Quiet Series <sup>™</sup>	TinyPower <sup>™</sup>
Fairchild Semiconductor <sup>®</sup>	MicroPak <sup>™</sup>	RapidConfigure <sup>™</sup>	TinyPWM <sup>™</sup>
FACT Quiet Series <sup>™</sup>	Motion-SPM <sup>™</sup>	SMART START <sup>™</sup>	TinyWire <sup>™</sup>
FACT <sup>®</sup>	OPTOLOGIC <sup>®</sup>	SPM <sup>®</sup>	μSerDes <sup>™</sup>
FAST <sup>®</sup>	OPTOPLANAR <sup>®</sup>	STEALTH <sup>™</sup>	UHC <sup>®</sup>
FastvCore <sup>™</sup>	 <sup>®</sup>	SuperFET <sup>™</sup>	UniFET <sup>™</sup>
FPS <sup>™</sup>	PDP-SPM <sup>™</sup>	SuperSOT <sup>™</sup> -3	VCX <sup>™</sup>
FRFET <sup>®</sup>	Power220 <sup>®</sup>	SuperSOT <sup>™</sup> -6	
Global Power Resource <sup>SM</sup>			

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I30